



THE DATASHEET OF
S558-5500-44





ELECTRICAL CHARACTERISTICS (@25°C)

MEDIA SIDE	TURNS RATIO	1CT : 1CT
• 24 RX1+	CHIP SIDE : MEDIA SIDE	PER DOT CONVENTION
	POLARITY	
	DCL	
• 23 RXCT1	MEDIA SIDE	350µH MIN @100KHz, 100mVrms WITH 8mA DC BIAS
• 22 RX1-	INSERTION LOSS	1.2dB MAX 1.0dB MAX 3.0dB MAX
• 21 TX1+	RETURN LOSS	18dB MIN 12-20LDG<F/80MHZ> dB MIN 10dB MIN
• 20 TXCT1	CROSSSTALK	55dB MIN 55-24LDG<F/10MHZ> dB MIN
• 19 TX1-	DM TD CM REJECTION	60dB MIN 40-22LDG<F/30MHZ> dB MIN
• 18 TX2+	HIPDT : LEAKAGE CURRENT LESS THAN 1mA AT 1500Vrms .	
• 17 TXCT2	DESIGNED FOR 10/100/1000BT PoE APPLICATIONS, REQUIRING OPERATION	
• 16 TX2-	FROM -40 TO 85C, WITH 350mA CURRENT CARRYING CAPACITY.	
• 15 RX2+		
• 14 RXCT2		
• 13 RX2-		

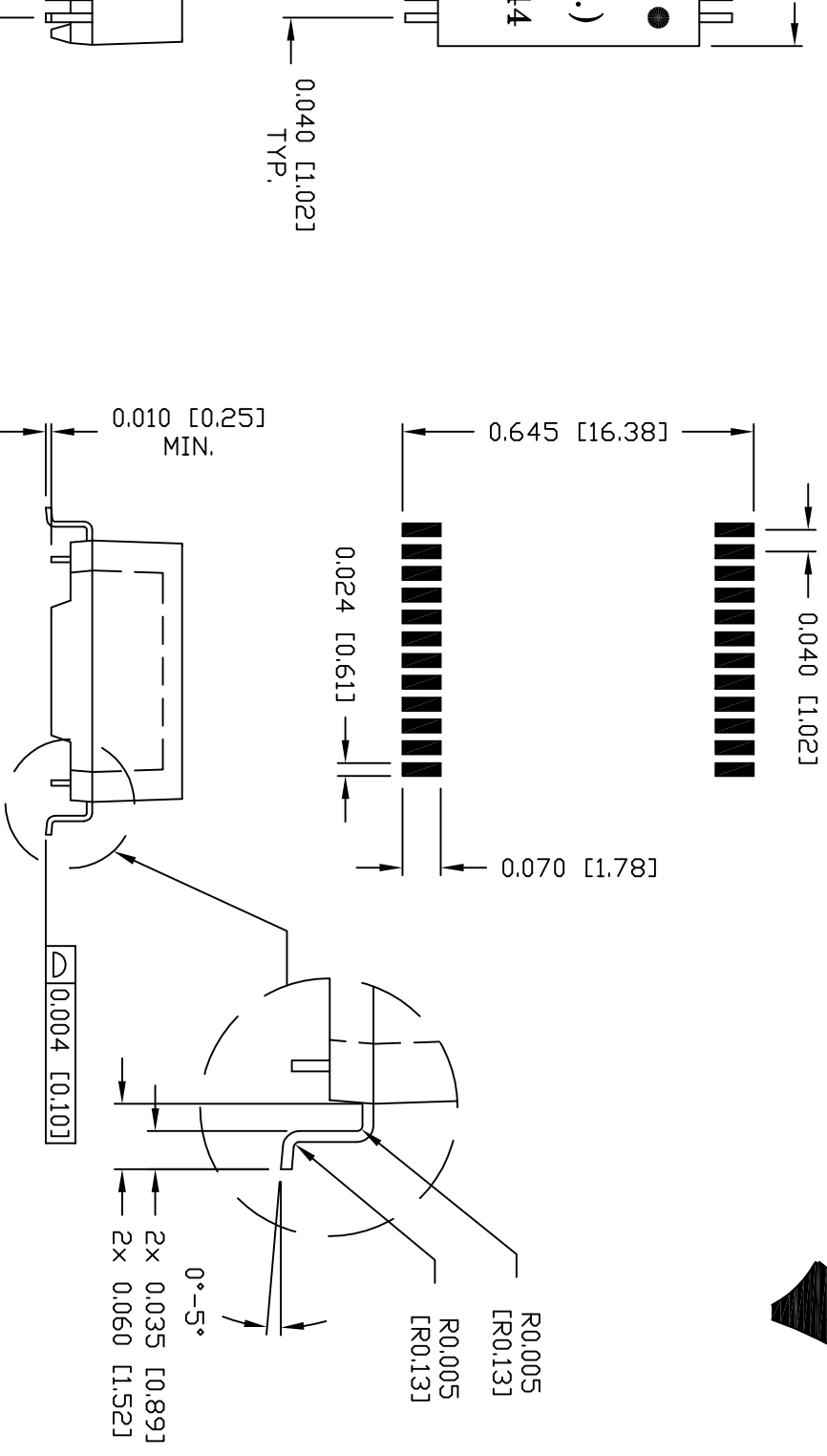
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	TOL. IN INCH	UNIT : INCH [mm]		
FILE NAME S558550044A.DWG	.X	SCALE : N/A		
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	.XXX	SIZE : A4		



POWER | PROTECT | CONNECT



SUGGESTED PCB PAD LAYOUT



HAND-WORK-04.

PART NO. / DRAWING NO. S558-5500-44		STANDARD DIM. [] METRIC DIM. AS REF. TOL. IN INCH	
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.X		SCALE : N/A	
.XX ±0.01		SIZE : A4	
.XXX ±0.005			
REV. : A	PAGE : 3		



NICAL OUTLINE
S58-5500-44

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