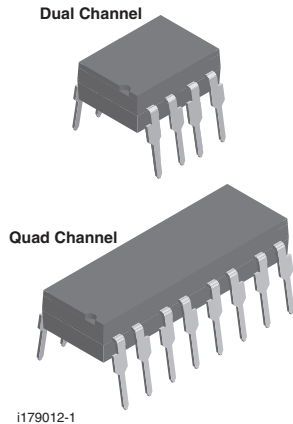




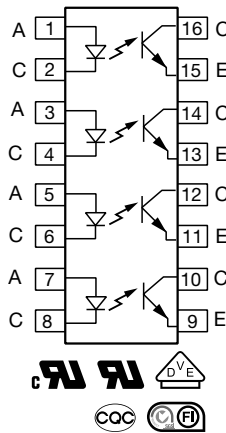
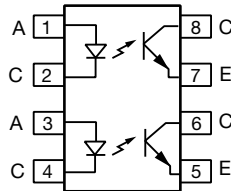
**THE DATASHEET OF
ILQ615-3X009T**



Optocoupler, Phototransistor Output (Dual, Quad Channel)



i179012-1



DESCRIPTION

The ILD615, ILQ615 are multi-channel phototransistor optocouplers that use GaAs IRED emitters and high gain NPN phototransistors. These devices are constructed using over / under leadframe optical coupling and double molded insulation technology resulting a withstand test voltage of 7500 V_{ACPEAK} and a working voltage of 1700 V_{RMS}.

The binned min./max. and linear CTR characteristics make these devices well suited for DC or AC voltage detection. Eliminating the phototransistor base connection provides added electrical noise immunity from the transients found in many industrial control environments.

Because of guaranteed maximum non-saturated and saturated switching characteristics, the ILD615, ILQ615 can be used in medium speed data I/O and control systems. The binned min. / max. CTR specification allow easy worst case interface calculations for both level detection and switching applications. Interfacing with a CMOS logic is enhanced by the guaranteed CTR at I_F = 1 mA.

FEATURES

- Identical channel to channel footprint
- Dual and quad packages feature:
 - Reduced board space
 - Lower pin and parts count
 - Better channel to channel CTR match
 - Improved common mode rejection
- Isolation rated voltage from double molded package, 4420 V_{RMS}
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
COMPLIANT

AGENCY APPROVALS

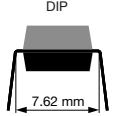
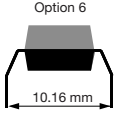
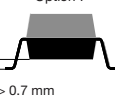
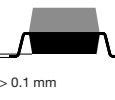
- [UL 1577](#)
- [cUL](#)
- [DIN EN 60747-5-5 \(VDE 0884-5\)](#), available with option 1
- [CQC: GB8898](#)
- [FIMKO](#)

LINKS TO ADDITIONAL RESOURCES


[3D Models](#)

[Design Tools](#)

[Related Documents](#)
SPICE
[Models](#)

ORDERING INFORMATION																					
<table border="1" style="margin: auto;"> <tr> <td style="padding: 2px 5px;">I</td> <td style="padding: 2px 5px;">L</td> <td style="padding: 2px 5px;">x</td> <td style="padding: 2px 5px;">6</td> <td style="padding: 2px 5px;">1</td> <td style="padding: 2px 5px;">5</td> <td style="padding: 2px 5px;">-</td> <td style="padding: 2px 5px;">#</td> <td style="padding: 2px 5px;">X</td> <td style="padding: 2px 5px;">0</td> <td style="padding: 2px 5px;">#</td> <td style="padding: 2px 5px;">#</td> <td style="padding: 2px 5px;">T</td> </tr> </table> <p style="text-align: center;"> PART NUMBER x = D (Dual) or Q (Quad) </p> <p style="text-align: center;"> CTR BIN PACKAGE OPTION TAPE AND REEL </p>	I	L	x	6	1	5	-	#	X	0	#	#	T								
I	L	x	6	1	5	-	#	X	0	#	#	T									
AGENCY CERTIFIED / PACKAGE	DUAL CHANNEL				QUAD CHANNEL																
	CTR (%)																				
	10 mA																				
UL, CSA	40 to 80	63 to 125	100 to 200	160 to 320	40 to 80	63 to 125	100 to 200	160 to 320													
DIP-8	ILD615-1	ILD615-2	ILD615-3	ILD615-4	-	-	-	-													
DIP-8, 400 mil, option 6	-	-	-	ILD615-4X006	-	-	-	-													
SMD-8, option 7	ILD615-1X007 T ⁽¹⁾	-	-	-	-	-	-	-													
SMD-8, option 9	-	ILD615-2X009T	ILD615-3X009T	ILD615-4X009T	-	-	-	-													
DIP-16	-	-	-	-	ILQ615-1	ILQ615-2	ILQ615-3	ILQ615-4													
SMD-16, option 7	-	-	-	-	-	ILQ615-2X007	ILQ615-3X007T	-													
SMD-16, option 9	-	-	-	-	ILQ615-1X009	-	ILQ615-3X009T ⁽¹⁾	ILQ615-4X009T ⁽¹⁾													
UL, CSA, VDE	40 to 80	63 to 125	100 to 200	160 to 320	40 to 80	63 to 125	100 to 200	160 to 320													
DIP-8	-	ILD615-2X001	-	ILD615-4X001	-	-	-	-													
DIP-8, 400 mil, option 6	-	ILD615-2X016	-	ILD615-4X016	-	-	-	-													
SMD-8, option 7	-	-	ILD615-3X017T ⁽¹⁾	-	-	-	-	-													
DIP-16	-	-	-	-	-	-	-	ILQ615-4X001													
DIP-16, 400 mil, option 6	-	-	-	-	-	-	ILQ615-3X016	-													

Notes

- Additional options may be possible, please contact sales office
- ⁽¹⁾ Also available in tubes; do not add T to end

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Reverse voltage		V _R	6	V
Forward current		I _F	60	mA
Surge current		I _{FSM}	1.5	A
Power dissipation		P _{diss}	100	mW
Derate linearly from 25 °C			1.33	mW/°C
OUTPUT				
Collector emitter breakdown voltage		BV _{CEO}	70	V
Emitter collector breakdown voltage		BV _{ECO}	7	V
Collector current		I _C	50	mA
	t < 1 ms	I _C	100	mA
Power dissipation		P _{diss}	150	mW
Derate linearly from 25 °C			2	mW/°C



ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
COUPLER				
Storage temperature		T_{stg}	-55 to +150	$^{\circ}\text{C}$
Operating temperature		T_{amb}	-55 to +100	$^{\circ}\text{C}$
Junction temperature		T_j	100	$^{\circ}\text{C}$
Soldering temperature ⁽¹⁾	2 mm distance from case bottom	T_{sld}	260	$^{\circ}\text{C}$
Package power dissipation ILD615			400	mW
Derate linearly from 25 $^{\circ}\text{C}$			5.33	mW/ $^{\circ}\text{C}$
Package power dissipation ILQ615			500	mW
Derate linearly from 25 $^{\circ}\text{C}$			6.67	mW/ $^{\circ}\text{C}$
Total power dissipation		P_{tot}	250	mW

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.
- ⁽²⁾ Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Forward voltage	$I_F = 10\text{ mA}$	V_F	1	1.15	1.3	V
Breakdown voltage	$I_R = 10\text{ }\mu\text{A}$	V_{BR}	6	30	-	V
Reverse current	$V_R = 6\text{ V}$	I_R	-	0.01	10	μA
Capacitance	$V_R = 0\text{ V}$, $f = 1\text{ MHz}$	C_O	-	25	-	pF
Thermal resistance, junction to lead		R_{THJL}	-	750	-	K/W
OUTPUT						
Collector emitter capacitance	$V_{CE} = 5\text{ V}$, $f = 1\text{ MHz}$	C_{CE}	-	6.8	-	pF
Collector emitter leakage current, -1, -2	$V_{CE} = 10\text{ V}$	I_{CEO}	-	2	50	nA
Collector emitter leakage current, -3, -4	$V_{CE} = 10\text{ V}$	I_{CEO}	-	5	100	nA
Collector emitter breakdown voltage	$I_{CE} = 0.5\text{ mA}$	BV_{CEO}	70	-	-	V
Emitter collector breakdown voltage	$I_E = 0.1\text{ mA}$	BV_{ECO}	7	-	-	V
Thermal resistance, junction to lead		R_{THJL}	-	500	-	K/W
PACKAGE TRANSFER CHARACTERISTICS						
Channel/channel CTR match	$I_F = 10\text{ mA}$, $V_{CE} = 5\text{ V}$	CTR/CTRY	1 to 1	-	2 to 1	
COUPLER						
Capacitance (input to output)	$V_{IO} = 0\text{ V}$, $f = 1\text{ MHz}$	C_{IO}	-	0.8	-	pF
Channel to channel isolation			500	-	-	VAC

Note

- Minimum and maximum values are tested requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio (collector emitter saturated)	$I_F = 10\text{ mA}$, $V_{CE} = 0.4\text{ V}$	ILD615-1	CTR_{CEsat}	-	25	-	%
		ILQ615-1					
		ILD615-2	CTR_{CEsat}	-	40	-	%
		ILQ615-2					
		ILD615-3	CTR_{CEsat}	-	60	-	%
		ILQ615-3					
		ILD615-4	CTR_{CEsat}	-	100	-	%
ILQ615-4							
Current transfer ratio (collector emitter)	$I_F = 1\text{ mA}$, $V_{CE} = 5\text{ V}$	ILD615-1	CTR_{CE}	13	30	-	%
		ILQ615-1					
		ILD615-2	CTR_{CE}	22	45	-	%
		ILQ615-2					
		ILD615-3	CTR_{CE}	34	70	-	%
		ILQ615-3					
		ILD615-4	CTR_{CE}	56	90	-	%
	ILQ615-4						
	$I_F = 10\text{ mA}$, $V_{CE} = 5\text{ V}$	ILD615-1	CTR_{CE}	40	60	80	%
		ILQ615-1					
		ILD615-2	CTR_{CE}	63	80	125	%
		ILQ615-2					
		ILD615-3	CTR_{CE}	100	150	200	%
		ILQ615-3					
ILD615-4		CTR_{CE}	160	200	320	%	
ILQ615-4							

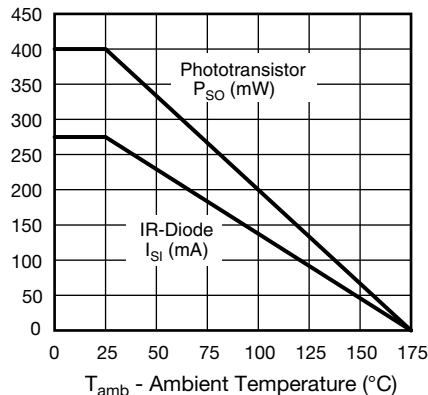


Fig. 1 - Derating Diagram

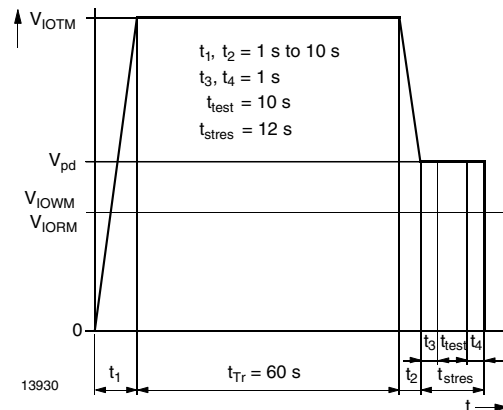


Fig. 2 - Test Pulse Diagram for Sample Test According to DIN EN 60747-5-2 (VDE0884); IEC60747-5-5



SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
NON-SATURATED							
Current	V _{CC} = 5 V, R _L = 75 Ω, 50 % of V _{PP}		I _F	-	10	-	mA
Turn-on time	V _{CC} = 5 V, R _L = 75 Ω, 50 % of V _{PP}		t _{on}	-	3	-	μs
Rise time	V _{CC} = 5 V, R _L = 75 Ω, 50 % of V _{PP}		t _r	-	2	-	μs
Turn-off time	V _{CC} = 5 V, R _L = 75 Ω, 50 % of V _{PP}		t _{off}	-	2.3	-	μs
Fall time	V _{CC} = 5 V, R _L = 75 Ω, 50 % of V _{PP}		t _f	-	2	-	μs
Propagation H to L	V _{CC} = 5 V, R _L = 75 Ω, 50 % of V _{PP}		t _{PHL}	-	1.1	-	μs
Propagation L to H	V _{CC} = 5 V, R _L = 75 Ω, 50 % of V _{PP}		t _{PLH}	-	2.5	-	μs
SATURATED							
Current	V _{CC} = 5 V, R _L = 1 kΩ, V _{TH} = 1.5 V	ILD615-1	I _F	-	20	-	mA
		ILQ615-1					
		ILD615-2	I _F	-	10	-	mA
		ILQ615-2					
		ILD615-3	I _F	-	10	-	mA
		ILQ615-3					
ILD615-4	I _F	-	5	-	mA		
ILQ615-4							
Turn-on time	V _{CC} = 5 V, R _L = 1 kΩ, V _{TH} = 1.5 V	ILD615-1	t _{on}	-	3	-	μs
		ILQ615-1					
		ILD615-2	t _{on}	-	4.3	-	μs
		ILQ615-2					
		ILD615-3	t _{on}	-	4.3	-	μs
		ILQ615-3					
ILD615-4	t _{on}	-	6	-	μs		
ILQ615-4							
Rise time	V _{CC} = 5 V, R _L = 1 kΩ, V _{TH} = 1.5 V	ILD615-1	t _r	-	2	-	μs
		ILQ615-1					
		ILD615-2	t _r	-	2.8	-	μs
		ILQ615-2					
		ILD615-3	t _r	-	2.8	-	μs
		ILQ615-3					
ILD615-4	t _r	-	4.6	-	μs		
ILQ615-4							
Turn-off time	V _{CC} = 5 V, R _L = 1 kΩ, V _{TH} = 1.5 V	ILD615-1	t _{off}	-	18	-	μs
		ILQ615-1					
		ILD615-2	t _{off}	-	25	-	μs
		ILQ615-2					
		ILD615-3	t _{off}	-	25	-	μs
		ILQ615-3					
ILD615-4	t _{off}	-	25	-	μs		
ILQ615-4							
Fall time	V _{CC} = 5 V, R _L = 1 kΩ, V _{TH} = 1.5 V	ILD615-1	t _f	-	11	-	μs
		ILQ615-1					
		ILD615-2	t _f	-	14	-	μs
		ILQ615-2					
		ILD615-3	t _f	-	14	-	μs
		ILQ615-3					
ILD615-4	t _f	-	15	-	μs		
ILQ615-4							



SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
SATURATED							
Propagation H to L	V _{CC} = 5 V, R _L = 1 kΩ, V _{TH} = 1.5 V	ILD615-1	t _{PHL}	-	1.6	-	μs
		ILQ615-1					
		ILD615-2	t _{PHL}	-	2.6	-	μs
		ILQ615-2					
		ILD615-3	t _{PHL}	-	2.6	-	μs
		ILQ615-3					
ILD615-4	t _{PHL}	-	5.4	-	μs		
ILQ615-4							
Propagation L to H	V _{CC} = 5 V, R _L = 1 kΩ, V _{TH} = 1.5 V	ILD615-1	t _{PLH}	-	8.6	-	μs
		ILQ615-1					
		ILD615-2	t _{PLH}	-	7.2	-	μs
		ILQ615-2					
		ILD615-3	t _{PLH}	-	7.2	-	μs
		ILQ615-3					
		ILD615-4	t _{PLH}	-	7.4	-	μs
		ILQ615-4					

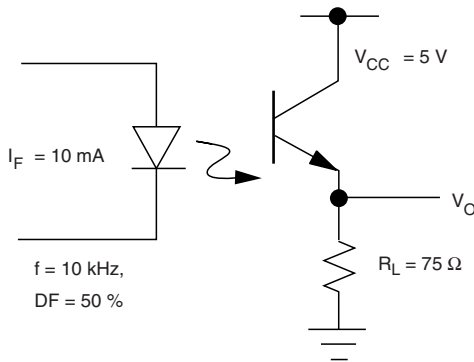
COMMON MODE TRANSIENT IMMUNITY (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Common mode rejection output high	V _{CM} = 50 V _{P-P} , R _L = 1 kΩ, I _F = 0 mA	CM _H	-	5000	-	V/μs
Common mode rejection output low	V _{CM} = 50 V _{P-P} , R _L = 1 kΩ, I _F = 0 mA	CM _L	-	5000	-	V/μs
Common mode coupling capacitance		C _{CM}	-	0.01	-	pF

SAFETY AND INSULATION RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		55 / 100 / 21	
Comparative tracking index		CTI	175	
Maximum rated withstanding isolation voltage	t = 1 min	V _{ISO}	4420	V _{RMS}
Maximum transient isolation voltage		V _{IOTM}	10 000	V _{peak}
Maximum repetitive peak isolation voltage		V _{IORM}	890	V _{peak}
Isolation resistance	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω
	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω
	V _{IO} = 500 V, T _{amb} = 175 °C (construction test only)	R _{IO}	≥ 10 ⁹	Ω
Output safety power		P _{SO}	400	mW
Input safety current		I _{SI}	275	mA
Safety temperature		T _S	175	°C
Creepage distance			≥ 7	mm
Clearance distance			≥ 7	mm
Insulation thickness		DTI	≥ 0.4	mm
Partial discharge test voltage - routine test	100 %, t _{test} = 1 s	V _{pd}	1.669	kV _{peak}
Partial discharge test voltage - lot test (sample test)	t _{Tr} = 60 s, t _{test} = 10 s, (see Fig. 2)	V _{pd}	1.424	kV _{peak}

Note

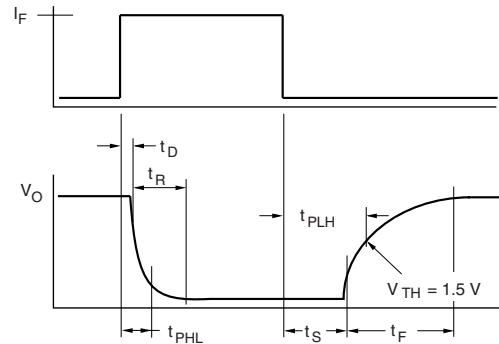
- As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for “safe electrical insulation” only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

TYPICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)



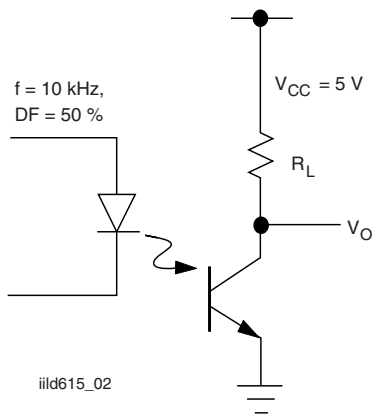
iiid615_01

Fig. 1 Non-Saturated Switching Timing



iiid615_04

Fig. 5 - Saturated Switching Timing



iiid615_02

Fig. 3 - Saturated Switching Timing

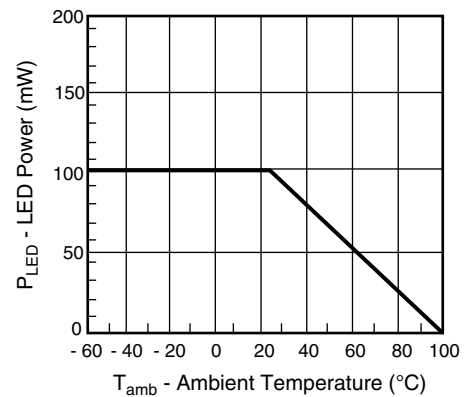
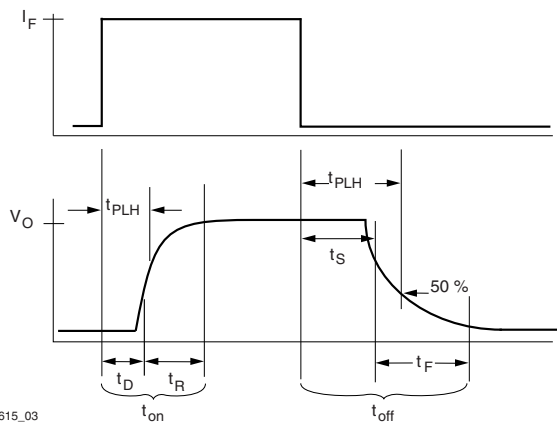
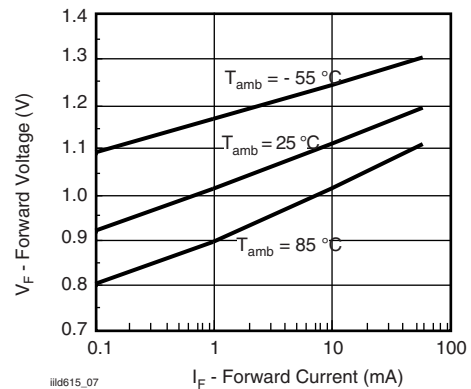


Fig. 6 - Maximum LED Power Dissipation



iiid615_03

Fig. 4 - Non-Saturated Switching Timing



iiid615_07

Fig. 7 - Forward Voltage vs. Forward Current

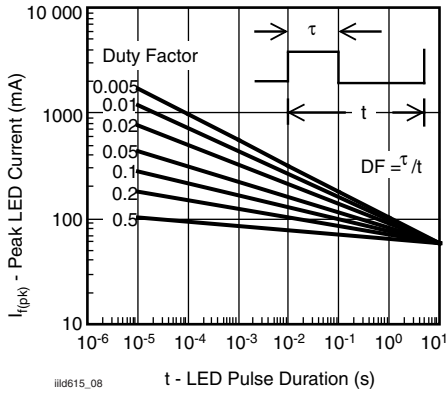


Fig. 8 - Peak LED Current vs. Pulse Duration, τ

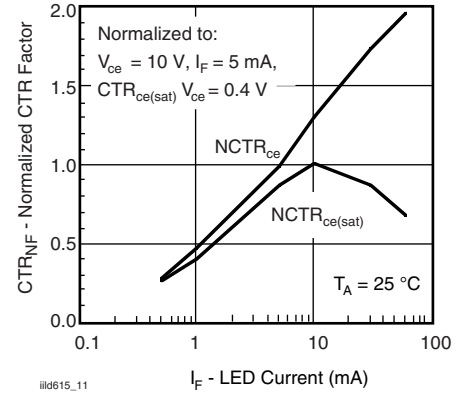


Fig. 11 - Normalization Factor for Non-Saturated and Saturated CTR vs. I_F

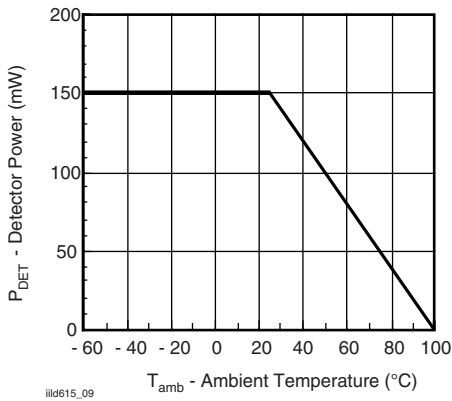


Fig. 9 - Maximum Detector Power Dissipation

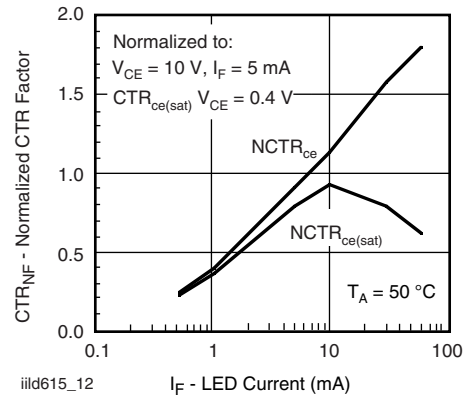


Fig. 12 - Normalization Factor for Non-Saturated and Saturated CTR vs. I_F

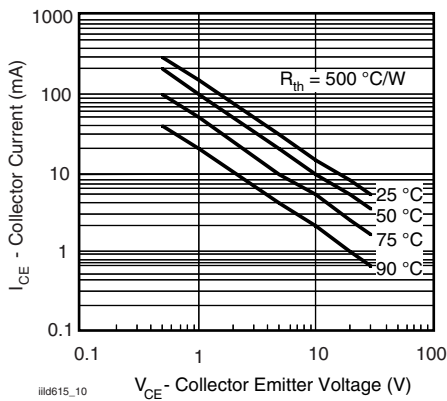


Fig. 10 - Maximum Collector Current vs. Collector Voltage

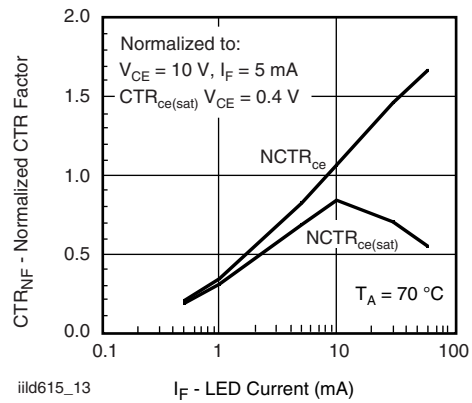


Fig. 13 - Normalization Factor for Non-Saturated and Saturated CTR vs. I_F

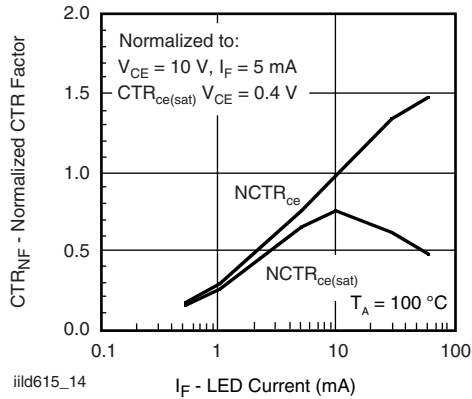


Fig. 14 - Normalization Factor for Non-Saturated and Saturated CTR vs. I_F

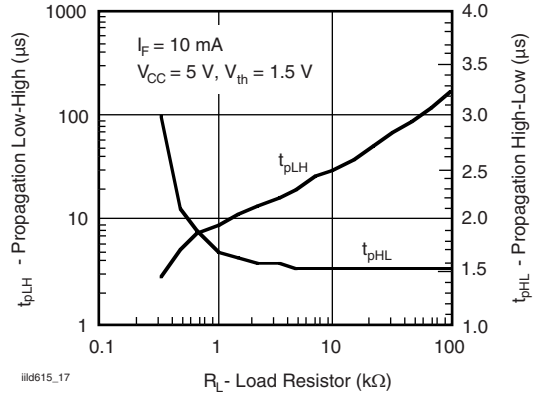


Fig. 17 - -1, Propagation Delay vs. Collector Load Resistor

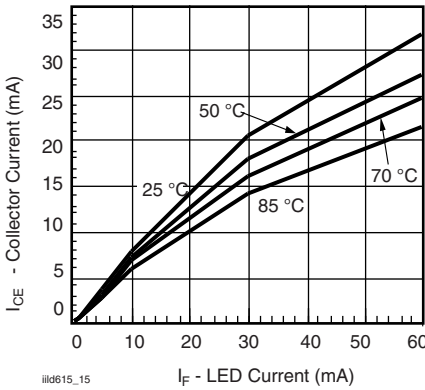


Fig. 15 - Collector Emitter Current vs. Temperature and LED Current

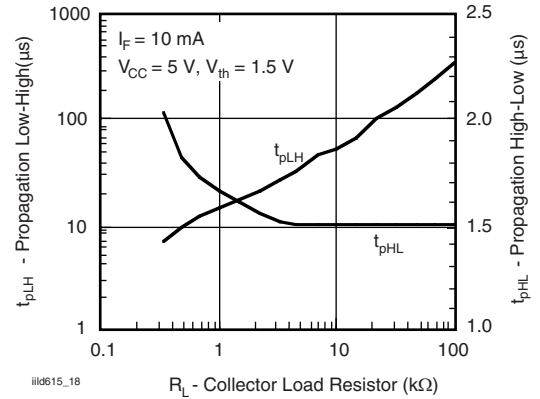


Fig. 18 - -2, -3, Propagation Delay vs. Collector Load Resistor

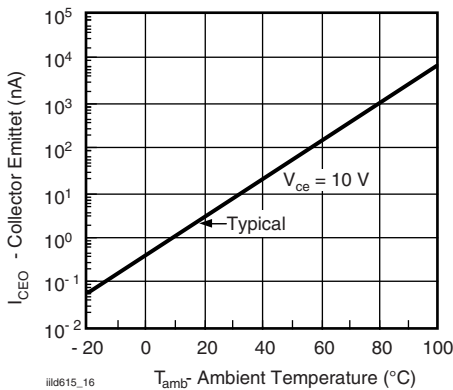


Fig. 16 - Collector Emitter Leakage vs. Temperature

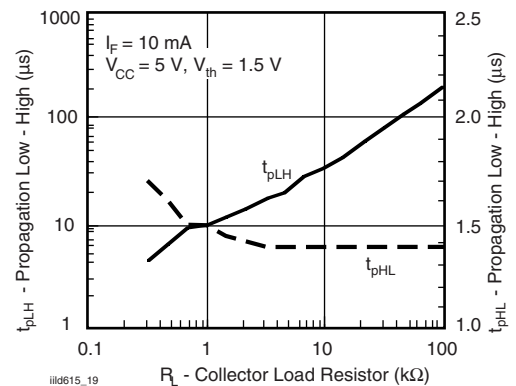
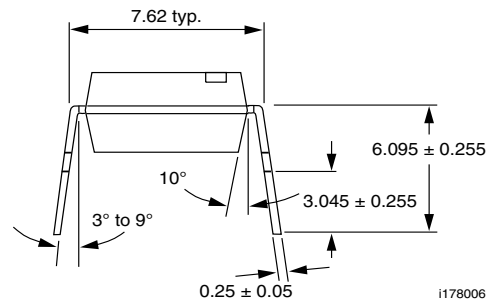
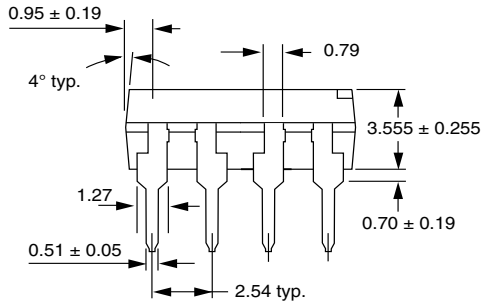
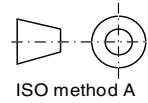
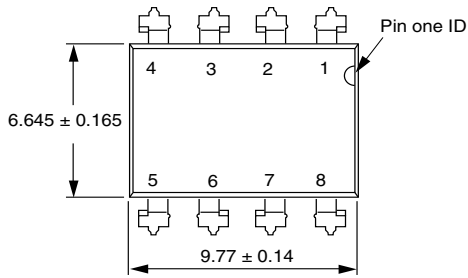


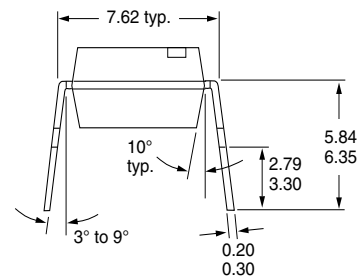
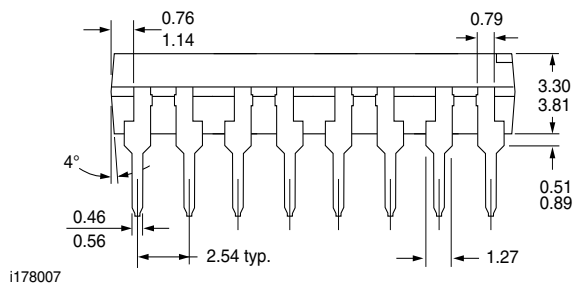
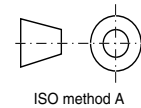
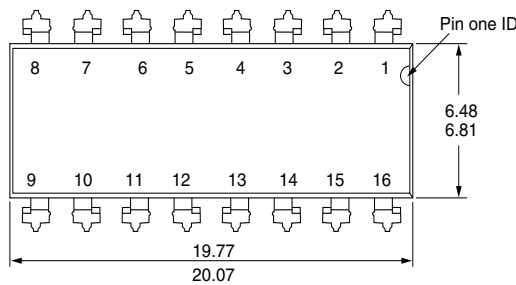
Fig. 19 - -4, Propagation Delay vs. Collector Load Resistor



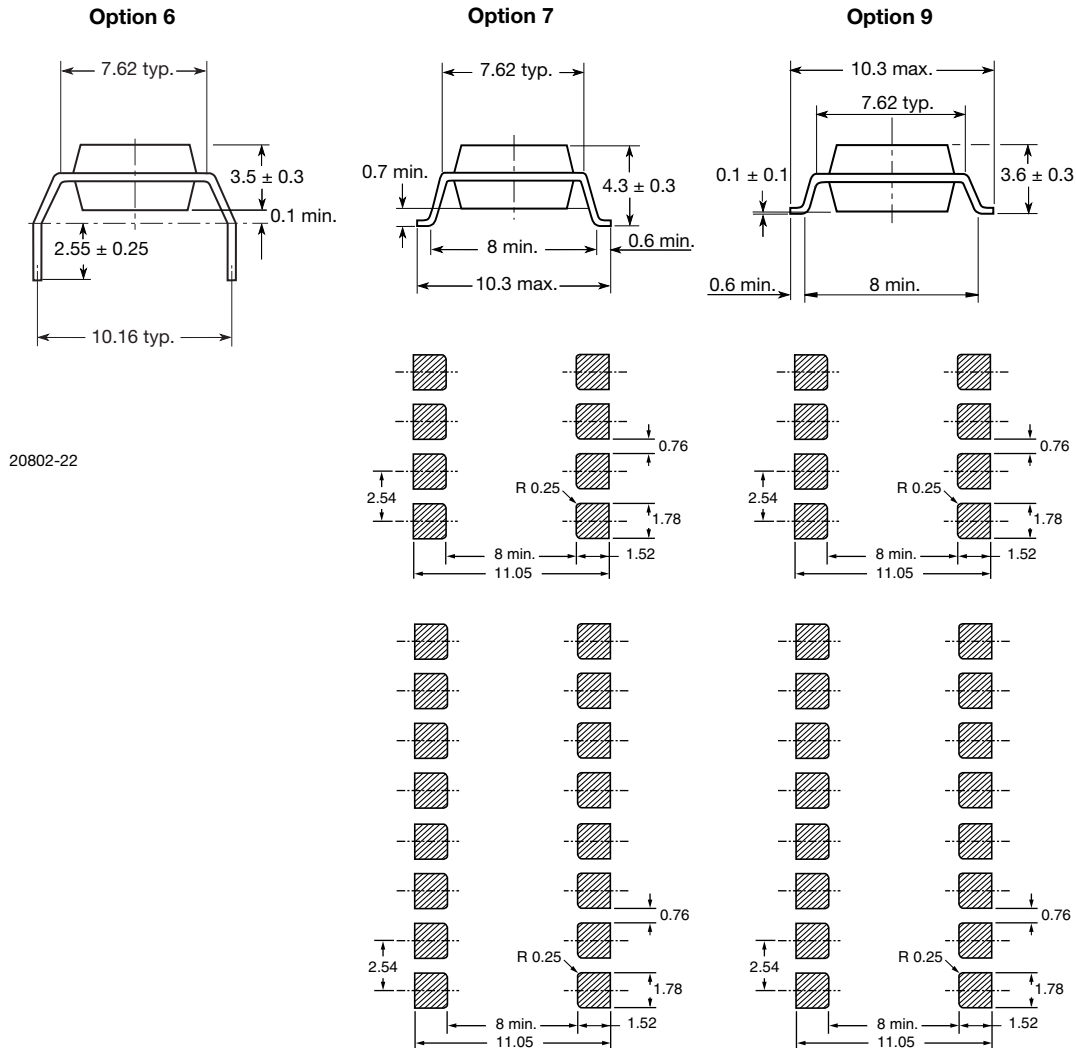
PACKAGE DIMENSIONS in millimeters



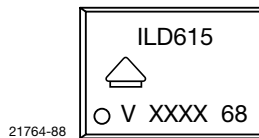
i178006



i178007



PACKAGE MARKING (example)



Notes

- XXXX = LMC (lot marking code)
- Only options 1 and 7 reflected in the package marking
- The VDE Logo is only marked on option1 parts
- Tape and reel suffix (T) is not part of the package marking



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