

0.65V to 5.5V, 1ch Ultra Low Dropout Linear Regulator Controller BD3504FVM

General Description

BD3504FVM is an ultra-low dropout linear regulator controller for chipset that can achieve ultra-low input to output voltage. By using N-MOSFET for external power transistor, the controller can be used at ultra-low I/O voltage difference as low as the voltage generated by ON-Resistance. By reducing the I/O voltage difference, large output current is achieved and conversion loss can be reduced, thus the controller can be used in replacement of a switching power supply. In addition, downsizing and cost reduction of the set can be achieved as suitable power transistor can be selected depending on the output current. BD3504FVM does not need any choke coil, diode for rectification and power transistor which are all necessary in a switching power supply, thus reduced total cost and compact size can be achieved for the set. Using external resistors, output voltage ranging from 0.65V to 2.5V can be selected. It is also possible to meet the power supply sequence of the set since output voltage start-up time can be adjusted by using the NRCS terminal.

Features

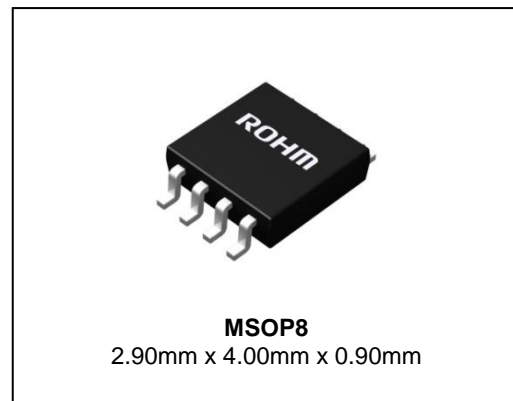
- Reduced rush current by NRCS
- Built-in driver for external Nch transistor
- Built-in timer latch short protection circuit
- Built-in under voltage lock out circuit
- Output voltage variable type
- Built-in thermal shutdown circuit

Key Specifications

- | | |
|------------------------------------|-----------------|
| ■ Drain Voltage Range: | 0.65V to 5.5V |
| ■ Supply Voltage Range: | 4.5V to 5.5V |
| ■ Output Voltage Range: | 0.65V to 2.5V |
| ■ External FET GATE Drive Current: | ±3mA(Typ) |
| ■ Standby Current: | 0μA (Typ) |
| ■ Operating Temperature Range: | -10°C to +100°C |

Package

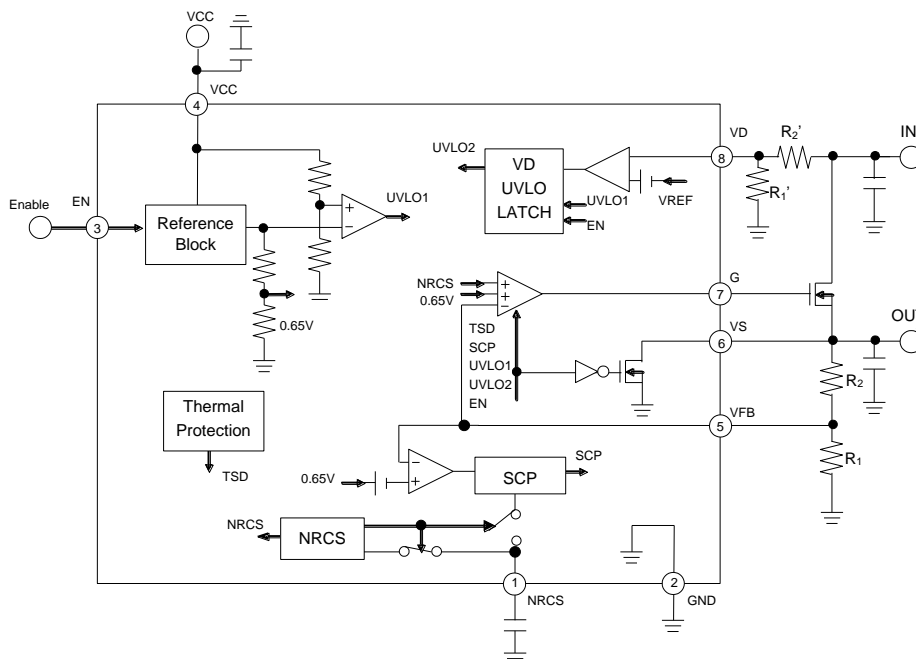
W(Typ) x D(Typ) x H(Max)



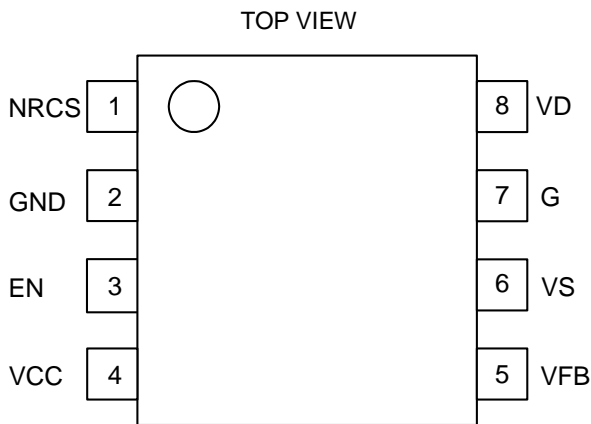
Applications

Mobile PC, desktop PC, digital home appliances

Typical Application Circuit and Block Diagram



Pin Configuration



Pin Descriptions

Pin No.	Pin Name	Pin Function
1	NRCS	NRCS (Non Rush Current on Start up) time setup. Timer latch setup for Short Circuit Protection operating time set up Pin.
2	GND	Ground pin
3	EN	Enable pin
4	VCC	Power source
5	VFB	Output voltage feedback
6	VS	Source voltage pin
7	G	MOSFET driver output
8	VD	Drain voltage sense

Description of Operations

- VCC**
 BD3504FVM has an independent power input pin for the internal circuit operation of the IC. This is used to bias the IC internal circuit and external N-Channel MOSFET. The voltage used for VCC terminal is 5.0V and maximum current is 1.7mA.
 It is recommended to connect a bypass capacitor with a value of 1µF or more to VCC pin.

- EN**
 With an input of 2.0V or higher, the EN terminal turns to “High” level and OUT is produced. At 0.8V or lower, it detects “Low” level and OUT is turned OFF. Simultaneously, the discharge circuit inside the VS terminal is activated and output voltage is reduced (150 mA (Min) when $V_{VS}=1V$ and $V_{EN}=0V$).

- VD**
 The VD terminal is the drain voltage detection terminal of the external N-Channel MOSFET. When drain voltage (IN) is low, output voltage is turned OFF to prevent under voltage lock out. The reset voltage (V_{DUVLO}) of under voltage lock out circuit is determined by the following equation:

$$V_{DUVLO} = V_{IN} \times 0.7 \times \left(\frac{R_1' + R_2'}{R_1'} \right)$$

At low-input drain voltage, when UVLO resistors have resistance values same as output voltage resistors ($R_1 = R_1'$, $R_2 = R_2'$), under voltage lock out (UVLO) is reset when drain voltage (IN) reaches 70% of the output voltage. UVLO detects only at the startup of the EN terminal.

- VFB**
 The VFB terminal is use to decide the output voltage and is determined by the following equation:

$$V_{OUT} = V_{VFB} \times \left(\frac{R_1 + R_2}{R_1} \right)$$

where:
 V_{VFB} is controlled to achieve 0.65 V (typ).

- NRCS terminal**
 The NRCS terminal is a constant current output terminal, and operates as

 - Soft-Start ... during start-up
 - SCP-Delay ... after start-up

How to set Soft-Start of NRCS terminal

The output voltage startup time (t_{NRCS}) is determined by the time when the NRCS terminal reaches V_{VFB} (0.65V). During start-up, the NRCS terminal serves as a constant current source (I_{NRCS}) of 20 µA (Typ) output, and charges externally connected capacitor (C_{NRCS}). Output voltage (V_{OUT}) becomes stable when NRCS terminal reaches the internal reference voltage (0.65V).

How to set NRCS terminal short protection Delay

BD3504FVM has short circuit protection (SCP) which is activated when output voltage becomes $V_{OUT} \times 0.35$ (typ) or lower. The time when SCP is activated until latch takes place (t_{SCP}) is determined by the following equation:

$$t_{SCP} = C_{NRCS} \times V_{OSCP} \div I_{SCP}$$

When SCP is activated, the NRCS terminal provides 20 µA (typ) constant current output (I_{SCP}), and charges the externally connected capacitor (C_{NRCS}). When the NRCS terminal reaches 1.3V (V_{OSCP}), latch operation is carried out and output voltage is turned OFF.

Description of Operations – continued

- 6. VS
VS terminal is a source voltage detection terminal of the external N-Channel MOSFET. VFB/VS terminal has the internal discharge circuit activated to lower output voltage when EN changes to Low level or various protection circuits (TSD, SCP, UVLO) are activated.
- 7. G
G terminal is the gate drive terminal of the external N-Channel MOSFET. Because the output voltage range of G terminal is up to 5V (VCC), it is necessary to use N-Channel MOSFET whose threshold is lower than “5V - V_{OUT}”. In addition, by including an RC snubber circuit to the G terminal, phase margin of loop gain can be increased and ceramic capacitors can be used for the terminal.

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Unit
Supply Voltage	V _{CC}	7 (Note 1)	V
Drain Voltage	V _{VD}	7	V
Enable Input Voltage	V _{EN}	7	V
Power Dissipation	P _d	0.43 (Note 2)	W
Operating Temperature Range	Topr	-10 to +100	°C
Storage Temperature Range	Tstg	-55 to +150	°C
Maximum Junction Temperature	Tjmax	+150	°C

(Note 1) However, not exceeding P_d.

(Note 2) To use at temperature above Ta=25°C, derate by 3.5mW/°C

Caution: Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.

Recommended Operating Conditions

Parameter	Symbol	Rating		Unit
		Min	Max	
Supply Voltage	V _{CC}	4.5	5.5	V
Drain Voltage	V _{VD}	0.65	5.5	V
Enable Input Voltage	V _{EN}	-0.3	+5.5	V
Capacitor in NRCS Pin	C _{NRCS}	0.001	1	μF
Output Voltage	V _{OUT}	0.65	2.5	V

Electrical Characteristics

(Unless otherwise specified, Ta=25°C V_{CC}=5V V_{IN}=3.3V V_{EN}=3V R₁=R₁'=∞Ω, R₂=R₂'=0Ω)

Parameter	Symbol	Standard Value			Unit	Conditions
		Min	Typ	Max		
Circuit Current	I _{CC}	-	0.85	1.7	mA	
Shut Down Mode Current	I _{ST}	-	0	10	μA	V _{EN} =0V
Feed Back Voltage 1	V _{VFB1}	0.643	0.650	0.657	V	I _{OUT} =50mA
Feed Back Voltage 2	V _{VFB2}	0.630	0.650	0.670	V	V _{CC} =4.5V to 5.5V , Ta=-10°C to +100°C (Note)
Output Voltage	V _{OUT}	-	1.20	-	V	R ₁ =R ₁ '=3.9kΩ, R ₂ =R ₂ '=3.3kΩ
Line Regulation	Reg.I	-	0.1	0.5	%/V	V _{CC} =4.5V to 5.5V
Load Regulation	Reg.L	-	0.5	10	mV	I _{OUT} =0A to 3A
[Enable]						
High Level Enable Input Voltage	V _{ENHI}	2	-	V _{CC}	V	
Low Level Enable Input Voltage	V _{ENLOW}	-0.3	-	+0.8	V	
Enable Pin Input Current	I _{EN}	-	7	10	μA	V _{EN} =3V

(Note) Not 100% tested

Electrical Characteristics- continued

(Unless otherwise specified, Ta=25°C V_{CC}=5V V_{IN}=3.3V V_{EN}=3V R₁=R₁'=∞Ω, R₂=R₂'=0Ω)

Parameter	Symbol	Standard Value			Unit	Conditions
		Min	Typ	Max		
[Voltage Feed Back]						
VFB Input Bias Current	I _{VFB}	-	80	-	nA	
[Source Voltage]						
VS Input Bias Current	I _{SBIAS}	-	1.2	2.4	mA	
VS Standby Current	I _{SSTB}	150	-	-	mA	V _{VS} =1V V _{EN} =0V
[Output MOSFET Driver]						
MOSFET Driver Source Current	I _{GSO}	2	3	4	mA	V _{VFB} =0.6V, V _{GATE} =2.5V
MOSFET Driver Sink Current	I _{GSI}	2	3	4	mA	V _{VFB} =0.7V, V _{GATE} =2.5V
[UVLO]						
VCC UVLO	V _{CCUVLO}	4.20	4.35	4.50	V	VCC: Sweep up
VCC UVLO Hysteresis	V _{CCCHYS}	100	160	220	mV	VCC: Sweep down
VD UVLO	V _{VDUVLO}	V _{OUT} x0.6	V _{OUT} x0.7	V _{OUT} x0.8	V	VD: Sweep up
[Drain Voltage Sensing]						
VD Input Bias Current	I _{VD}	-	0	-	nA	
[NRCS/SCP]						
NRCS Charge Current	I _{NRCS}	14	20	26	μA	V _{NRCS} =0.5V
SCP Charge Current	I _{SCPCH}	14	20	26	μA	V _{NRCS} =0.5V
SCP Discharge Current	I _{SCPDI}	0.3	-	-	mA	V _{NRCS} =0.5V
SCP Threshold Voltage	V _{SCP}	1.2	1.3	1.4	V	
Short Detect Voltage	V _{OSCP}	V _{OUT} x0.3	V _{OUT} x0.35	V _{OUT} x0.4	V	
NRCS Stand-by Voltage	V _{STB}	-	-	50	mV	

Typical Performance Curves

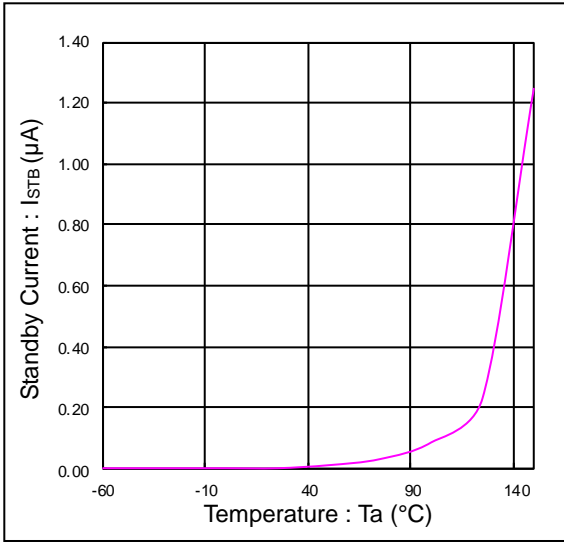


Figure 1. Standby Current vs Temperature

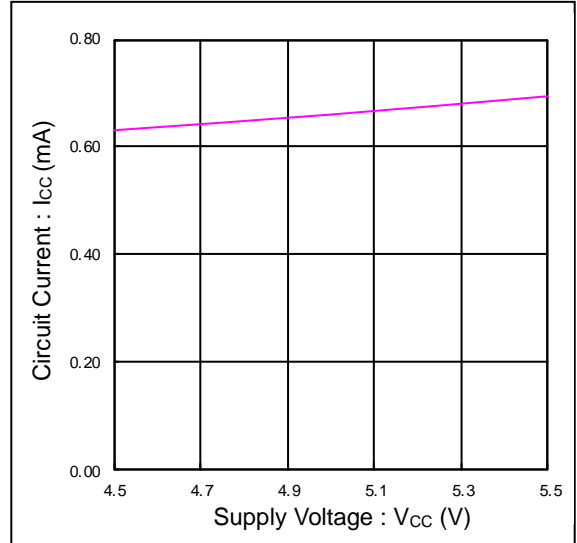


Figure 2. Circuit Current vs Supply Voltage

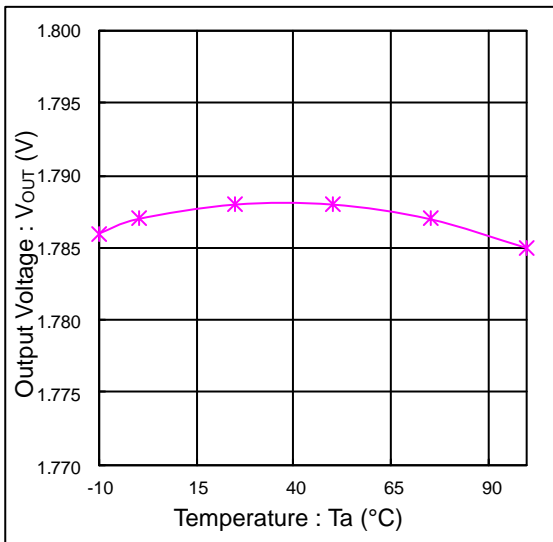


Figure 3. Output Voltage vs Temperature

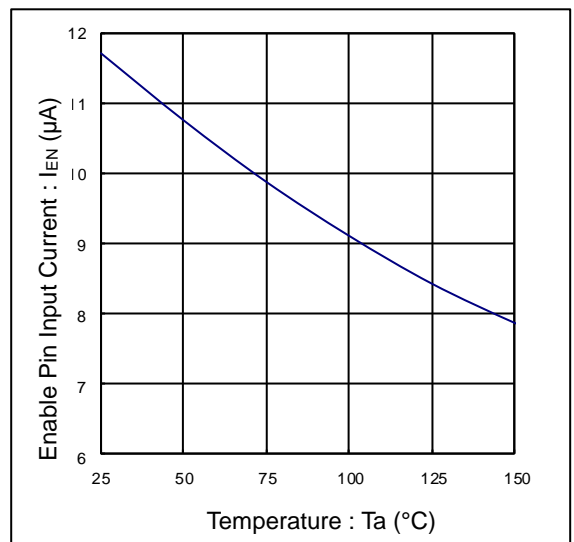


Figure 4. Enable Pin Input Current vs Temperature

Typical Performance Curves – continued

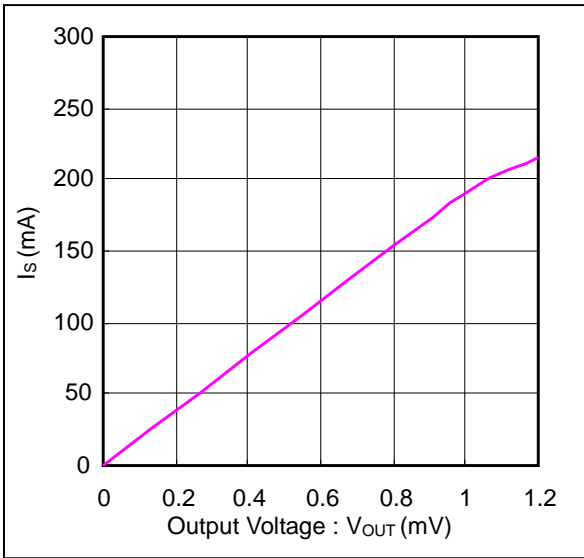


Figure 5. VS Discharge Current vs Output Voltage

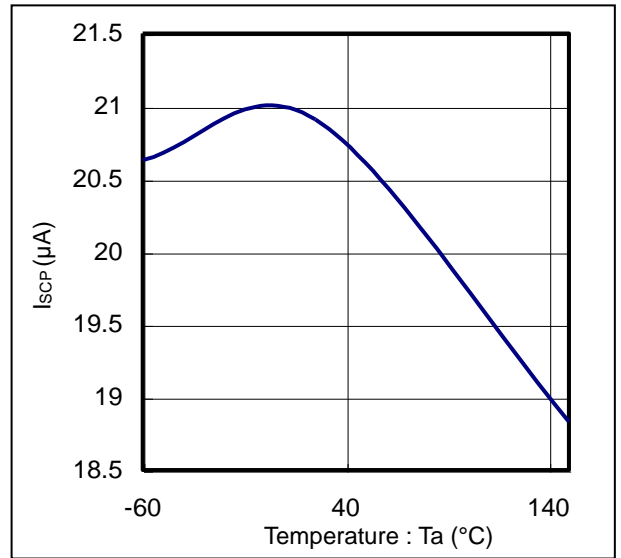


Figure 6. I_{SCP} vs Temperature

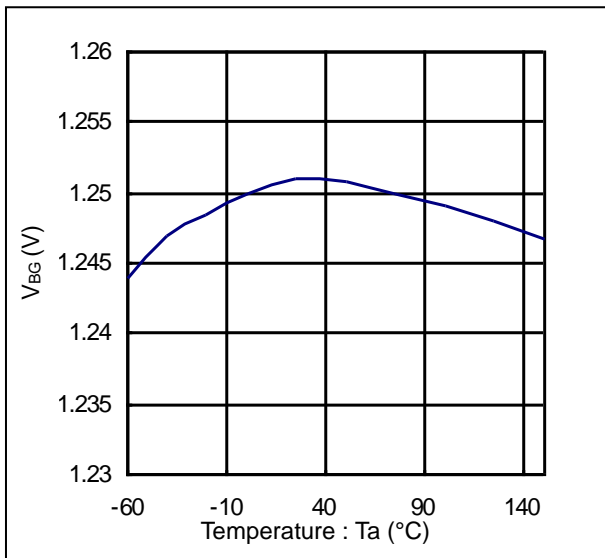


Figure 7. V_{BG} vs Temperature

Typical Waveforms

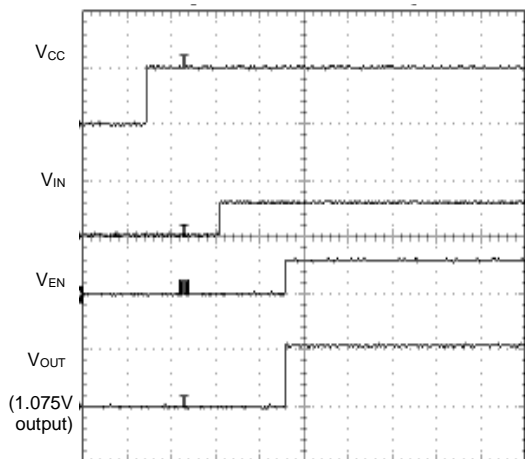


Figure 8. Input Sequence 1 (EN)

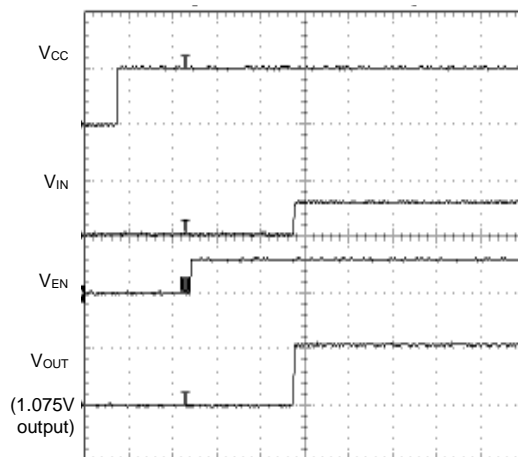


Figure 9. Input Sequence 2 (IN)

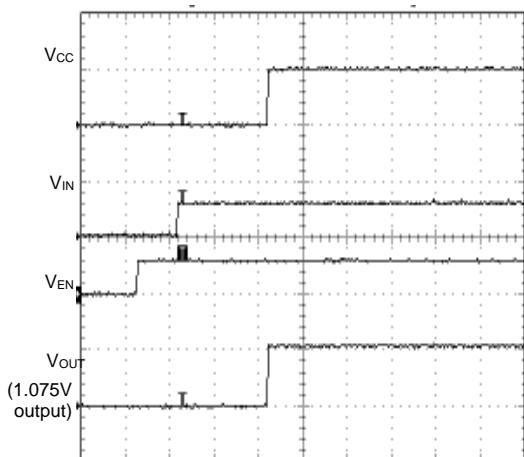


Figure 10. Input Sequence 3 (VCC)

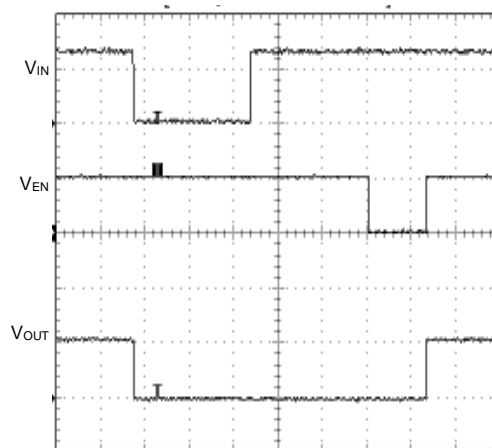


Figure 11. Input Sequence 4

Typical Waveforms – continued

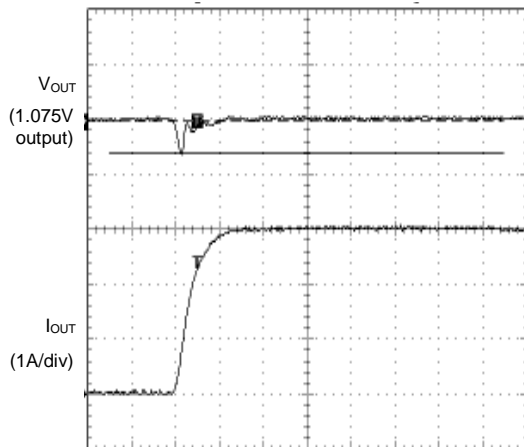


Figure12. Transient Response
(0A to 3A (0.6A/μs) ΔV=30mV)

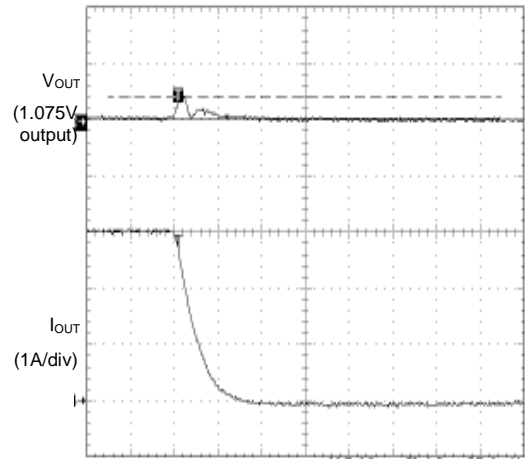
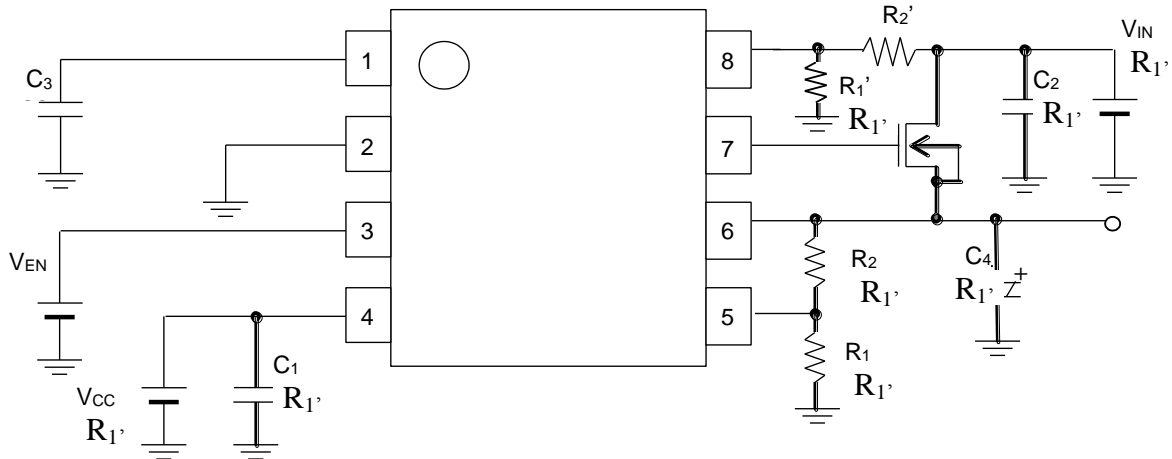


Figure 13. Transient Response
(3A to 0A (0.6A/μs) ΔV=20mV)

Application Information

1. Application Circuit



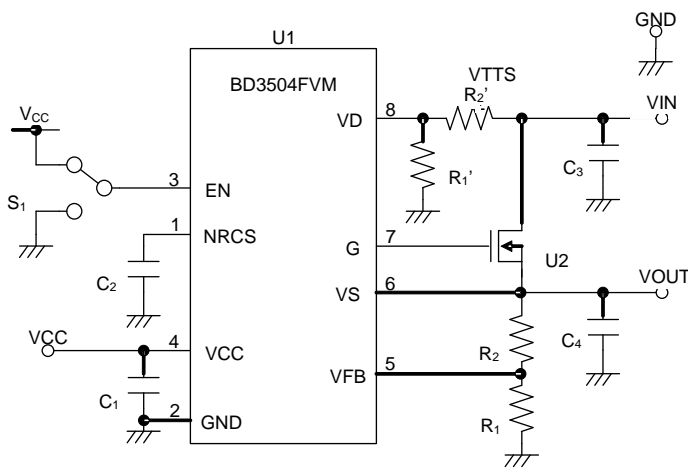
2. Directions for Pattern Layout of PCB

- (1) Because IN input capacitor causes impedance to drop, mount it as close as possible to the IN terminal using thick wiring patterns. When it causes the wire to come in contact with the inner-layer ground plane, multiple through holes connection.
- (2) Because NRCS terminal is analog I/O, be careful of noise. In particular, high-frequency noise of GND may cause IC malfunction through capacitors. It is recommended to connect the GND terminal of NRCS capacitor to the IC GND terminal at a single point.
- (3) The VFB terminal is an output voltage sense line. Effects of wiring impedance can be ignored by sensing the output voltage from the load side, but increased sense wiring causes VFB to be susceptible to noise, in which care must be taken.
- (4) Because the GND terminal is the same GND to be used inside the analog circuit of BD3504FVM, connect it to the inner-layer GND of substrate at a single point using a pattern that is as short as possible. Place a bypass capacitor across VCC and GND as close as possible so that a loop can be minimized.
- (5) The G terminal is the terminal for gate drive. If long wiring will be used, increase the pattern width to lower impedance.
- (6) Heat generated in the output transistor can be calculated by:

$$(V_{IN} - V_{OUT}) \times I_{OUT} (Max)$$
 Design heat generation not to exceed the guaranteed temperature of transistors.
- (7) Connect the output capacitor using thick short wirings so that the impedance is minimized. Connect capacitor GND terminal to the inner-layer GND plane by multiple through holes connection.

3. Evaluation Board

■ BD3504FVM Evaluation Board Circuit

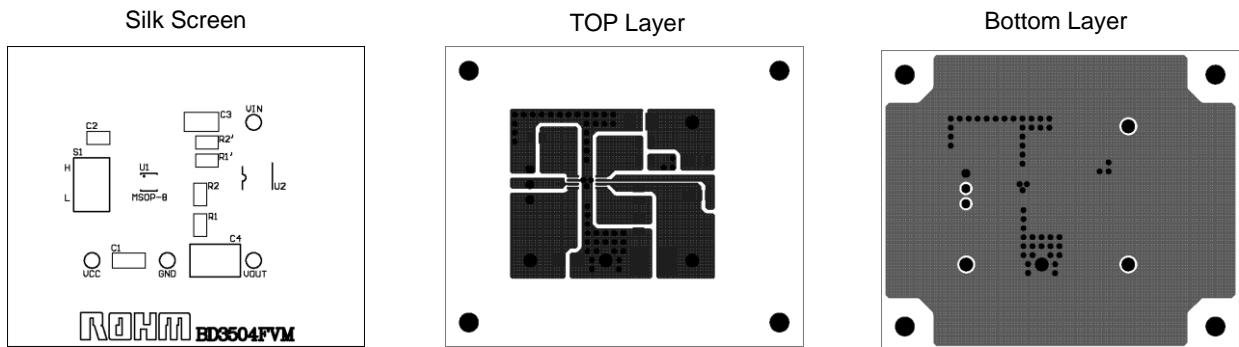


■ BD3504FVM Evaluation Board Application Components

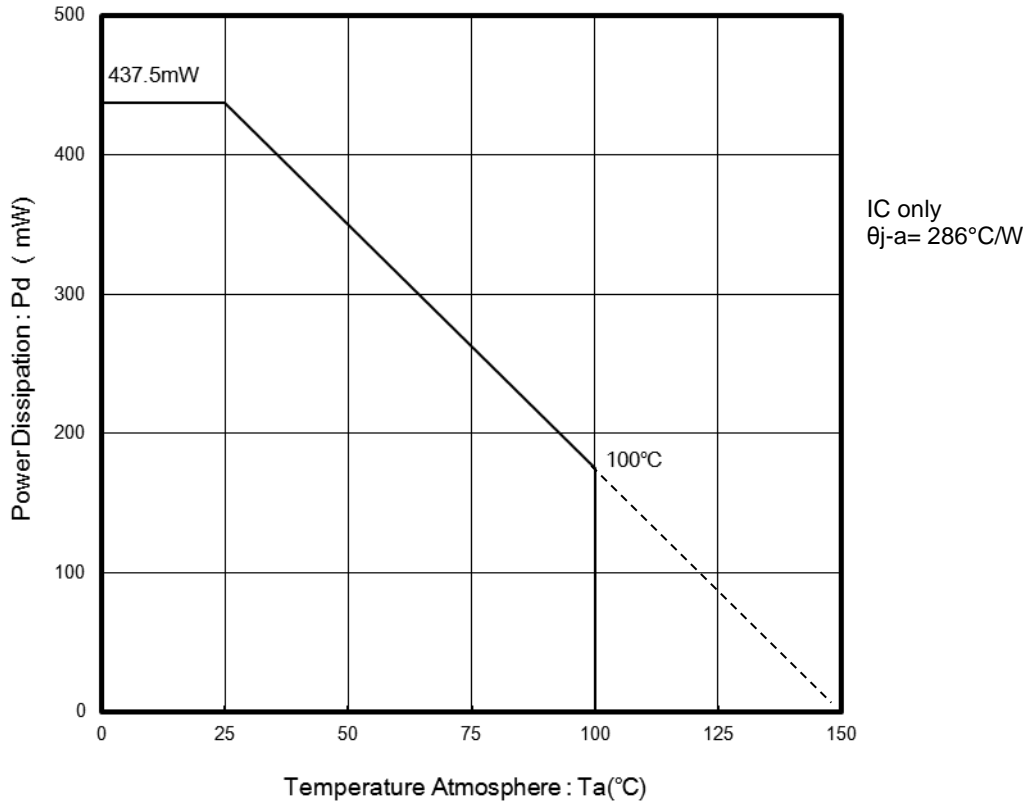
Part No	Value	Company	Parts Name
U1	-	ROHM	BD3504FVM
U2	NMOS	ROHM	RTW060N03
R ₁	3.9K	ROHM	MCR03EZPF3901
R ₁ '	3.3K	ROHM	MCR03EZPF3301
R ₂	3.9K	ROHM	MCR03EZPF3901

R ₂ '	3.3K	ROHM	MCR03EZPF3301
C ₁	1μF	MURATA	GRM18 series
C ₂	0.01μF	MURATA	GRM18 series
C ₃	10μF	MURATA	GRM21 series
C ₄	220μF	SANYO,etc	2R5TPE220MF

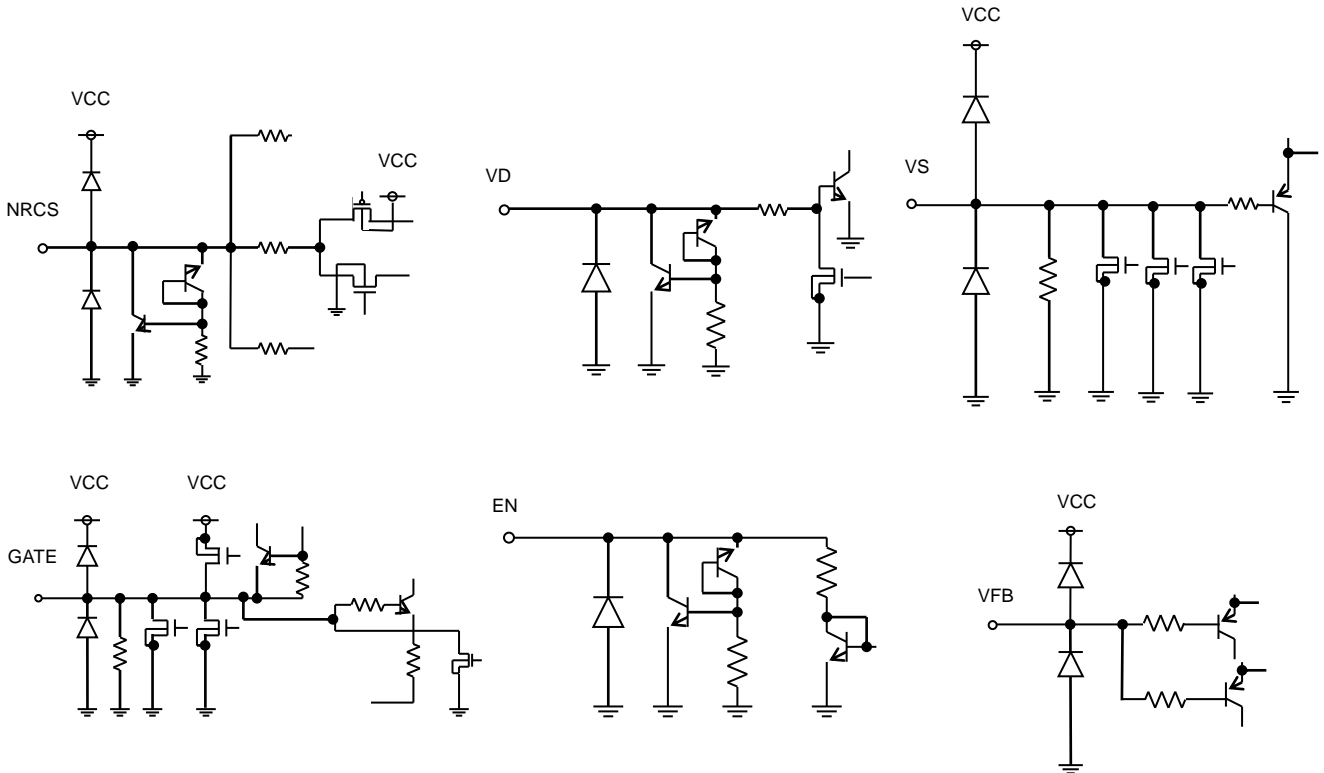
■ BD3504FVM Evaluation Board Layout



Power Dissipation



I/O Equivalent Circuits



Operational Notes

1. Reverse Connection of Power Supply

Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply pins.

2. Power Supply Lines

Design the PCB layout pattern to provide low impedance supply lines. Separate the ground and supply lines of the digital and analog blocks to prevent noise in the ground and supply lines of the digital block from affecting the analog block. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

3. Ground Voltage

Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

4. Ground Wiring Pattern

When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

5. Thermal Consideration

Should by any chance the power dissipation rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. In case of exceeding this absolute maximum rating, increase the board size and copper area to prevent exceeding the Pd rating.

6. Recommended Operating Conditions

These conditions represent a range within which the expected characteristics of the IC can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

7. Inrush Current

When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

8. Operation Under Strong Electromagnetic Field

Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

9. Testing on Application Boards

When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

10. Inter-pin Short and Mounting Errors

Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

11. Unused Input Pins

Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.

Operational Notes – continued

12. Regarding the Input Pin of the IC

This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of the P layers with the N layers of other elements, creating a parasitic diode or transistor. For example (refer to figure below):

When GND > Pin A and GND > Pin B, the P-N junction operates as a parasitic diode.
 When GND > Pin B, the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions that cause these diodes to operate, such as applying a voltage lower than the GND voltage to an input pin (and thus to the P substrate) should be avoided.

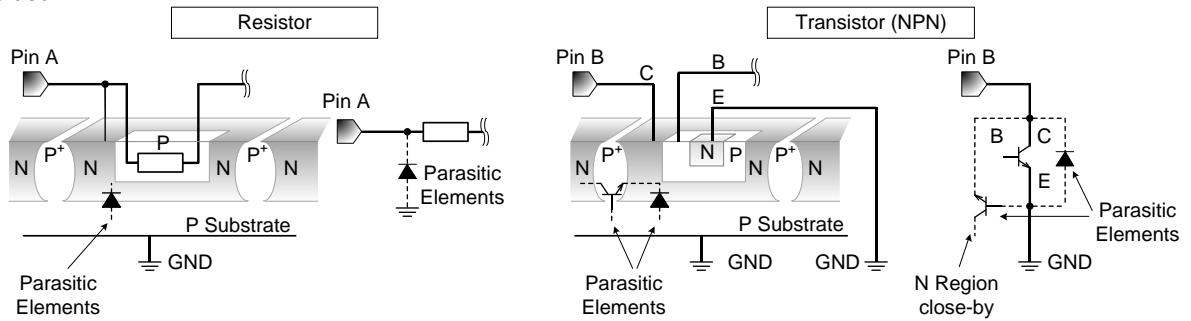


Figure 14. Example of monolithic IC structure

13. Thermal Shutdown Circuit(TSD)

This IC has a built-in thermal shutdown circuit that prevents heat damage to the IC. Normal operation should always be within the IC's power dissipation rating. If however the rating is exceeded for a continued period, the junction temperature (Tj) will rise which will activate the TSD circuit that will turn OFF all output pins. The IC should be powered down and turned ON again to resume normal operation because the TSD circuit keeps the outputs at the OFF state even if the Tj falls below the TSD threshold.

Note that the TSD circuit operates in a situation that exceeds the absolute maximum ratings and therefore, under no circumstances, should the TSD circuit be used in a set design or for any purpose other than protecting the IC from heat damage.

	TSD on Temperature [°C] (typ)
BD3504FVM	175

14. Capacitor Between Output and Ground

If a large capacitor is connected between the output pin and ground pin, current from the charged capacitor can flow into the output pin and may destroy the IC when the VCC or IN pin is shorted to ground or pulled down to 0V. Use a capacitor smaller than 1000µF between output and ground.

15. Output Capacitor (C4)

Connect the output capacitor between VS terminals and GND terminal properly in order to stabilize output voltage. The output capacitor is use to compensate for the phase of loop gain and to reduce output voltage fluctuation when there is an abrupt change in load. When there is insufficient capacitance value, there is a possibility for oscillation to occur, and when the equivalent serial resistance (ESR) of the capacitor is large, output voltage fluctuation is increased when there is an abrupt change in load. About 220 µF high-performance electrolytic capacitors are recommended, but this greatly depends on the gate capacity of external MOSFET and transconductance (gm), temperature and load conditions. In addition, when only ceramic capacitors with low ESR are used, or various capacitors are connected in series, the total phase margin of loop gain becomes insufficient, and oscillation may occur. Output capacitance values should be determined only through sufficient testing of the actual application.

Operational Notes – continued

16. Input Capacitor Setting Method (C₁, C₂)

The input capacitor is used to lower output impedance of the power supply connected to input terminals (VCC, IN). When output impedance of this power supply increases, the input voltages (VCC, IN) become unstable and there is a possibility to have oscillation and degraded ripple rejection characteristics. It is recommended to use capacitors of about 10 μ F with low ESR, which can provide less change in capacitance due to temperature change, but since input capacitor greatly depends on the characteristics of the power supply used for input, substrate wiring pattern, MOSFET gate-drain capacity, thorough confirmation under the application temperature, load range, and M-MOSFET conditions is required.

17. NRCS Terminal Capacitor Setting Method (C₃)

This IC has a Non Rush Current on Start-up (NRCS) function to prevent rush current from IN to load and output capacitor via OUT at the output voltage start-up. When the EN terminal is reset from Hi or UVLO, constant current is allowed to flow from the NRCS terminal. With this current, voltage generated at the NRCS terminal becomes the reference voltage and output voltage is started. In order to stabilize the NRCS set time, it is recommended to use a capacitor (B special) with less change in capacitance due to temperature change.

18. Input Terminals (VCC, VD, EN)

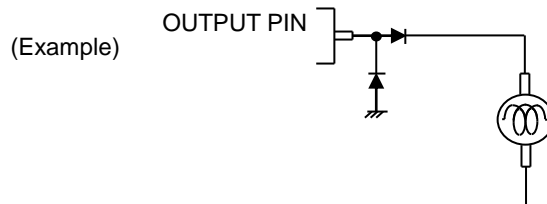
This IC has independent construction of EN, VD and VCC terminals. In addition, in order to prevent malfunction during low input, the UVLO function is connected to VD and VCC terminals. They begin to start output voltage when all the terminals reach the threshold voltage regardless of the input sequence of input terminals.

19. Maximum Output Current (Maximum Load)

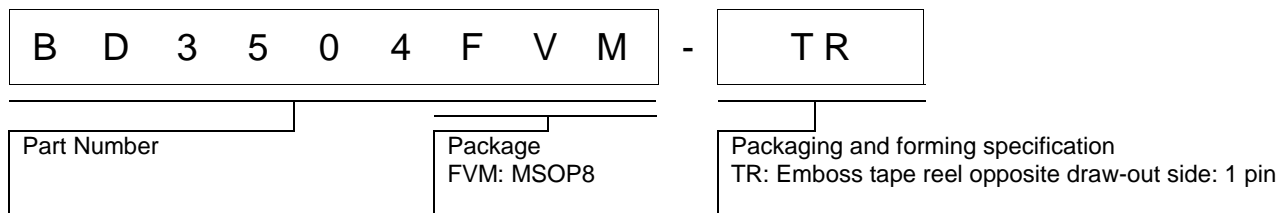
The maximum output current capacity of the power supply which is used in this IC depends on the external FET. Consequently, confirm the power requirement of the external FET to be used.

20. Output Protection Diode

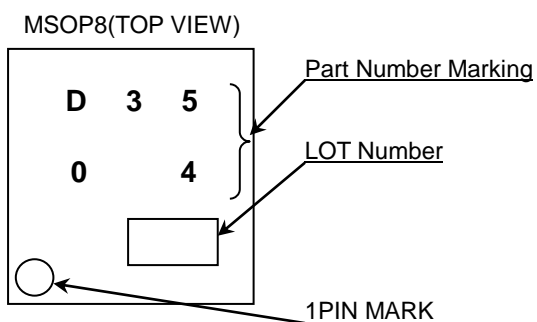
When a load containing a large inductance component is connected to the output terminal, and generation of back-EMF at the start-up and when output is turned OFF is assumed, it is requested to insert a protection diode.



Ordering Information

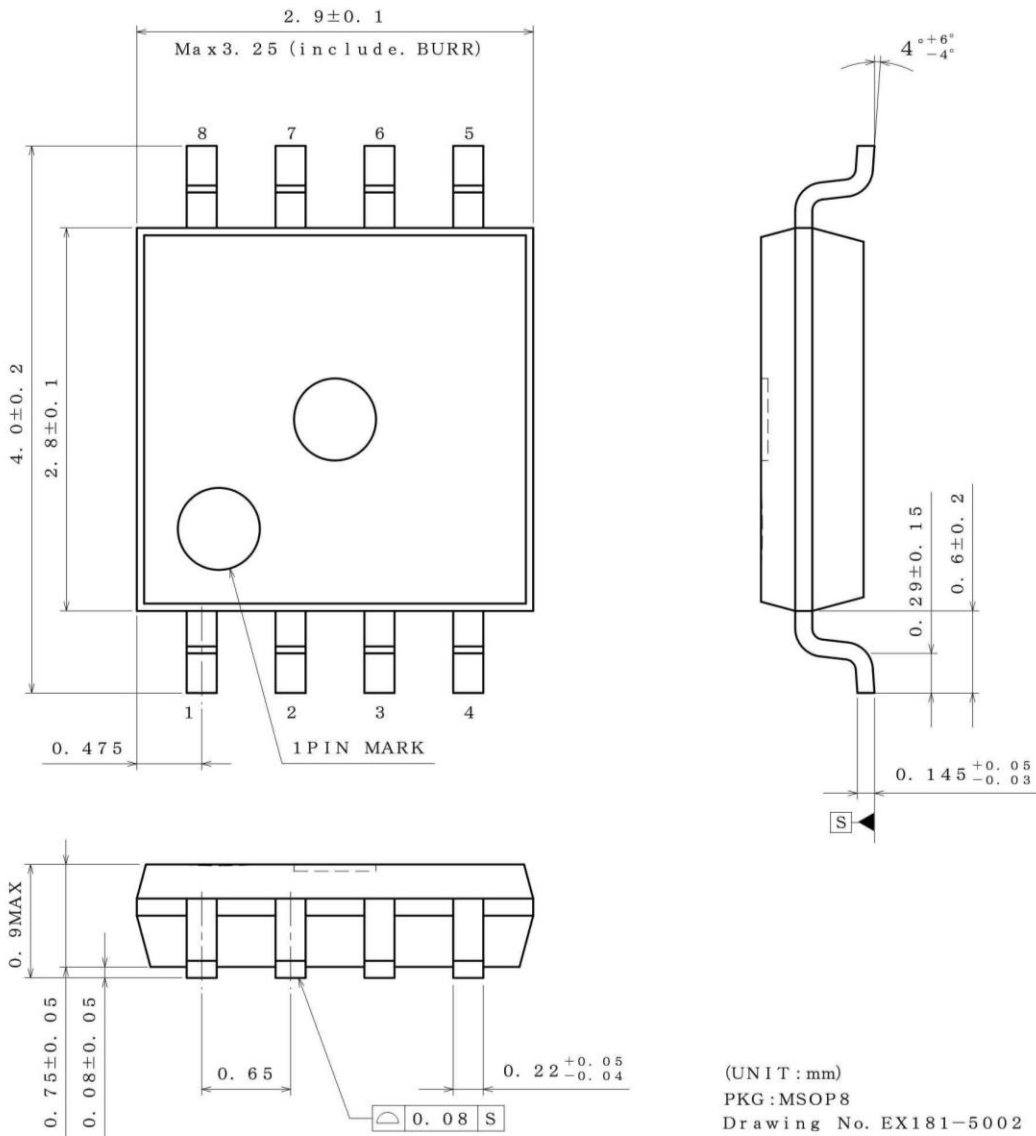


Marking Diagram



Physical Dimension, Tape and Reel Information

Package Name	MSOP8
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<Tape and Reel information>

Tape	Embossed carrier tape
Quantity	3000pcs
Direction of feed	TR (The direction is the 1pin of product is at the upper right when you hold reel on the left hand and you pull out the tape on the right hand)

*Order quantity needs to be multiple of the minimum quantity.

Revision History

Date	Revision	Changes
02.Nov.2015	001	New Release

Notice

Precaution on using ROHM Products

- Our Products are designed and manufactured for application in ordinary electronic equipments (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment ^(Note 1), transport equipment, traffic equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

- ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
 - Installation of protection circuits or other protective devices to improve system safety
 - Installation of redundant circuits to reduce the impact of single or multiple circuit failure
- Our Products are designed and manufactured for use under standard conditions and not under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc. prior to use, must be necessary:
 - Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
 - Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - Sealing or coating our Products with resin or other coating materials
 - Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - Use of the Products in places subject to dew condensation
- The Products are not subject to radiation-proof design.
- Please verify and confirm characteristics of the final or mounted products in using the Products.
- In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

Precaution for Product Label

QR code printed on ROHM Products label is for ROHM's internal use only.

Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

Precaution for Foreign Exchange and Foreign Trade act

Since concerned goods might be fallen under listed items of export control prescribed by Foreign exchange and Foreign trade act, please consult with ROHM in case of export.

Precaution Regarding Intellectual Property Rights

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