



**THE DATASHEET OF  
USB50415CE3/TR7**





## Bidirectional TVS Array

### DESCRIPTION

This USB50403C – USB50424C Transient Voltage Suppressor (TVS) family is packaged in a SOT-143 configuration giving protection to 1 bidirectional data or interface line. It is designed for use in applications where protection is required at the board level from voltage transients caused by electrostatic discharge (ESD) as defined in IEC 61000-4-2, electrical fast transients (EFT) per IEC 61000-4-4 and secondary effects of lightning. It is also available in RoHS compliant versions.

These TVS arrays have a peak power rating of 500 watts for an 8/20  $\mu$ sec pulse. This array is suitable for protection of sensitive circuitry consisting of TTL, CMOS, DRAM's, SRAM's, HCMOS, HSIC microprocessors, and Universal Serial Bus (USB) and I/O transceivers.

**Important:** For the latest information, visit our website <http://www.microsemi.com>.

### FEATURES

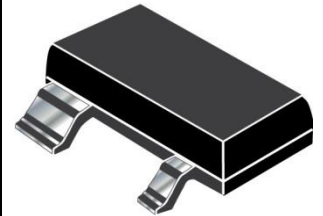
- Protects 1 bidirectional line.
- Surge protection per IEC 61000-4-2 and IEC 61000-4-4.
- Provides electrically isolated protection.
- UL 94V-0 flammability classification.
- Ultra low capacitance, 3 pF per line pair.
- Ultra low leakage.
- RoHS compliant versions available.

### APPLICATIONS / BENEFITS

- EIA-RS485 data rates: 5 Mbs
- 10 Base T Ethernet.
- USB data rate: 900 Mbs


### MAXIMUM RATINGS

Parameters/Test Conditions	Symbol	Value	Unit
Junction and Storage Temperature	$T_J$ and $T_{STG}$	-55 to +150	$^{\circ}C$
Peak Pulse Power @ 8/20 $\mu$ s (see <a href="#">Figure 1</a> )	$P_{PP}$	500	W
Impulse Repetition Rate	df	< .01	%
Total Capacitance (f = 1 MHz) @ 0 V	$C_T$	3	pF
Solder Temperature @ 10 s		260	$^{\circ}C$



### SOT-143 Package

Also available in:

**Unidirectional SOT-143**  
(surface mount)  
 [USB50403 – USB50424](#)

#### MSC – Lawrence

6 Lake Street,  
Lawrence, MA 01841  
Tel: 1-800-446-1158 or  
(978) 620-2600  
Fax: (978) 689-0803

#### MSC – Ireland

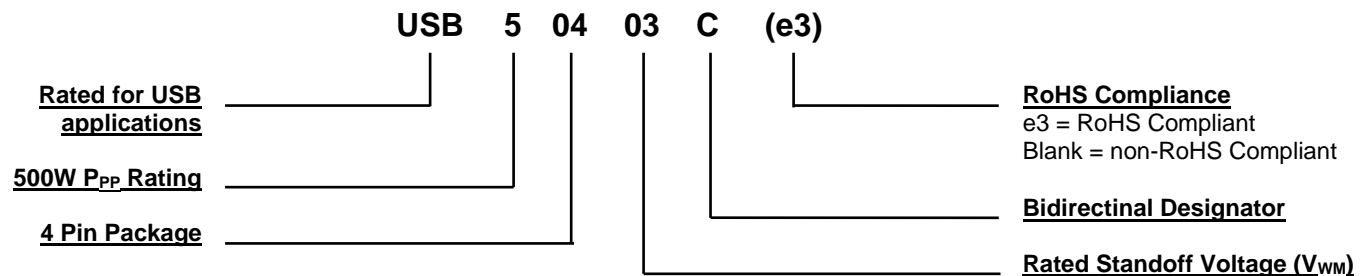
Gort Road Business Park,  
Ennis, Co. Clare, Ireland  
Tel: +353 (0) 65 6840044  
Fax: +353 (0) 65 6822298

**Website:**

[www.microsemi.com](http://www.microsemi.com)

**MECHANICAL and PACKAGING**

- CASE: Molded SOT-143 surface mount
- TERMINALS: Tin-lead or RoHS compliant annealed matte-tin plating
- MARKING: See electrical characteristics table
- POLARITY: Pin #1 (Please refer to [Schematic and Pad Layout](#) for pin 1.)
- TAPE & REEL option: Per EIA standard 481-1-A. Consult factory for quantities
- WEIGHT: Approximately 0.035 grams
- See [Package Dimensions](#) on last page.

**PART NOMENCLATURE**

**SYMBOLS & DEFINITIONS**

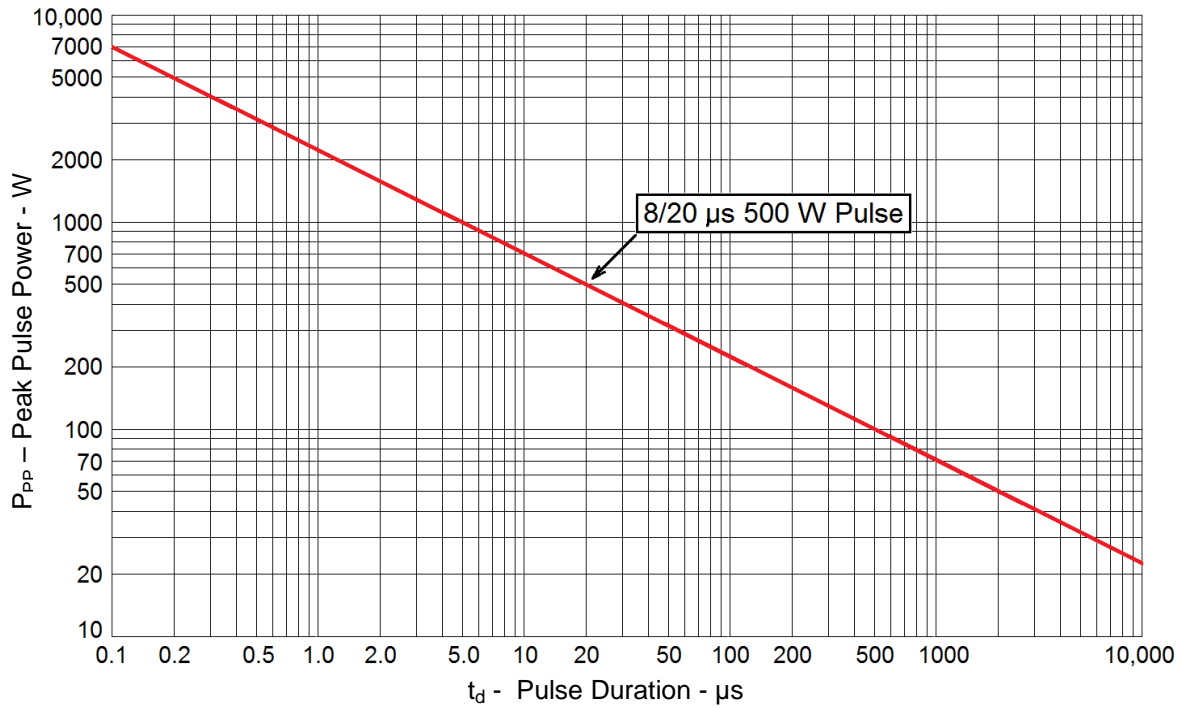
Symbol	Definition
$\alpha_{V(BR)}$	Temperature Coefficient of Breakdown Voltage: The change in breakdown voltage divided by the change in temperature that caused it expressed in %/°C or mV/°C.
$C_T$	Total Capacitance: The total small signal capacitance between the diode terminals of a complete device.
$I_D$	Standby Current: The current through the device at rated stand-off voltage.
$V_{(BR)}$	Breakdown Voltage: The voltage across the device at a specified current $I_{(BR)}$ in the breakdown region.
$V_C$	Clamping Voltage: The voltage across the device in a region of low differential resistance during the application of an impulse current ( $I_{PP}$ ) for a specified waveform.
$V_{WM}$	Working Standoff Voltage: The maximum-rated value of dc or repetitive peak positive cathode-to-anode voltage that may be continuously applied over the standard operating temperature.

**ELECTRICAL CHARACTERISTICS**

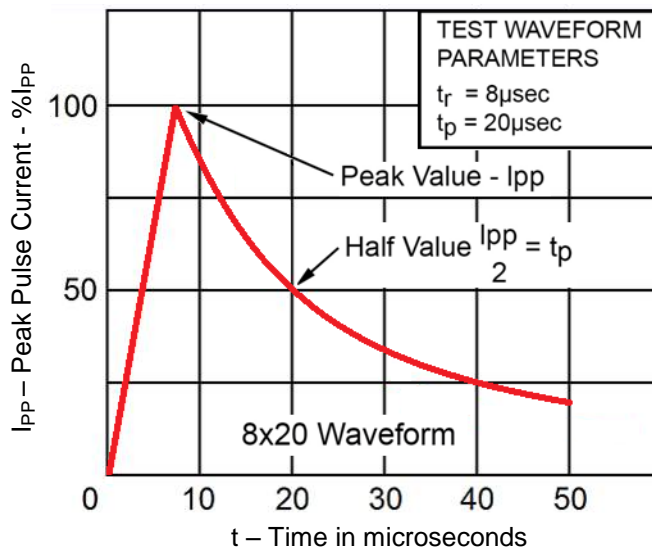
PART NUMBER	DEVICE MARKING*	STAND-OFF VOLTAGE	BREAKDOWN VOLTAGE	CLAMPING VOLTAGE	CLAMPING VOLTAGE	STANDBY CURRENT	TOTAL CAPACITANCE	TEMPERATURE COEFFICIENT
		$V_{WM}$	$V_{(BR)}$	$V_C$	$V_C$	$I_D$	(f= 1 MHz)	OF $V_{(BR)}$
		Volts	Volts	@ 1 Amp	@ 5 Amp	@ $V_{WM}$	$C_T$	$\alpha_{V(BR)}$
		MAX	MIN	(Figure 2)	(Figure 2)	$\mu A$	@ 0 V	mV/°C
		MAX	MIN	MAX	MAX	MAX	MAX	MAX
USB50403C	503	3.3	4	8	11	200	3	-5
USB50405C	505	5.0	6.0	10.8	13	40	3	1
USB50412C	512	12.0	13.3	19	26	1	3	8
USB50415C	515	15.0	16.7	24	32	1	3	11
USB50424C	524	24.0	26.7	43	57	1	3	28

\* Device marking has a dot suffix for the e# RoHS compliant option (e.g. 503●, 512●, etc.)

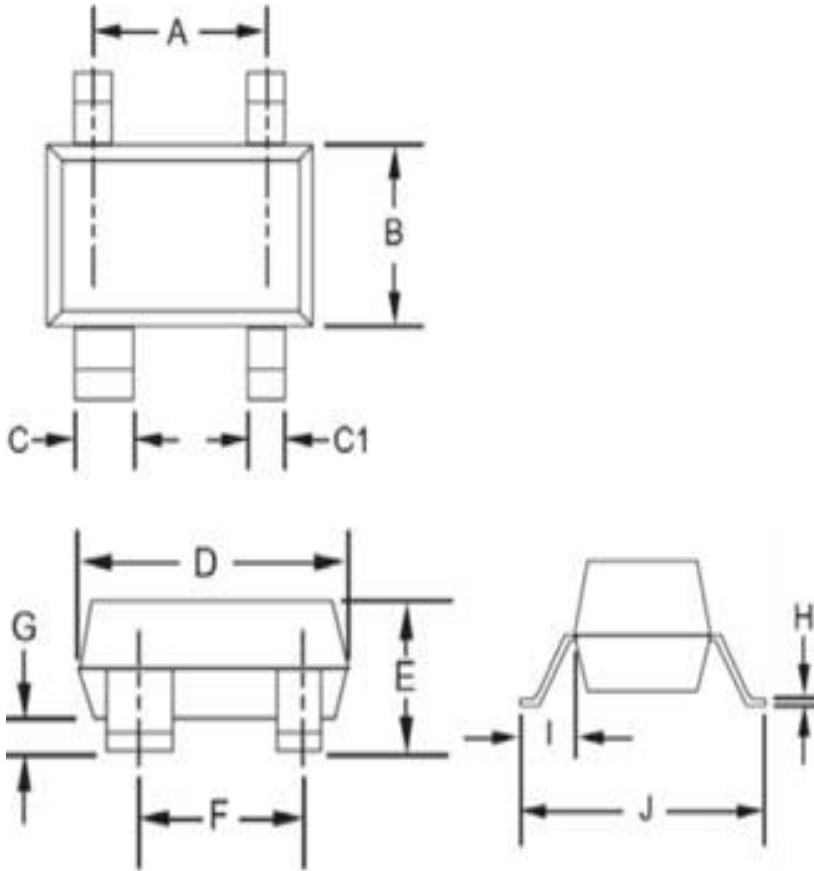
GRAPHS



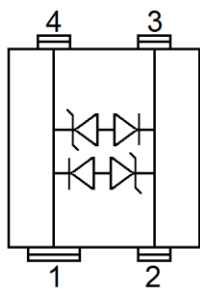
**FIGURE 1**  
Peak Pulse Power vs Pulse Time



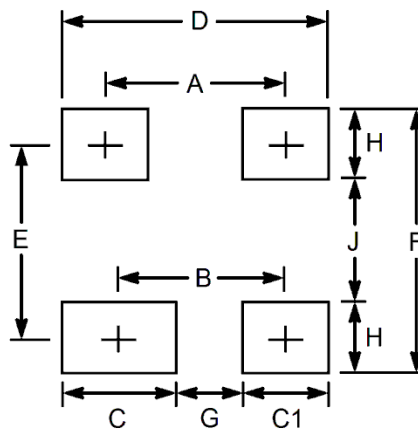
**FIGURE 2**  
Pulse Waveform

**PACKAGE DIMENSIONS**


Ltr	Dimensions			
	Inch		Millimeters	
	Min	Max	Min	Max
<b>A</b>	0.0701	0.0807	1.78	2.05
<b>B</b>	0.047	0.055	1.20	1.40
<b>c1</b>	0.027	0.033	0.69	0.84
<b>C1</b>	0.012	0.018	0.30	0.46
<b>D</b>	0.107	0.113	2.72	2.87
<b>E</b>	0.042	0.045	1.07	1.14
<b>F</b>	0.067	0.079	1.70	2.01
<b>G</b>	0.002	0.008	0.051	0.2
<b>H</b>	0.003	0.009	0.076	0.23
<b>I</b>	0.018	0.023	0.46	0.58
<b>J</b>	0.083	0.093	2.11	2.36

**SCHEMATIC AND PAD LAYOUT**


Schematic



Ltr	Dimensions	
	Inch	Millimeters
	Typ	Typ
<b>A</b>	0.079	2.00
<b>B</b>	0.071	1.80
<b>C</b>	0.047	1.20
<b>C1</b>	0.033	0.85
<b>D</b>	0.112	2.85
<b>E</b>	0.075	1.90
<b>F</b>	0.108	2.75
<b>G</b>	0.310	0.80
<b>H</b>	0.033	0.85
<b>J</b>	0.041	1.05

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 [Microchip Technology](#) Information

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