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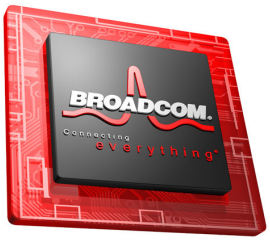
BCM5482S PRODUCT Brief



DUAL-PORT 10/100/1000BASE-T GIGABIT ETHERNET TRANSCEIVER

FEATURES

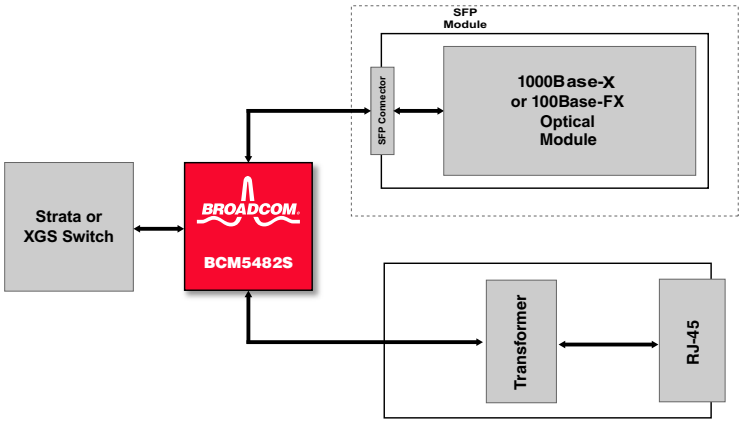
- **Dual-port integrated 10BASE-T/100BASE-TX/1000BASE-T Gigabit Ethernet transceiver**
- **RGMII, SGMII, and SerDes MAC interface options**
- **SGMII to SGMII support for SFP modules**
- **Fully compliant with IEEE 802.3™, IEEE 802.3u, IEEE 802.3ab standard, and IEEE 802.3z**
- **0.13-micron CMOS—low power and cost**
- **Supports copper or fiber in RGMII mode**
- **Low power**
  - 725 mW per port
  - Advanced power management
- **Trace-matched output impedance**
- **Line-side and MAC-side loopback**
- **Low EMI emissions**
- **CableChecker™ diagnostics**
  - Cable plant analyzer function detects cable plant impairments
  - Ethernet@WireSpeed
  - Automatic MDI/MDIX crossover at all speeds
- **Robust cable-sourced electrostatic discharge (CESD) tolerance**
- **Support for jumbo packets up to 10 kB**
- **IEEE 1149.1 (JTAG) boundary scan**
- **121-pin BGA package**

SUMMARY OF BENEFITS

- **Compact solution for high density uplink applications, enabling 10/100/1000BASE-T and 1000BASE-X/100BASE-FX uplinks**

  1000BASE-X and 100BASE-FX support from same line side pins enables direct interface to SFP modules
- **Provides compatibility with IEEE standard devices operating at 10, 100, and 1000 Mbps at half-duplex and full-duplex.**
- **Clock timing can be adjusted to eliminate board trace delays required by the RGMII specification.**
- **Lowers system BOM cost and simplifies system design**
- **Eases system level debugging**
- **Enables use of low-cost magnetics**
- **Cable diagnostic function characterizes cable plant condition and immediately indicates cabling issues.**
  - Prevents erroneous equipment return due to bad cable plants
  - Prevents manufacturing fallout due to bad cable plants
  - Allows immediate uptime at reduced speeds (100BASE-TX or 10BASE-T)
- **Over 3 kV of CESD tolerance prevents equipment damage and return.**
- **Operates with larger packets for wider range of packet protocol support and improved efficiency**
- **Ease of manufacturing with JTAG support, simplified power supply, and multiple MAC interfaces**

BCM5482S System Diagram



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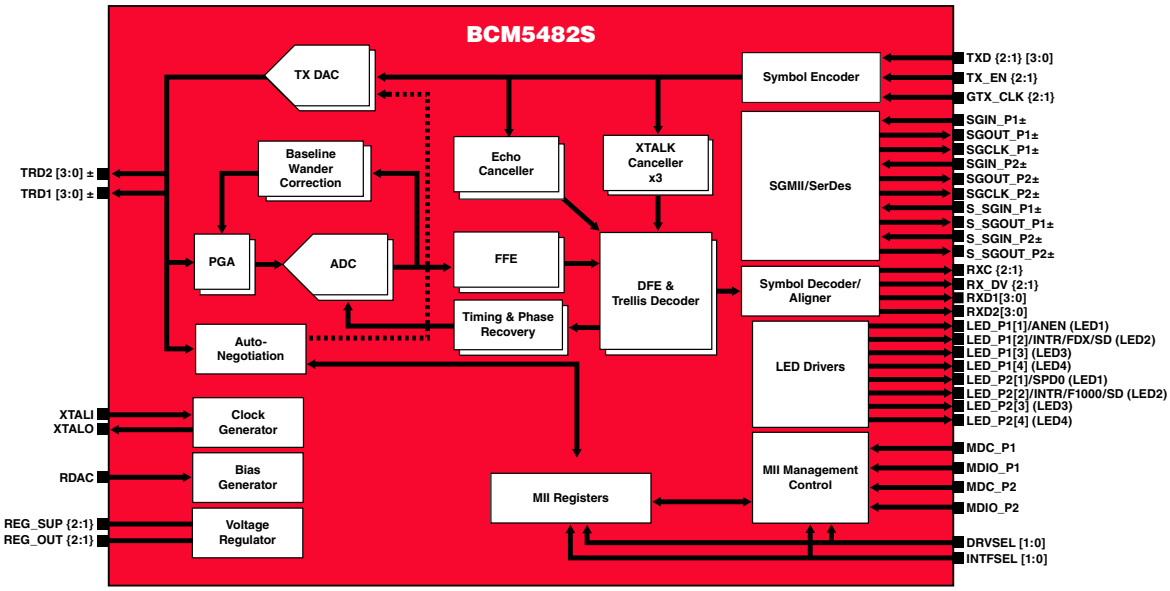
OVERVIEW



BCM5482S Block Diagram

A member of Broadcom's third generation of Gigabit Ethernet PHYs, the BCM5482S consists of two complete 10/100/1000BASE-T Gigabit Ethernet transceivers integrated on a single monolithic CMOS chip. The BCM5482S is optimized for low power and small footprint size to reduce design complexity for high-density uplink applications. For high-density uplink applications, the single package offers a compelling advantage over two single devices for Gigabit uplinks.

The BCM5482S DSP-based architecture and advanced power management techniques combine to achieve robust and low-power operation over existing Category 5 twisted-pair wiring. The BCM5482S architecture not only meets the requirements of IEEE 802.3, IEEE 802.3u, and IEEE 802.3ab, but also maintains the industry's highest level of margin over IEEE requirements for echo, near-end crosstalk (NEXT), and far-end crosstalk (FEXT). With the industry's lowest power at 725 mW per port, the BCM5482S assists in reducing the power distribution requirements of IP phone systems. In addition, the BCM5482S has extremely low EMI emissions, which reduces the design constraints required to meet EMI radiation specifications.

The BCM5482S supports the RGMII, SGMII, and SerDes MAC interfaces. The RGMII, SGMII, and serial SerDes interfaces are reduced-pin-count (12, 6, and 4, respectively, versus 25) versions of the GMII. The RGMII clock timing can be adjusted to eliminate the board trace delays required by the RGMII specification. These reduced-pin-count interfaces simplify design and lower system cost by reducing the number of layers required for signal routing. In addition, these interfaces allow fewer pins at the MAC/switch, which reduces the MAC/switch cost by enabling smaller die sizes than would be possible with full GMII.

In most switch designs, a few ports are targeted as uplink ports to connect to servers, switches, or remote links. These connections can be 10/100/1000BASE-T, 1000BASE-SX/LX, or 100BASE-FX. In order to accommodate these various interfaces, one or more SFP slots can be designed into the switch and the user can plug an SFP module into the slot that meets their specific requirement.

The BCM5482S addresses the specific problem of supporting multispeed SFP uplinks. 1000BASE-X SFP modules require a SerDes interface to the SFP module. 10/100/1000BASE-T and 100BASE-FX require an SGMII interface in order to rate adapt to the lower speeds (less than 1 gigabit). The BCM5482S can switch between any of these interfaces, depending on the SFP module installed.

Each BCM5482S port is fully independent and has individual interface, control, and status registers, and incorporates a number of advanced features. This includes identifying physical wiring defects that the BCM5482S cannot automatically correct and channel conditions such as excessive cable length and return loss, crosstalk, echo, and noise. Broadcom's cable analyzer software can be used with the device to provide remote management of the cable and a first level of diagnostics and fault isolation.

The BCM5482S also has ESD tolerance that is well above typical industry standards. This prevents ESD damage not only during manufacturing, but also during CESD events in the field. The CESD is an ESD event that occurs when an electrically charged network cable is plugged into a network port. This issue is becoming more prevalent with contemporary cable installations. The BCM5482S can tolerate over 3 kV of CESD.

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