



**THE DATASHEET OF  
PT78ST112S**



# PT78ST100 Series

## 1.5 Amp Positive Step-Down Integrated Switching Regulator

**SLTS059A**

(Revised 6/30/2000)

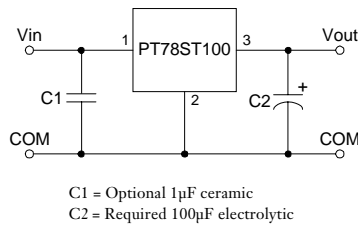
- Very Small Footprint
- High Efficiency > 85%
- Self-Contained Inductor
- Internal Short-Circuit Protection
- Over-Temperature Protection
- Fast Transient Response
- Wide Input Range

The PT78ST100 is a series of wide-input range, 3-terminal regulators.

These ISRs have a maximum output current of 1.5 Amps and an output voltage that is laser trimmed to a variety of industry standard voltages.

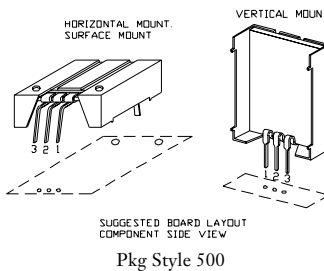
These 78 series regulators have excellent line and load regulation with internal short-circuit and over-temperature protection, and are offered in a variety of standard output voltages. These ISRs are very flexible and may be used in a wide variety of applications.

### Standard Application



### Pin-Out Information

Pin	Function
1	V <sub>in</sub>
2	GND
3	V <sub>out</sub>



### Ordering Information

PT78ST1 XX Y

Output Voltage

- 33 = 3.3 Volts
- 36 = 3.6 Volts
- 05 = 5.0 Volts
- 51 = 5.1 Volts
- 53 = 5.25 Volts
- 06 = 6.0 Volts
- 65 = 6.5 Volts
- 07 = 7.0 Volts
- 08 = 8.0 Volts
- 09 = 9.0 Volts
- 10 = 10.0 Volts
- 12 = 12.0 Volts
- 14 = 13.9 Volts
- 15 = 15.0 Volts

Package Suffix

- V = Vertical Mount
- S = Surface Mount
- H = Horizontal Mount

### Specifications

Characteristics (T <sub>a</sub> = 25°C unless noted)	Symbols	Conditions	PT78ST100 SERIES			Units
			Min	Typ	Max	
Output Current	I <sub>o</sub>	Over V <sub>in</sub> range	0.1*	—	1.5	A
Short Circuit Current	I <sub>sc</sub>	V <sub>in</sub> = V <sub>in</sub> min	—	3.5	—	Apk
Input Voltage Range	V <sub>in</sub>	0.1 ≤ I <sub>o</sub> ≤ 1.5A V <sub>o</sub> = 3.3V V <sub>o</sub> = 5V V <sub>o</sub> = 12V	9 9 16	— — —	26 38 38	V V V
Output Voltage Tolerance	ΔV <sub>o</sub>	Over V <sub>in</sub> range, I <sub>o</sub> = 1.5A T <sub>a</sub> = 0°C to +60°C	—	±1.0	±2.0	%V <sub>o</sub>
Line Regulation	Reg <sub>line</sub>	Over V <sub>in</sub> range	—	±0.2	±0.4	%V <sub>o</sub>
Load Regulation	Reg <sub>load</sub>	0.1 ≤ I <sub>o</sub> ≤ 1.5A	—	±0.1	±0.2	%V <sub>o</sub>
V <sub>o</sub> Ripple/Noise	V <sub>n</sub>	V <sub>in</sub> = 9V, I <sub>o</sub> = 1.5A V <sub>in</sub> = 16V, I <sub>o</sub> = 1.5A V <sub>o</sub> = 5V V <sub>o</sub> = 12V	—	65 90	—	mV <sub>pp</sub> mV <sub>pp</sub>
Transient Response (with 100µF output cap)	t <sub>tr</sub>	50% load change V <sub>o</sub> over/undershoot	—	100 5	—	µSec %V <sub>o</sub>
Efficiency	η	V <sub>in</sub> = 10V, I <sub>o</sub> = 1A V <sub>in</sub> = 10V, I <sub>o</sub> = 1A V <sub>in</sub> = 17V, I <sub>o</sub> = 1A V <sub>o</sub> = 3.3V V <sub>o</sub> = 5V V <sub>o</sub> = 12V	—	80 85 90	—	% % %
Switching Frequency	f <sub>o</sub>	Over V <sub>in</sub> range, I <sub>o</sub> = 1.5A	600	650	700	kHz
Absolute Maximum Operating Temperature Range	T <sub>a</sub>	—	-40	—	+85	°C
Recommended Operating Temperature Range	T <sub>a</sub>	Free Air Convection, (40-60LFM) At V <sub>in</sub> = 24V, I <sub>o</sub> = 1.0A	-40	—	+80**	°C
Thermal Resistance	θ <sub>ja</sub>	Free Air Convection, (40-60LFM)	—	45	—	°C/W
Storage Temperature	T <sub>s</sub>	—	-40	—	+125	°C
Mechanical Shock	—	Per Mil-STD-883D, Method 2002.3	—	500	—	G's
Mechanical Vibration	—	Per Mil-STD-883D, Method 2007.2, 20-2000 Hz, soldered in a PC board	—	5	—	G's
Weight	—	—	—	6.5	—	grams

\*ISR will operate down to no load with reduced specifications.

\*\*See Thermal Derating chart.

**Note:** The PT78ST100 Series requires a 100µF electrolytic or tantalum output capacitor for proper operation in all applications.

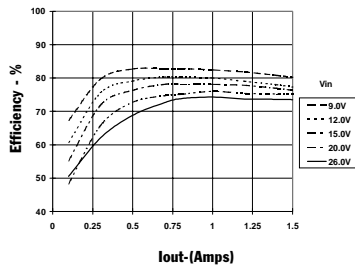
# PT78ST100 Series

# Typical Characteristics

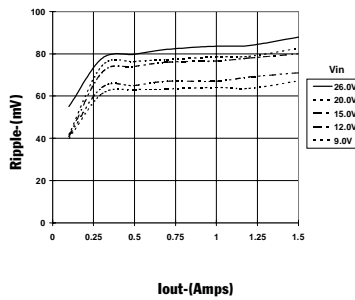
1.5 Amp Positive Step-Down  
Integrated Switching Regulator

**PT78ST133, 3.3 VDC** (See Note 1)

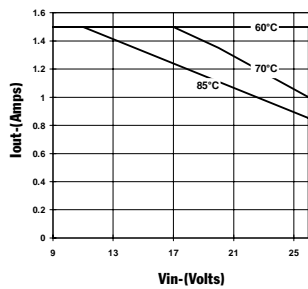
**Efficiency vs Output Current**



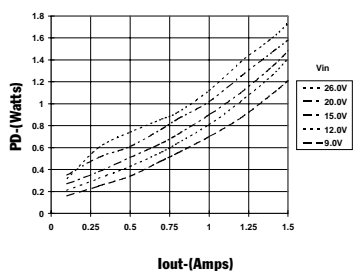
**Ripple vs Output Current**



**Thermal Derating (Ta)** (See Note 2)

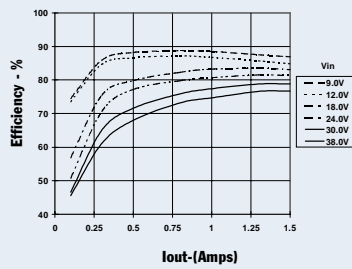


**Power Dissipation vs Output Current**

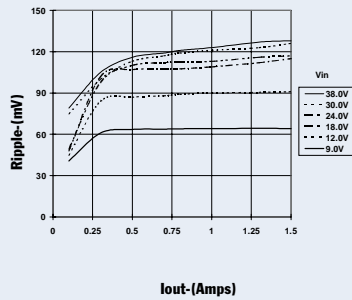


**PT78ST105, 5.0 VDC** (See Note 1)

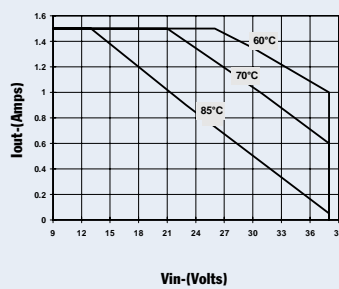
**Efficiency vs Output Current**



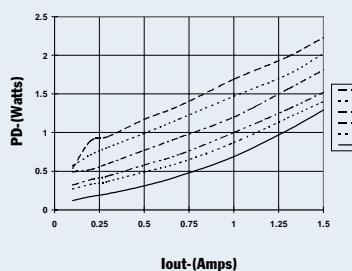
**Ripple vs Output Current**



**Thermal Derating (Ta)** (See Note 2)

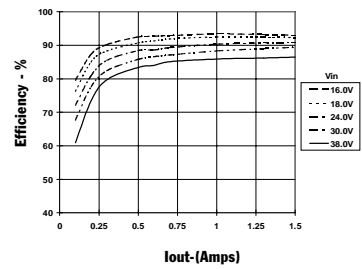


**Power Dissipation vs Output Current**

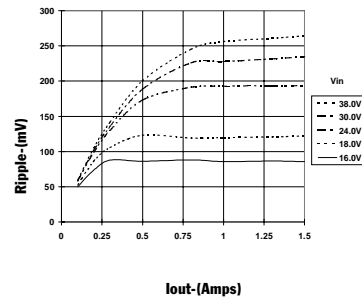


**PT78ST112, 12.0 VDC** (See Note 1)

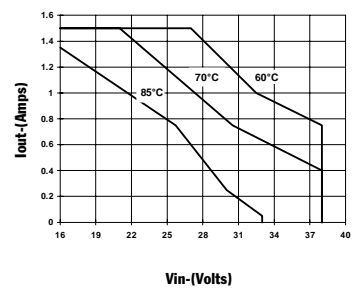
**Efficiency vs Output Current**



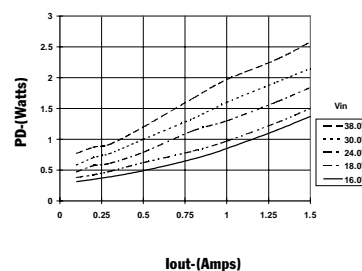
**Ripple vs Output Current**



**Thermal Derating (Ta)** (See Note 2)



**Power Dissipation vs Output Current**



**Note 1:** All data listed in the above graphs, except for derating data, has been developed from actual products tested at 25°C. This data is considered typical data for the ISR.  
**Note 2:** Thermal derating graphs are developed in free air convection cooling of 40-60 LFM. (See Thermal Application Notes.)

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