



**THE DATASHEET OF
FDC500026W**



| | | | | | |
|---------------------------------|---------------------|--|---------------------|--------------|--------------------------------|
| Supply Current, Standby Mode | | | 10 | μA | Output Hi-Z |
| Frequency Stability | | | ±20 to ±50 | ppm | See Note 1 below |
| Operating Temperature Range | -20 | | +70 | °C | Commercial (standard) |
| | -40 | | +85 | | Industrial (standard) |
| Output Logic 0, V _{OL} | | | 10% V _{DD} | V | |
| Output Logic 1, V _{OH} | 90% V _{DD} | | | V | |
| Output Load | | | 15 | pF | See Note 2 below |
| Duty Cycle | 45 | | 55 | % | Measured 50% V _{DD} |
| Rise and Fall Time | up to 32 MHz | | 7 | ns | Measured 20/80% of waveform |
| | 32 to 70 MHz | | 5 | | |
| | 70 to 133 MHz | | 2.5 | | |
| Jitter, Phase | 1 to 133 MHz | | 1 | ps RMS (1-σ) | 10kHz to 20 MHz frequency band |
| Jitter, Accumulated | up to 70 MHz | | 5 | ps RMS (1-σ) | 20,000 adjacent periods |
| | 70 to 133 MHz | | 3 | | |
| Jitter, Total | up to 70 MHz | | 50 | ps pk-pk | 100,000 random periods |
| | 70 to 133 MHz | | 30 | | |

Notes:

1. Stability includes all combinations of operating temperature, load changes, rated input (supply) voltage changes, initial calibration tolerance (25°C), aging (1 year at 25°C average effective ambient temperature), shock and vibration.
2. For specifications other than those listed, please contact sales.

RMS Phase jitter or integrated phase jitter

RMS Period jitter

Total Period jitter (pk-pk)

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