



THE DATASHEET OF L8104R



L8104

EK0502-0018 Ver.A



PIN Diode

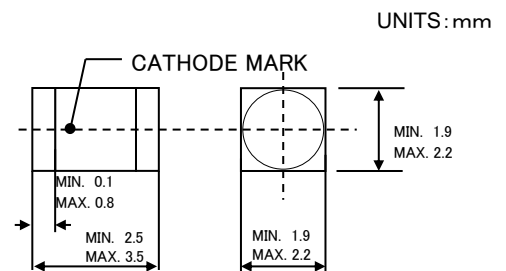
FEATURES

- High Power Handling
- Low Capacitance at Zero Bias, Extremely Small Reverse Bias
- Low Series Resistance
- Very Low Insertion Loss, High Isolation
- Hermetic Ceramic MELF Package
- RoHS Compliant
- Pb Free

DESCRIPTIONS

The L8104 PIN diode is designed for high power antenna switches in two-way radios.

DIMENSION



ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

SYMBOL	PARAMETER	RATING	UNITS
V_R	Reverse Voltage	180	V
P_D*	Power Dissipation	3	W
T_j	Junction Temperature	175	°C
T_{stg}	Storage Temperature Range	-55 to 175	°C

*) 25°C contacts

ELECTRICAL CHARACTERISTICS (Ta=25°C)

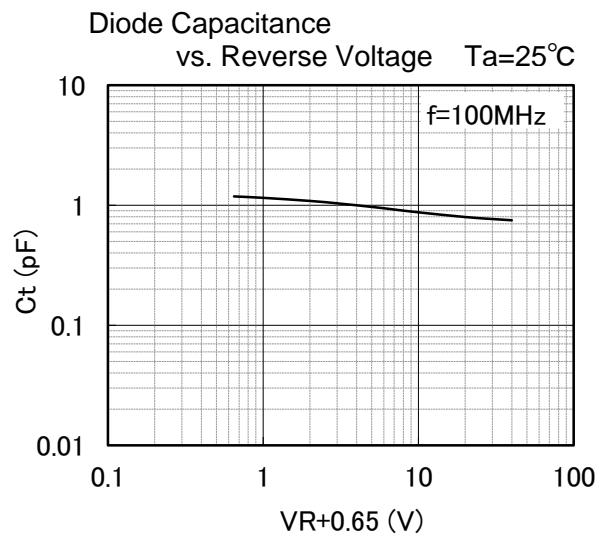
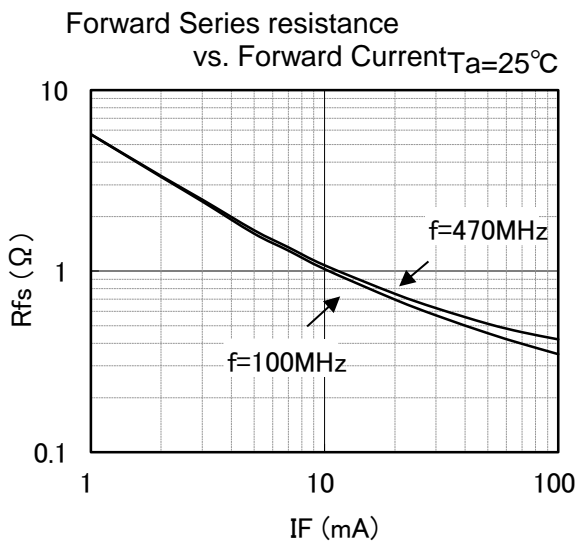
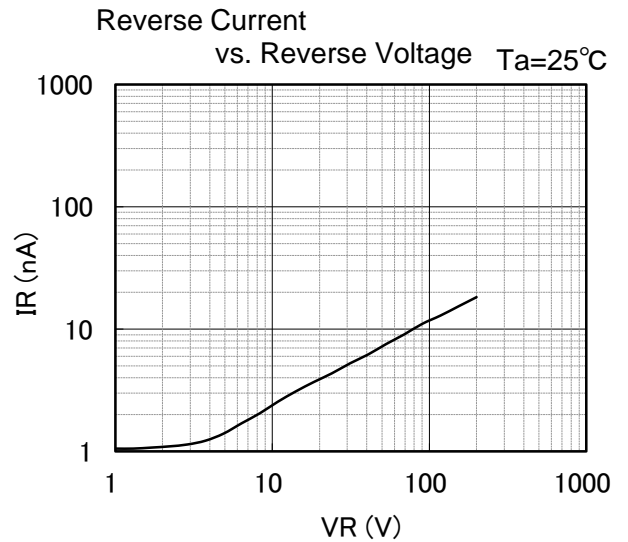
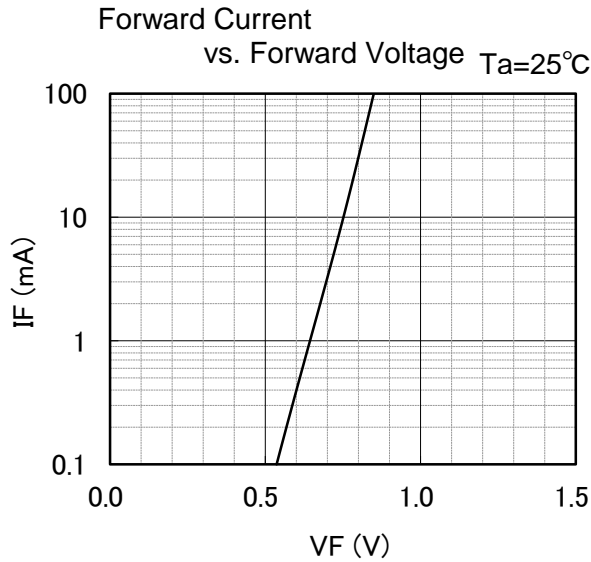
SYMBOL	PARAMETER	CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
I_R	Reverse Current	V_R = 180V	-	-	10	μA
V_F	Forward Voltage	I_F = 50mA	-	-	1.0	V
C_T	Diode Capacitance	V_R = 40V, f = 100MHz	-	-	1.2	pF
R_{fs}	Forward Series Resistance	I_F = 50mA, f = 100MHz	-	0.5	0.75	Ω
R_P	Parallel Resistance	V_R = 0V, f = 100MHz	1.0	3.0	-	kΩ

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■ TYPICAL PERFORMANCE CHARACTERISTICS





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