



**THE DATASHEET OF
AW002R2-12LF**



DATA SHEET

AW002R2-12, AW002R2-12LF: GaAs SPDT IC 5 W T/R Switch 300 kHz–2.5 GHz

Features

- High isolation (30 dB @ 0.9 GHz)
- Designed for mobile radio applications
- $P_{-1\text{ dB}} = 10\text{ W @ }0.9\text{ GHz @ }25\text{ °C}$
- High intercept point (IP3 63 dBm, @ 0.9 GHz)
- Available lead (Pb)-free and RoHS-compliant MSL-1 @ 260 °C per JEDEC J-STD-020

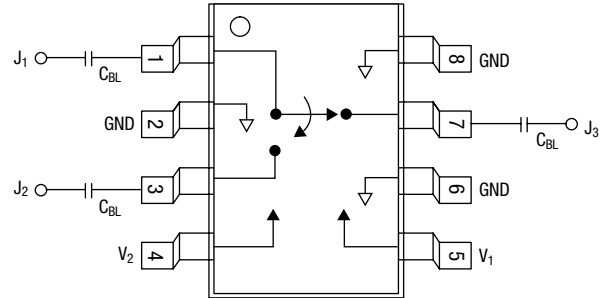
Description

The AW002R2-12 is a high-power IC FET SPDT switch in a plastic SOIC-8 package. This switch has been designed for use where extremely high linearity is required. It can be controlled with positive, negative or a combination of both voltages. Some standard implementations include antenna changeover, T/R and diversity switching over 2 W. This switch can be used in many analog and digital wireless communication systems including cellular, GSM and PCS applications.

NEW Skyworks offers lead (Pb)-free, RoHS (Restriction of Hazardous Substances)-compliant packaging.



Pin Out



External DC blocking capacitors (C_{BL}) are required only if $V_{HIGH} > 0.0\text{ V}$.
 $C_{BL} = 100\text{ pF}$ for operation $>500\text{ MHz}$.

Electrical Specifications at 25 °C (0, -5 V)

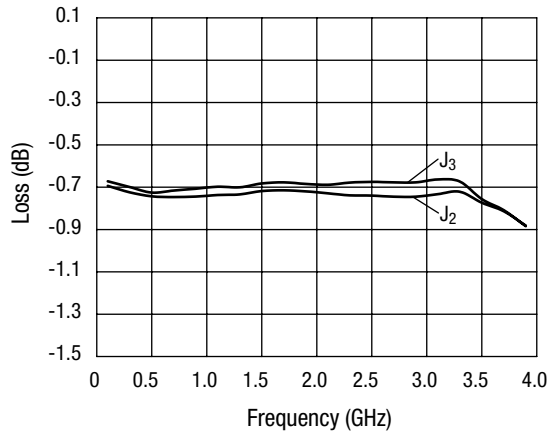
Parameter ⁽¹⁾	Frequency	Min.	Typ.	Max.	Unit
Insertion Loss ⁽²⁾	300 kHz–0.5 GHz		0.7	0.8	dB
	300 kHz–1.0 GHz		0.8	0.9	dB
	300 kHz–2.5 GHz		1.0	1.1	dB
Isolation	300 kHz–0.5 GHz	33	37		dB
	300 kHz–1.0 GHz	28	30		dB
	300 kHz–2.5 GHz	20	22		dB
VSWR ⁽³⁾	300 kHz–1.0 GHz		1.2:1	1.4:1	dB
	300 kHz–2.5 GHz		1.5:1	1.7:1	dB

1. All measurements made in a 50 Ω system, unless otherwise specified.
2. Insertion loss changes by 0.003 dB/°C.
3. Insertion loss state.

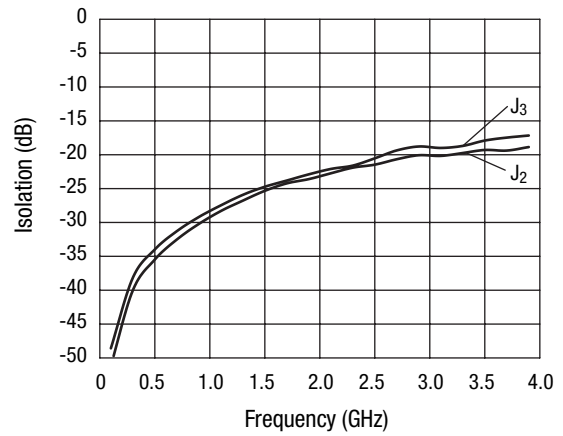
Operating Characteristics at 25 °C (0, -5 V)

Parameter	Condition	Frequency	Min.	Typ.	Max.	Unit
Switching characteristics						
Rise, fall	10/90% or 90/10% RF			6		ns
On, off	50% CTL to 90/10% RF			12		ns
Video feedthru	$T_{RISE} = 1 \text{ ns}$, $BW = 500 \text{ MHz}$			30		mV
Input Power for 1 dB compression	$V_{CTL} = 5 \text{ V}$	0.9 GHz		35		dBm
	$V_{CTL} = 10 \text{ V}$	0.9 GHz		40		dBm
Intermodulation intercept point	For two-tone input power 13 dBm	0.9 GHz		63		dBm
Thermal resistance				60		°C/W
Control voltages	$V_{LOW} = -12 \text{ V} \leq V_{LOW} \leq 0 \text{ V}$, 500 μA max. $V_{HIGH} = 0 \text{ V} \leq V_{HIGH} \leq 12 \text{ V}$, 500 μA max. Differential = $5 \text{ V} \leq (V_{HIGH} - V_{LOW}) < 12 \text{ V}$					

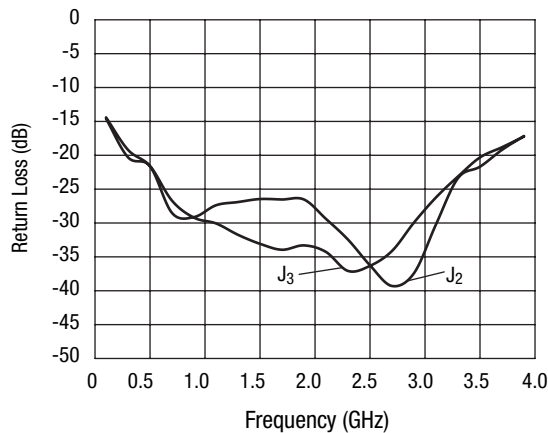
Typical Performance Data (0, -5 V)



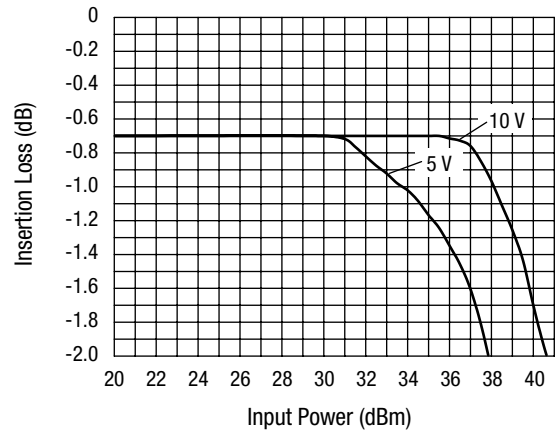
Typical Insertion Loss vs. Frequency



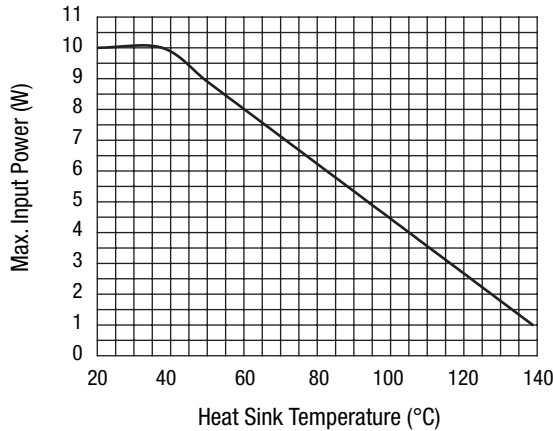
Typical Isolation vs. Frequency



Typical Return Loss vs. Frequency



Compression at 900 MHz 25 °C



Temperature Derating Curve (0, 10 V)

Absolute Maximum Ratings

Characteristic	Value
RF input power	5 W > 0.9 GHz, 0, -12 V
Control voltage	(V _{HIGH} - V _{LOW}) < 12 V
Operating temperature	-40 °C to +85 °C
Storage temperature	-65 °C to +150 °C

Performance is guaranteed only under the conditions listed in the specifications table and is not guaranteed under the full range(s) described by the Absolute Maximum specifications. Exceeding any of the absolute maximum/minimum specifications may result in permanent damage to the device and will void the warranty.

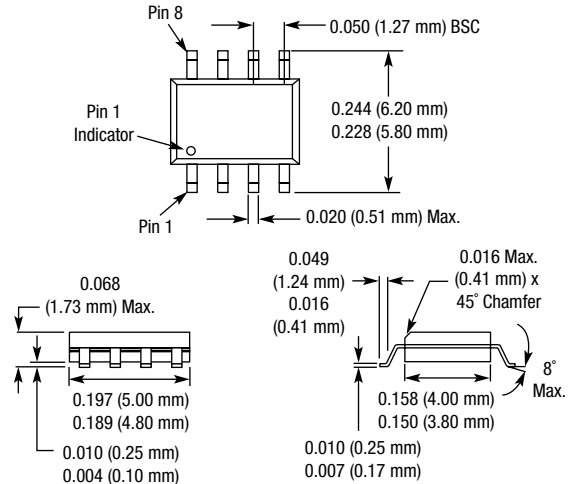
CAUTION: Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions must be employed at all times.

Truth Table

V ₁	V ₂	J ₁ -J ₂	J ₁ -J ₃
V _{LOW}	V _{HIGH}	Insertion loss	Isolation
V _{HIGH}	V _{LOW}	Isolation	Insertion loss

All other conditions not recommended.
 V_{LOW} = 0 to -12 V.
 V_{HIGH} = 0 to 12 V.
 Differential = 5 V ≤ (V_{HIGH} - V_{LOW}) < 12 V.
 Refer to Application Notes for further information on differential voltage operation.

SOIC-8



Recommended Solder Reflow Profiles

Refer to the [“Recommended Solder Reflow Profile”](#) Application Note.

Tape and Reel Information

Refer to the [“Discrete Devices and IC Switch/Attenuators Tape and Reel Package Orientation”](#) Application Note.

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