



**THE DATASHEET OF
9LP505-1HGLFT**



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9LP505-1HGLFT

Category: Desktop Chipsets

Generic Part: 9LP505-1

Market Group: PC CLOCK

Description: PC MAIN CLOCK

ICS9LP505-1 follows Intel CK505 Yellow Cover for 64-TSSOP device specification. This clock synthesizer provides a single chip solution for next generation P4 Intel processors and Intel chipsets. ICS9LP505-1 is driven with a 14.318MHz crystal. It also provides a tight ppm accuracy output for Serial ATA and PCI-Express support.

Output Features:

- 2 - CPU differential low power push-pull pairs
- 10 - SRC differential low power push-pull pairs
- 1 - CPU/SRC selectable differential low power push-pull pair
- 1 - SRC/DOT selectable differential low power push-pull pair
- 5 - PCI, 33MHz
- 1 - PCI_F, 33MHz free running
- 1 - USB, 48MHz
- 1 - REF, 14.318MHz



Parameters

Package	TSSOP 64 (PAG64)
Voltage	2.5V/3.3V
Package	TSSOP 64
Speed	NA
Temperature	C
Status	Active
Sample	No
Minimum Order Quantity	1000
Factory Order Increment	1000

Distributor Inventory

No Pricing information is available from our Distributors at this time.

Documents

Type	Title	Size	Revision Date
Misc	PC Clocks Contact Info	61 KB	05/29/2007

Package

Description	TSSOP 6.10 MM
Class	PLASTIC
Moisture Sensitivity Level (MSL)	1
Category	Green
Moisture Exposure Floor Life	Unlimited @ <30°C/85% RH
Peak Reflow Temperature	260°C
Rebake Conditions	NA
Length	17.0
Mark	G
Width	6.1
Pitch	0.5
Thickness	1.0
Status	Active

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