



**THE DATASHEET OF
ICS93777BF**



Home > Products > Clock / Timing Devices > PC-Notebook-Server Clocks > DDR I and DDR II Buffers > Zero Delay Buffers > 93777 > 93777BF

[Add to myIDT \[?\]](#)

You may also like...

93777BF

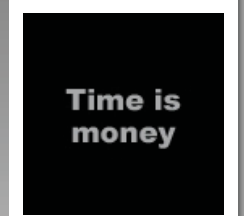
Category: Zero Delay Buffers

Generic Part: 93777

Market Group: PC CLOCK

Description: PC BUFFER

Product Description/Features:
 • Max frequency supported = 266MHz (DDR 533)
 • Low skew, low jitter PLL clock driver
 • I2C address selection for dual buffer application
 • I2C for functional and output control
 • Feedback pins for input to output synchronization
 • Spread Spectrum tolerant inputs
 • 3.3V toerant CLK_INT input



Parameters

| | |
|-------------------------|----------------|
| Package | SSOP 28 (FY28) |
| Voltage | 3.3 V |
| Package | SSOP 28 |
| Speed | NA |
| Temperature | C |
| Status | Active |
| Sample | Yes |
| Minimum Order Quantity | 141 |
| Factory Order Increment | 47 |

Distributor Inventory

No Pricing information is available from our Distributors at this time.

Documents

| Type | Title | Size | Revision Date |
|------|------------------------|-------|---------------|
| Misc | PC Clocks Contact Info | 61 KB | 05/29/2007 |

Package

| | |
|----------------------------------|--------------------------|
| Description | SSOP 5.3 MM |
| Class | PLASTIC |
| Moisture Sensitivity Level (MSL) | 1 |
| Category | Standard |
| Moisture Exposure Floor Life | Unlimited @ <30°C/85% RH |
| Peak Reflow Temperature | 240°C |
| Rebake Conditions | NA |
| Length | 10.2 |
| Mark | R |
| Width | 5.3 |
| Pitch | 0.65 |
| Thickness | 1.73 |
| Status | Active |

Looking for pricing, stock, or lifecycle information?

Click below to explore more details on WIN SOURCE:

- ⊖ [View ICS93777BF on WIN SOURCE](#)
- ⊖ [IDT, Integrated Device Technology Inc Information](#)

Optimize Your Supply Chain with WIN SOURCE Solutions

- ✓ Global Sourcing Solution
- ✓ Obsolete Management
- ✓ Cost Control Management
- ✓ Shortage Management
- ✓ Alternative Solution
- ✓ Excess Inventory Management