

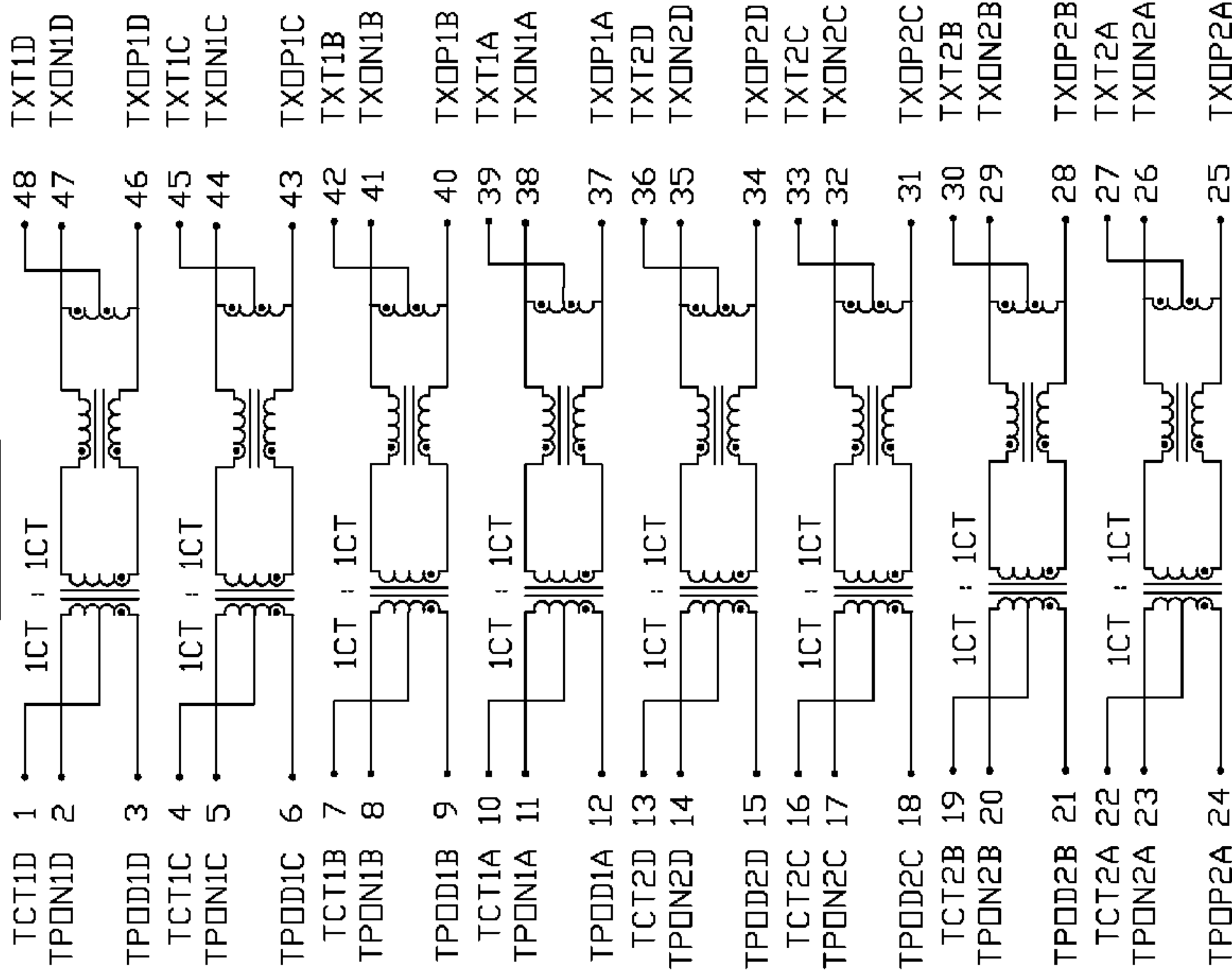


**THE DATASHEET OF
S558-5999-Q3-F**



THE INFORMATION CONTAINED HEREIN IS CONSIDERED 'PROPRIETARY' TO BEL FUSE INC. AND SHALL NOT BE COPIED, REPRODUCED OR DISCLOSED WITHOUT THE WRITTEN APPROVAL OF BEL FUSE INC.

SCHEMATIC



ELECTRICAL CHARACTER

TURNS RATIO
 Tx SIDE: 1CT : 1CT

OCL
 350μH MIN.
 @100KHZ, 10

PIN (46-47), (43-44), (40-41), (37-38)
 PIN (34-35), (31-32), (28-29), (25-26)

INSERTION LOSS

FREQ.	1M
MAX.	1.1

RETURN LOSS
 MEASURED UNDER A
 100 Ω ENVIRONMENT
 18dB MIN. @
 12-20LOGGCF

COMMON TO COMMON
 MODE REJECTION
 30dB MIN. @
 33-20LOGGCF

CROSSTALK
 33-20LOGGCF

HIPDT
 PER HAND-

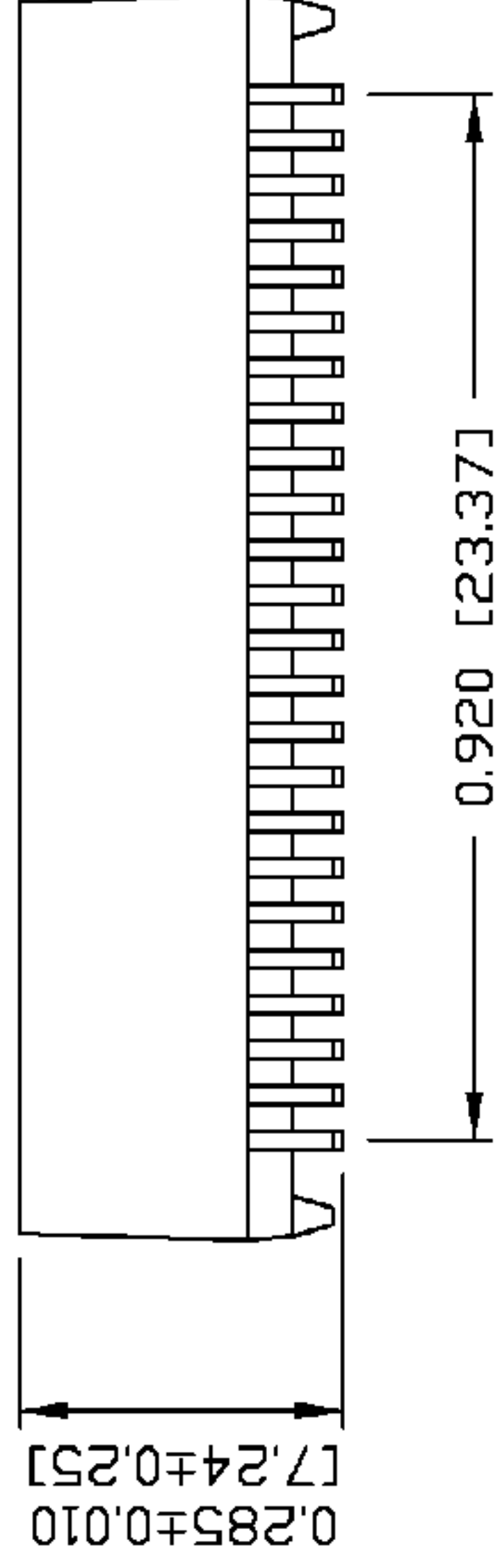
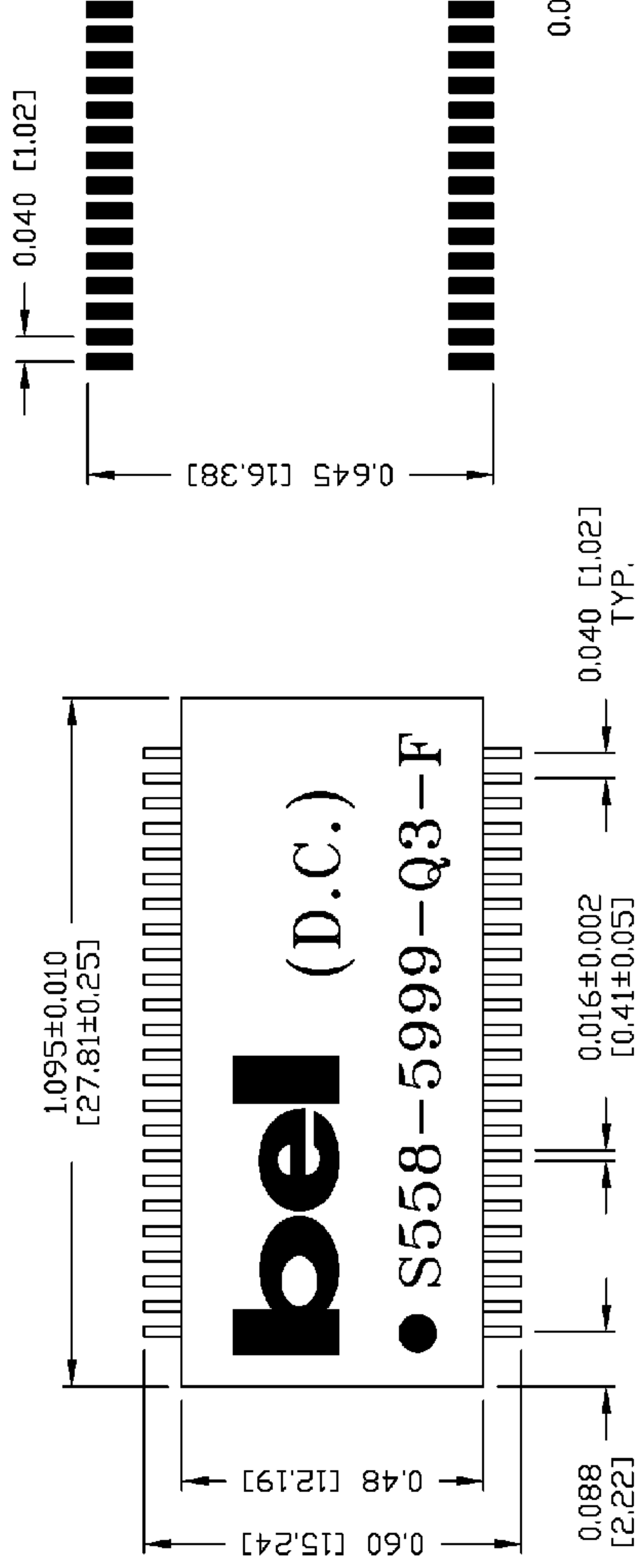
ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.
HD	08-10-05	DUAL 1000BASE-T	X5585999Q3-F
DRAWN BY	DATE		FILE NAME
DQ Chen	08-10-05	S558-5999-Q3-F	X5585999Q3FA.DWG

This document is electronically generated. This is a controlled copy if used

DC002(2)032505

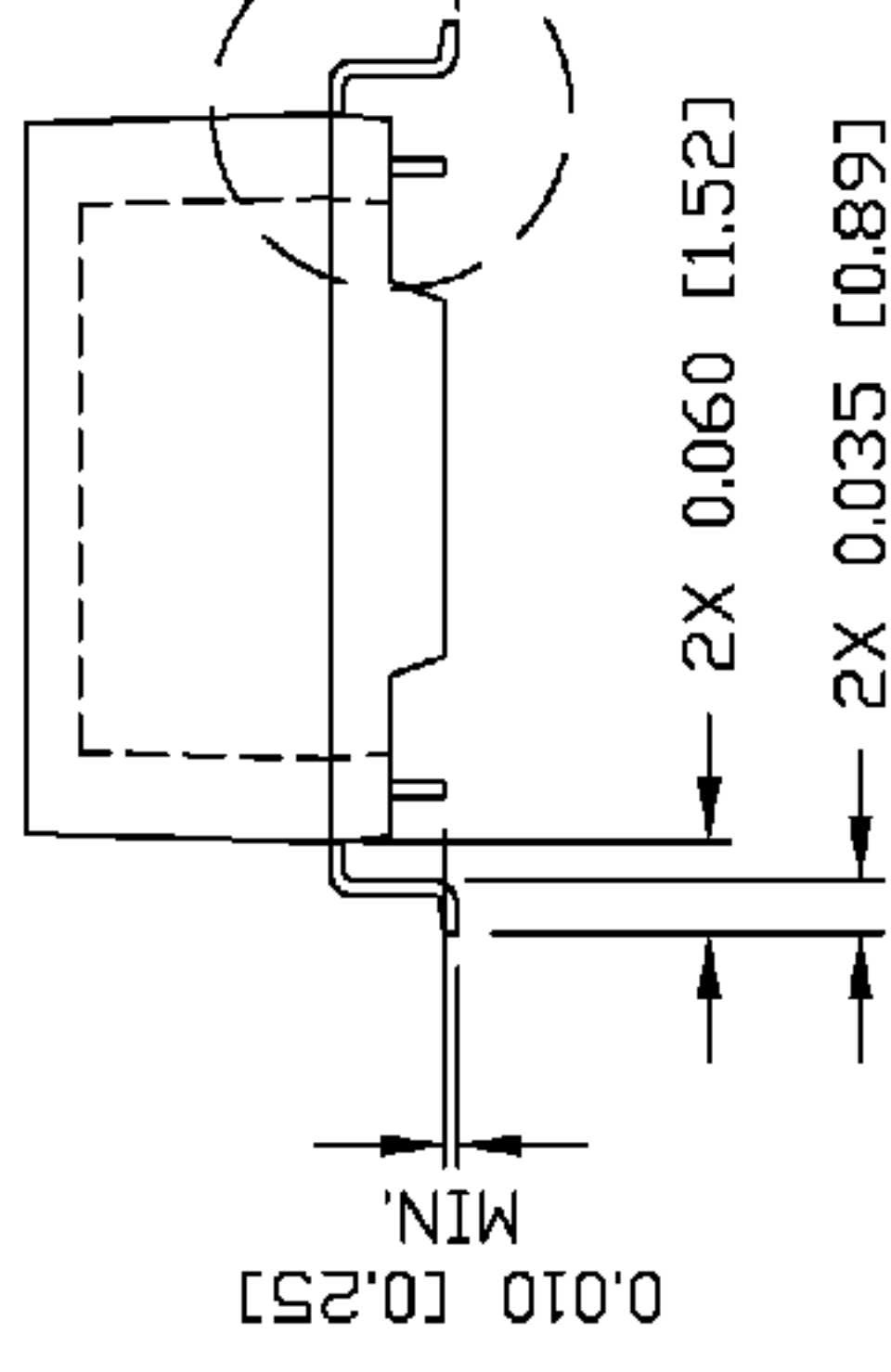
THE INFORMATION CONTAINED HEREIN IS CONSIDERED 'PROPRIETARY' TO BEL FUSE INC. AND SHALL NOT BE COPIED, REPRODUCED OR DISCLOSED WITHOUT THE WRITTEN APPROVAL OF BEL FUSE INC.

SUGGESTED PCB PA



NOTES:

1. STANDARD MARKING REFER TO DDC, HAND-WORK-04.
2. PACKAGE CODE: "QVS001".



ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM.
HD	08-10-05	MECHANICAL DRAWING	X5585999Q3-F	TOL. IN INCH
DRAWN BY	DATE	S558-5999-Q3-F	FILE NAME	.X ±0.01
DQ Chen	08-10-05		X5585999Q3FA.DWG	.XXX ±0.005

DC002(2)032505

This document is electronically generated. This is a controlled copy if used

Looking for pricing, stock, or lifecycle information?

Click below to explore more details on WIN SOURCE:

- [View S558-5999-Q3-F on WIN SOURCE](#)
- [Bel Fuse Inc. Information](#)

Optimize Your Supply Chain with WIN SOURCE Solutions

- ✓ Global Sourcing Solution
- ✓ Obsolete Management
- ✓ Cost Control Management
- ✓ Shortage Management
- ✓ Alternative Solution
- ✓ Excess Inventory Management