THE DATASHEET OF

LTC2900-1IMS

Programmable Quad Supply Monitor with Adjustable Reset Timer

FEATURES

- **Simultaneously Monitors Four Supplies**
- **16 User Selectable Combinations of 5V, 3.3V, 3V, 2.5V, 1.8V, 1.5V and/or ±Adjustable Voltage Thresholds**
- **Guaranteed Threshold Accuracy: ±1.5% of Monitored Voltage Over Temperature**
- **Low Supply Current: 43µA Typ**
- **Adjustable Reset Time**
- Small MSOP and 3mm × 3mm DFN Packages
- Manual Reset Pin
- Open-Drain $\overline{RST}$ Output (LTC2900-1)
- Push-Pull $\overline{RST}$ Output (LTC2900-2)
- Power Supply Glitch Immunity
- Guaranteed $\overline{RST}$ for $V_{CC} \geq 1V$

APPLICATIONS

- Desktop and Notebook Computers
- Multivoltage Systems
- Telecom Equipment
- Portable Battery-Powered Equipment
- Network Servers

DESCRIPTION

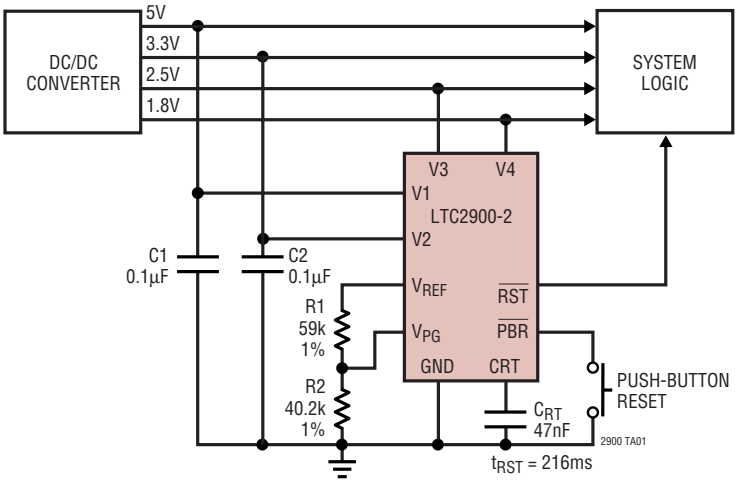
The LTC®2900 is a programmable supply monitor for systems with up to four supply voltages. One of 16 preset or adjustable voltage monitor combinations can be selected using an external resistor divider connected to the program pin. The preset voltage thresholds are accurate to ±1.5% over temperature.

The reset delay time is adjustable using an external capacitor and the manual reset input may be used with a momentary switch to issue reset pulses with programmed duration. Tight voltage threshold accuracy and glitch immunity ensure reliable reset operation without false triggering. The $\overline{RST}$ output is guaranteed to be in the correct state for V_{CC} down to 1V. The LTC2900-1 features an open-drain $\overline{RST}$ output, while the LTC2900-2 has a push-pull $\overline{RST}$ output.

The 43µA supply current makes the LTC2900 ideal for power conscious systems and it may be configured to monitor less than four inputs. The parts are available in the 10-lead MSOP and the 10-lead 3mm × 3mm DFN packages.

TYPICAL APPLICATION

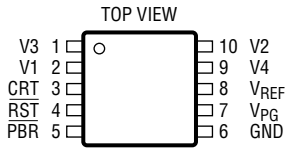
Quad Supply Monitor (5V, 3.3V, 2.5V, 1.8V)



ABSOLUTE MAXIMUM RATINGS (Notes 1, 2, 3)

V1, V2, V3, V4, V_{PG}, $\overline{PBR}$ ..... –0.3V to 7V
$\overline{RST}$ (LTC2900-1) ..... –0.3V to 7V
RST (LTC2900-2) ..... –0.3V to (V2 + 0.3V)
CRT ..... –0.3V to (V_{CC} + 0.3V)
V_{REF} ..... –0.3V to (V_{CC} + 0.3V)
Reference Load Current (I_{VREF}) ..... ±1mA
V4 Input Current (–ADJ Mode) ..... –1mA
Operating Temperature Range
LTC2900-1C/LTC2900-2C ..... 0°C to 70°C
LTC2900-1I/LTC2900-2I ..... –40°C to 85°C
Storage Temperature Range ..... –65°C to 150°C
Lead Temperature (Soldering, 10 sec) ..... 300°C

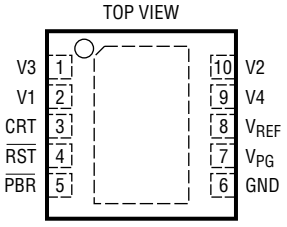
PACKAGE/ORDER INFORMATION



MS PACKAGE
10-LEAD PLASTIC MSOP

T_{JMAX} = 125°C, θ_{JA} = 250°C/W

ORDER PART NUMBER
LTC2900-1CMS LTC2900-2CMS LTC2900-1IMS LTC2900-2IMS
MS PART MARKING
LTYJ LTYL LTYK LTYM



DD PACKAGE
10-LEAD (3mm × 3mm) PLASTIC DFN

T_{JMAX} = 125°C, θ_{JA} = 43°C/W

ORDER PART NUMBER
LTC2900-1CDD LTC2900-2CDD LTC2900-1IDD LTC2900-2IDD
DD PART MARKING
LABU LABW LABV LABX

Consult LTC Marketing for parts specified with wider operating temperature ranges.

ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at T_A = 25°C. V_{CC} = 5V, unless otherwise noted. (Note 3)

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
V_{RT50}	5V, 5% Reset Threshold	V1 Input Threshold	●	4.600	4.675	4.750	V
V_{RT33}	3.3V, 5% Reset Threshold	V1, V2 Input Threshold	●	3.036	3.086	3.135	V
V_{RT30}	3V, 5% Reset Threshold	V2 Input Threshold	●	2.760	2.805	2.850	V
V_{RT25}	2.5V, 5% Reset Threshold	V2, V3 Input Threshold	●	2.300	2.338	2.375	V
V_{RT18}	1.8V, 5% Reset Threshold	V3, V4 Input Threshold	●	1.656	1.683	1.710	V
V_{RT15}	1.5V, 5% Reset Threshold	V3, V4 Input Threshold	●	1.380	1.403	1.425	V
V_{RTA}	ADJ Reset Threshold	V3, V4 Input Threshold	●	0.492	0.500	0.508	V
V_{RTAN}	–ADJ Reset Threshold	V4 Input Threshold	●	–18	0	18	mV
V_{CC}	Minimum Internal Operating Voltage	$\overline{RST}$ in Correct Logic State, V_{CC} Rising Prior to Program	●			1	V
V_{CCMINP}	Minimum Required for Programming	V_{CC} Rising	●			2.42	V
V_{REF}	Reference Voltage	$V_{CC} \geq$ 2.3V, I_{VREF} = ±1mA, $C_{REF} \leq$ 1000pF	●	1.192	1.210	1.228	V
V_{PG}	Programming Voltage Range	$V_{CC} \geq V_{CCMINP}$	●	0		V_{REF}	V
I_{VPG}	V_{PG} Input Current	$V_{PG} = V_{REF}$	●			±20	nA
I_{V1}	V1 Input Current	V1 = 5V, I_{VREF} = 12µA, (Note 4)	●		43	75	µA

ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at T_A = 25°C. V_{CC} = 5V, unless otherwise noted. (Note 3)

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
I_{V2}	V2 Input Current	V2 = 3.3V	●		0.8	2	µA
I_{V3}	V3 Input Current	V3 = 2.5V V3 = 0.55V (ADJ Mode)	● ●	 –15	0.52	1.2 15	µA nA
I_{V4}	V4 Input Current	V4 = 1.8V V4 = 0.55V (ADJ Mode) V4 = –0.05V (–ADJ Mode)	● ● ●	 –15 –15	0.34	0.8 15 15	µA nA nA
$I_{CRT(UP)}$	CRT Pull-Up Current	V_{CRT} = 0V	●	–1.4	–2	–2.6	µA
$I_{CRT(DN)}$	CRT Pull-Down Current	V_{CRT} = 1.3V	●	10	20	30	µA
t_{RST}	Reset Time-Out Period	C_{RT} = 1500pF	●	5	7	9	ms
t_{UV}	V_X Undervoltage Detect to $\overline{RST}$	V_X Less Than Reset Threshold V_{RTX} by More Than 1%			150		µs
V_{OL}	Output Voltage Low $\overline{RST}$	I_{SINK} = 2.5mA; V1 = 3V, V2 = 3V; V3, V4 = 0V; V_{PG} = 0V	●		0.15	0.4	V
		I_{SINK} = 100µA; V2 = 1V; V1, V3, V4 = 0V I_{SINK} = 100µA; V1 = 1V; V2, V3, V4 = 0V	● ●		0.05 0.05	0.3 0.3	V V
V_{OH}	Output Voltage High $\overline{RST}$ (LTC2900-1) (Note 5)	I_{SOURCE} = 1µA	●	V2 – 1			V
	Output Voltage High $\overline{RST}$ (LTC2900-2) (Note 6)	I_{SOURCE} = 200µA	●	0.8 • V2			V
Manual Reset Pin							
V_{IH}	$\overline{PBR}$ Input Threshold High	V_{CC} = 3.3V to 5.5V	●			1.6	V
V_{IL}	$\overline{PBR}$ Input Threshold Low	V_{CC} = 3.3V to 5.5V	●	0.4			V
t_{PBW}	$\overline{PBR}$ Input Pulse Width	V_{CC} = 3.3V	●	150			ns
t_{PBD}	Manual Reset Propagation Delay	V_{CC} = 3.3V, $V_{\overline{PBR}}$ Falling	●		0.1	1	µs
I_{PBR}	$\overline{PBR}$ Pull-Up Current	$V_{\overline{PBR}}$ = 0V			–10		µA

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: All currents into pins are positive, all voltages are referenced to GND unless otherwise noted.

Note 3: The greater of V1, V2 is the internal supply voltage (V_{CC}).

Note 4: Under static no-fault conditions, V1 will necessarily supply quiescent current. If at any time V2 is larger than V1, V2 must be capable of supplying the quiescent current, programming (transient) current and reference load current.

Note 5: The $\overline{RST}$ output pin on the LTC2900-1 has an internal pull-up to V2 of typically 6µA. However, an external pull-up resistor may be used when faster rise times are required or for V_{OH} voltages greater than V2.

Note 6: The push-pull $\overline{RST}$ output pin on the LTC2900-2 is *actively* pulled up to V2.

TEST CIRCUITS

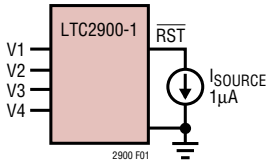


Figure 1. $\overline{RST}$ V_{OH} Test

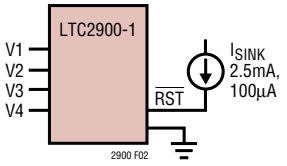


Figure 2. $\overline{RST}$ V_{OL} Test

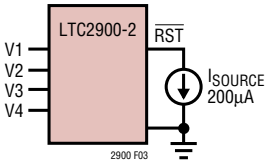
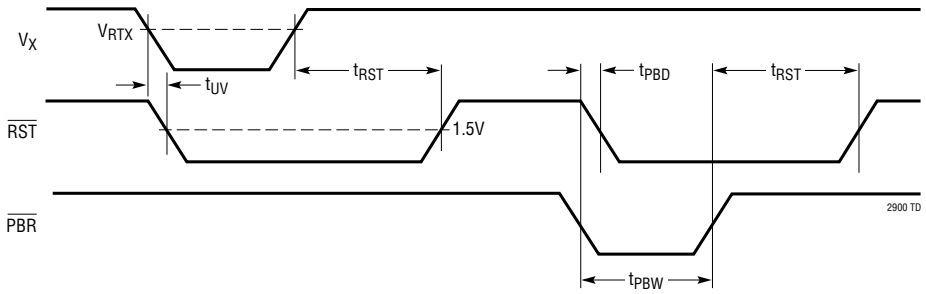


Figure 3. Active Pull-Up $\overline{RST}$ V_{OH} Test

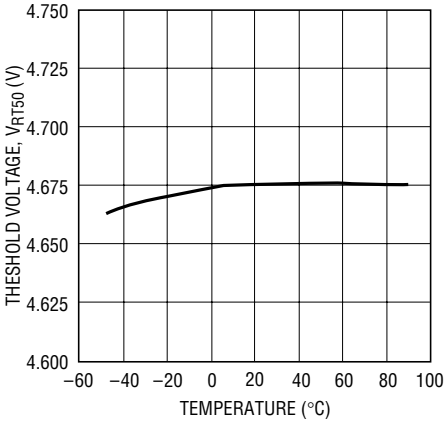
TIMING DIAGRAM

Monitor Timing

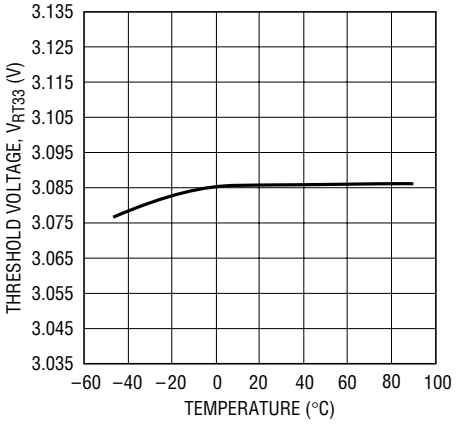


TYPICAL PERFORMANCE CHARACTERISTICS

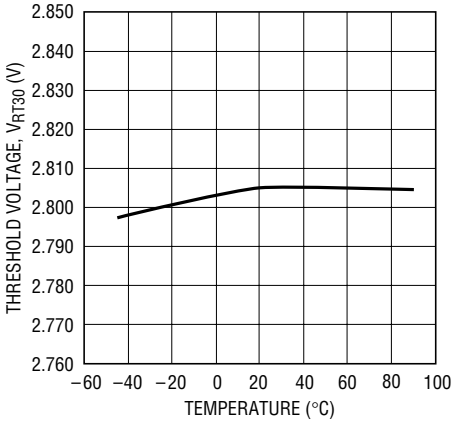
5V Threshold Voltage vs Temperature



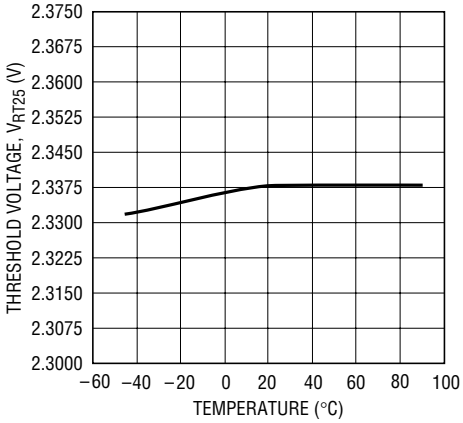
3.3V Threshold Voltage vs Temperature



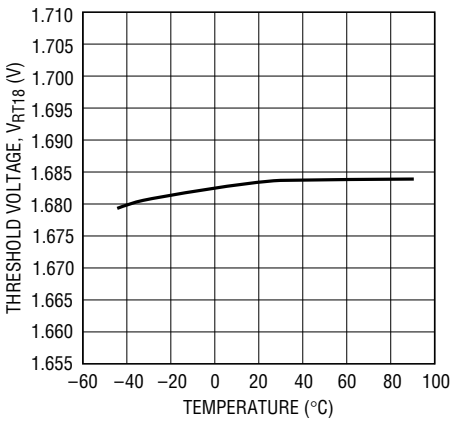
3V Threshold Voltage vs Temperature



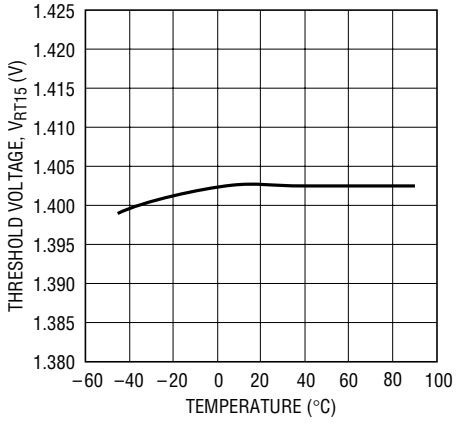
2.5V Threshold Voltage vs Temperature



1.8V Threshold Voltage vs Temperature

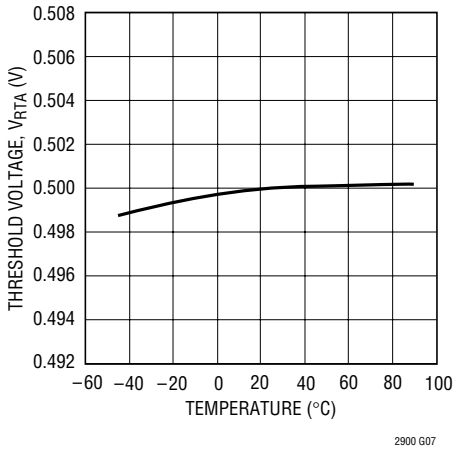


1.5V Threshold Voltage vs Temperature

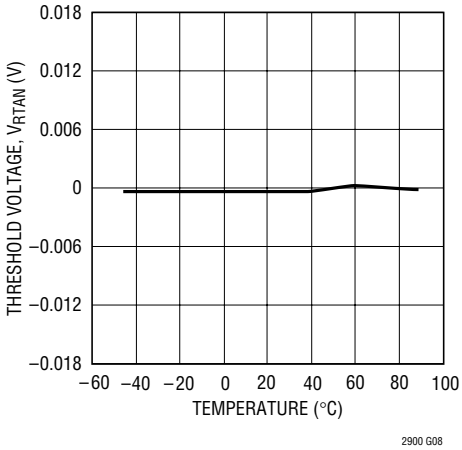


TYPICAL PERFORMANCE CHARACTERISTICS

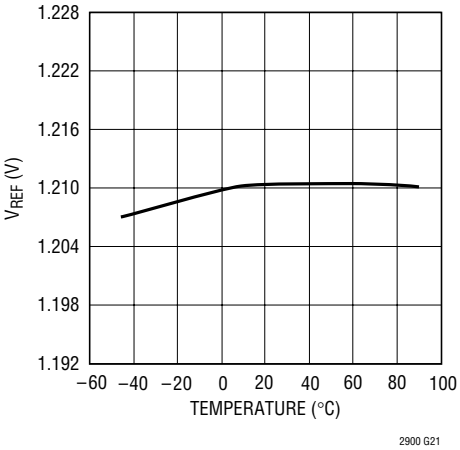
ADJ Threshold Voltage vs Temperature



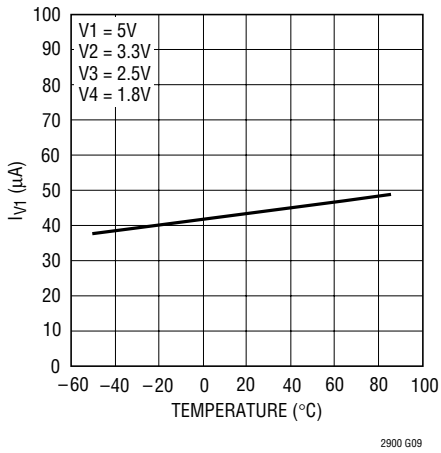
–ADJ Threshold Voltage vs Temperature



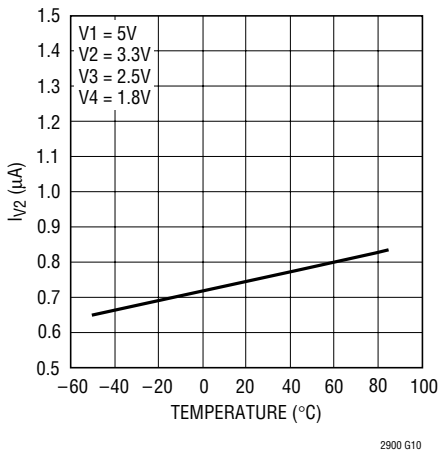
V_{REF} vs Temperature



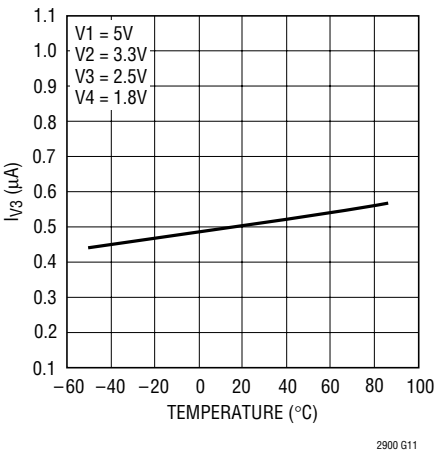
I_{V1} vs Temperature



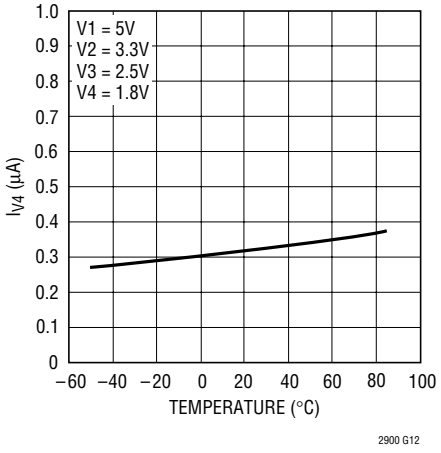
I_{V2} vs Temperature



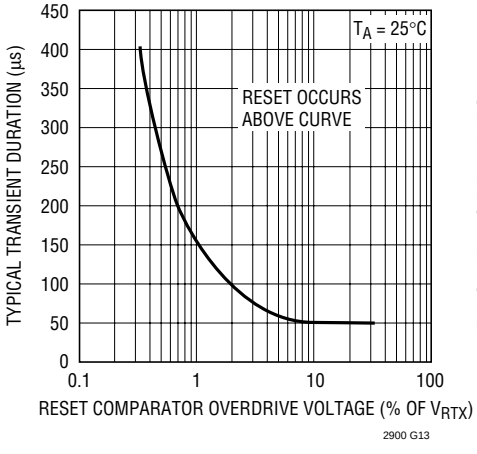
I_{V3} vs Temperature



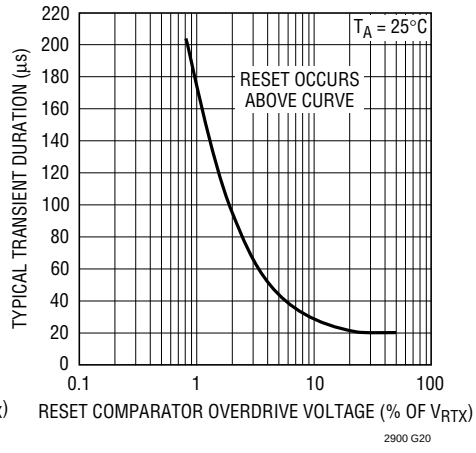
I_{V4} vs Temperature



Typical Transient Duration vs Comparator Overdrive (V1, V2)

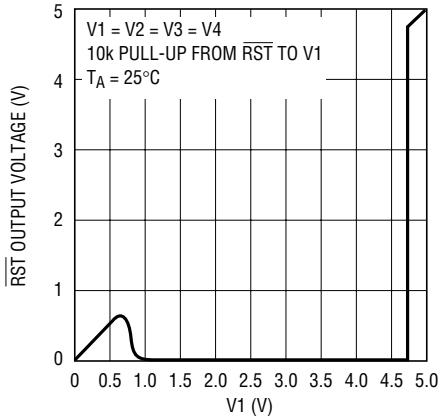


Typical Transient Duration vs Comparator Overdrive (V3, V4)



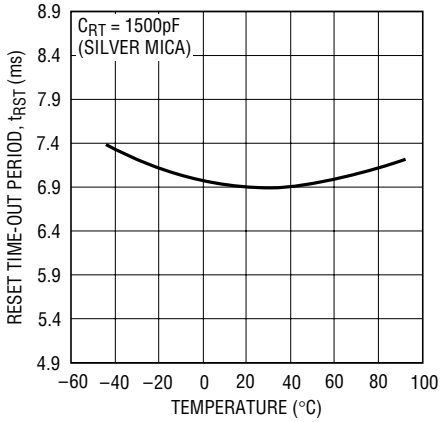
TYPICAL PERFORMANCE CHARACTERISTICS

$\overline{RST}$ Output Voltage vs V1, V_{PG} = 0V



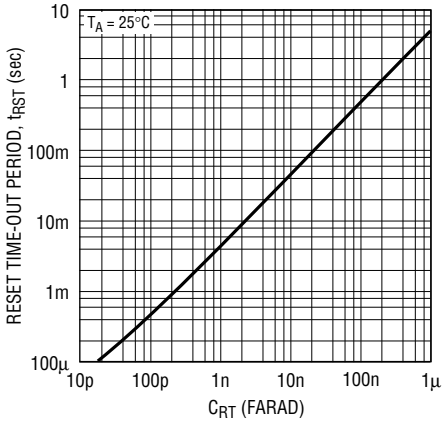
2900 G14

Reset Time-Out Period vs Temperature



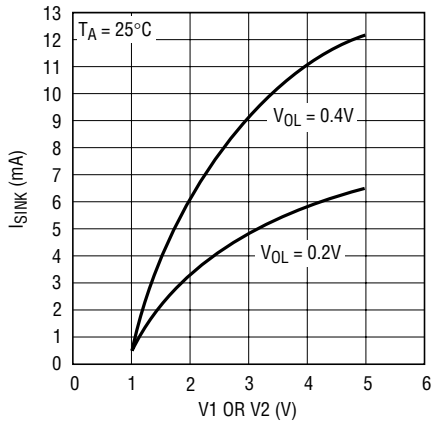
2900 G15

Reset Time-Out Period vs Capacitance



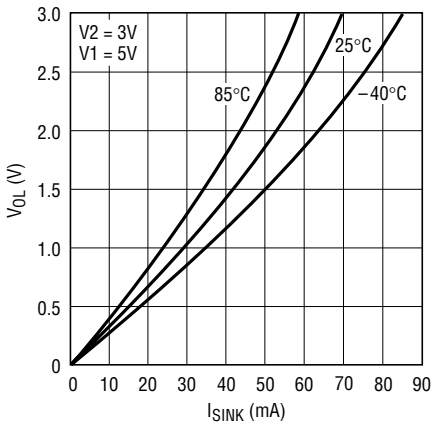
2900 G16

$\overline{RST}$, I_{SINK} vs Supply Voltage



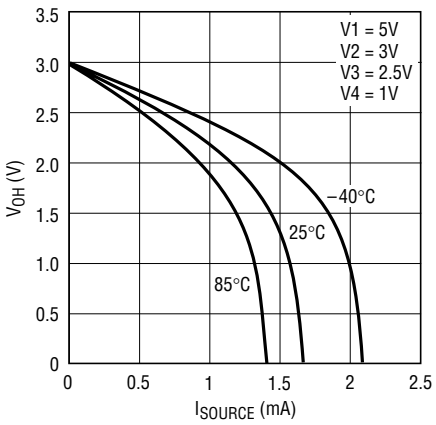
2900 G17

$\overline{RST}$ Voltage Output Low vs Output Sink Current



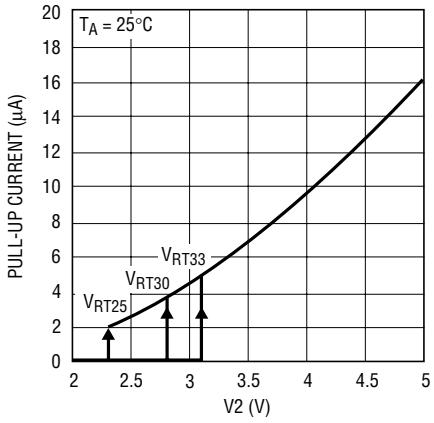
2900 G18

$\overline{RST}$ High Level Output Voltage vs Output Source Current (LTC2900-2)



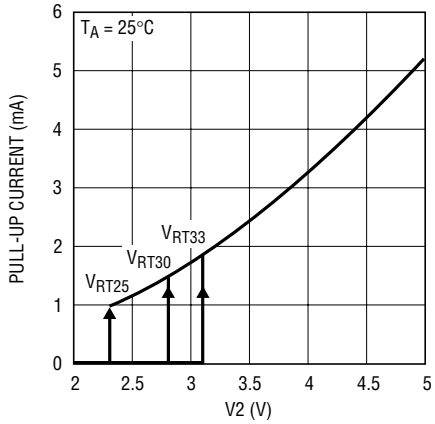
2900 G19

$\overline{RST}$ Pull-Up Current vs V2 (LTC2900-1)



2900 G22

$\overline{RST}$ Pull-Up Current vs V2 (LTC2900-2)



2900 G23

PIN FUNCTIONS

V3 (Pin 1): Voltage Input 3. Select from 2.5V, 1.8V, 1.5V or ADJ. See Table 1 for details.

V1 (Pin 2): Voltage Input 1. Select from 5V or 3.3V. See Table 1 for details. The greater of (V1, V2) is also V_{CC} for the device. Bypass this pin to ground with a 0.1µF (or greater) capacitor.

CRT (Pin 3): Reset Delay Time Programming Pin. Attach an external capacitor (C_{RT}) to GND to set a reset delay time of 4.6ms/nF. Leaving the pin open generates a minimum delay of approximately 50µs. A 47nF capacitor will generate a 216ms reset delay time.

$\overline{\text{RST}}$ (Pin 4): Reset Logic Output. Active low with weak pull-up to V2 (LTC2900-1) or active pull-up to V2 (LTC2900-2). Pulls low when any voltage input is below the reset threshold and held low for the programmed delay time after all voltage inputs are above threshold. May be pulled above V2 using an external pull-up (LTC2900-1 only).

$\overline{\text{PBR}}$ (Pin 5): Manual Reset Pin. Attach a push-button switch between this pin and ground. A logic low on this pin will pull $\overline{\text{RST}}$ low. When the $\overline{\text{PBR}}$ pin returns high, $\overline{\text{RST}}$ will return high after the programmed reset delay assuming all four voltage inputs are above threshold. A weak internal pull-up allows the pin to be left floating for normal monitor operation. When using a switch, the switch is debounced through the reset circuitry using the delay provided by the C_{RT} timing capacitor.

GND (Pin 6): Ground.

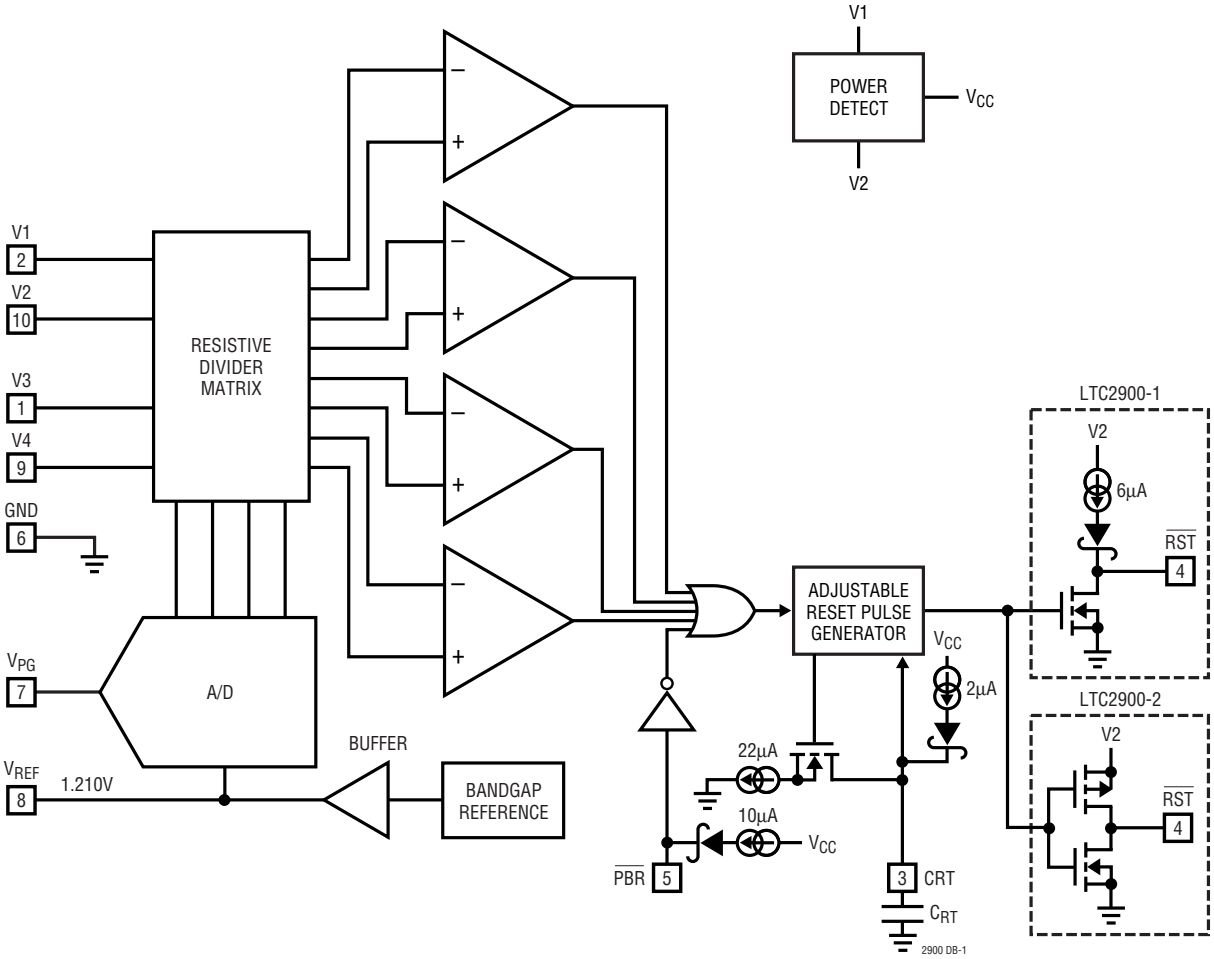
V_{PG} (Pin 7): Voltage Threshold Combination Select Input. Connect to an external 1% resistive divider between V_{REF} and GND to select 1 of 16 combinations of preset and/or ±adjustable voltage thresholds (see Table 1). Do not add capacitance on the V_{PG} pin.

V_{REF} (Pin 8): Buffered Reference Voltage. A 1.210V nominal reference used for the programming voltage (V_{PG}) and for the offset of negative adjustable applications. The buffered reference can source and sink up to 1mA. The reference can drive a bypass capacitor of up to 1000pF without oscillation.

V4 (Pin 9): Voltage Input 4. Select from 1.8V, 1.5V, ADJ or –ADJ. See Table 1 for details.

V2 (Pin 10): Voltage Input 2. Select from 3.3V, 3V or 2.5V. See Table 1 for details. The greater of (V1, V2) is also V_{CC} for the device. Bypass this pin to ground with a 0.1µF (or greater) capacitor. $\overline{\text{RST}}$ is weakly pulled up to V2 (LTC2900-1). $\overline{\text{RST}}$ is actively pulled up to V2 in the LTC2900-2.

BLOCK DIAGRAM



APPLICATIONS INFORMATION

Power-Up

The greater of V1, V2 is the internal supply voltage (V_{CC}). On power-up, V_{CC} will power the drive circuits for the $\overline{RST}$ pin. This ensures that the $\overline{RST}$ output will be low as soon as V1 or V2 reaches 1V. The $\overline{RST}$ output will remain low until the part is programmed. After programming, if any one of the V_X inputs is below its programmed threshold, $\overline{RST}$ will be a logic low. Once all the V_X inputs rise above their thresholds, an internal timer is started and $\overline{RST}$ is released after the programmed delay time. If $V_{CC} < (V3 - 1)$ and $V_{CC} < 2.4V$, the V3 input impedance will be low (1kΩ typ).

Monitor Programming

The LTC2900 input voltage combination is selected by placing the recommended resistive divider from V_{REF} to GND and connecting the tap point to V_{PG}, as shown in Figure 4. Table 1 offers recommended 1% resistor values

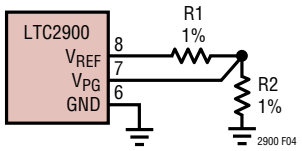


Figure 4. Monitor Programming

for the various modes. The last column in Table 1 specifies optimum V_{PG}/V_{REF} ratios (±0.01) to be used when programming with a ratiometric DAC.

During power-up, once V1 or V2 reaches 2.4V max, the monitor enters a programming period of approximately 150µs during which the voltage on the V_{PG} pin is sampled and the monitor is configured to the desired input combination. Do not add capacitance to the V_{PG} pin. Immediately after programming, the comparators are enabled and supply monitoring will begin.

Table 1. Voltage Threshold Programming

MODE	V1 (V)	V2 (V)	V3 (V)	V4 (V)	R1 (kΩ)	R2 (kΩ)	$\frac{V_{PG}}{V_{REF}}$
0	5.0	3.3	ADJ	ADJ	Open	Short	0.000
1	5.0	3.3	ADJ	–ADJ	93.1	9.53	0.094
2	3.3	2.5	ADJ	ADJ	86.6	16.2	0.156
3	3.3	2.5	ADJ	–ADJ	78.7	22.1	0.219
4	3.3	2.5	1.5	ADJ	71.5	28.0	0.281
5	5.0	3.3	2.5	ADJ	66.5	34.8	0.344
6	5.0	3.3	2.5	1.8	59.0	40.2	0.406
7	5.0	3.3	2.5	1.5	53.6	47.5	0.469
8	5.0	3.0	2.5	ADJ	47.5	53.6	0.531
9	5.0	3.0	ADJ	ADJ	40.2	59.0	0.594
10	3.3	2.5	1.8	1.5	34.8	66.5	0.656
11	3.3	2.5	1.8	ADJ	28.0	71.5	0.719
12	3.3	2.5	1.8	–ADJ	22.1	78.7	0.781
13	5.0	3.3	1.8	–ADJ	16.2	86.6	0.844
14	5.0	3.3	1.8	ADJ	9.53	93.1	0.906
15	5.0	3.0	1.8	ADJ	Short	Open	1.000

Supply Monitoring

The LTC2900 is a low power, high accuracy programmable quad supply monitoring circuit with a common reset output and a manual reset input. Reset timing is adjustable using an external capacitor. Single pin programming selects 1 of 16 input voltage monitor combinations. All four voltage inputs must be above predetermined thresholds for the reset not to be invoked. The LTC2900 will assert the reset output during power-up, power-down and brownout conditions on any one of the voltage inputs.

The inverting inputs on the V3 and/or V4 comparators are set to 0.5V when the positive adjustable modes are selected (Figure 5). The tap point on an external resistive divider, connected between the positive voltage being

APPLICATIONS INFORMATION

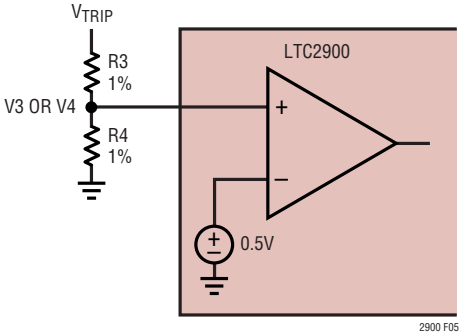


Figure 5. Setting the Positive Adjustable Trip Point

sensed and ground, is connected to the high impedance noninverting inputs (V3, V4). The trip voltage is calculated from:

$$V_{TRIP} = 0.5V\left(1+\frac{R3}{R4}\right)$$

In the negative adjustable mode, the noninverting input on the V4 comparator is connected to ground (Figure 6). The tap point on an external resistive divider, connected between the negative voltage being sensed and the V_{REF} pin, is connected to the high impedance inverting input (V4). V_{REF} provides the necessary level shift required to operate at ground. The trip voltage is calculated from:

$$V_{TRIP} = -V_{REF}\left(\frac{R3}{R4}\right); V_{REF} = 1.210V\ Nominal$$

In a negative adjustable application, the minimum value for R4 is limited by the sourcing capability of V_{REF} (±1mA). With no other load on V_{REF}, R4 (minimum) is:

1.21V ÷ 1mA = 1.21kΩ.

Tables 2 and 3 offer suggested 1% resistor values for various adjustable applications.

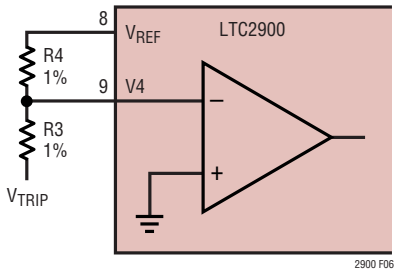


Figure 6. Setting the Negative Adjustable Trip Point

Table 2. Suggested 1% Resistor Values for the ADJ Inputs

V_{SUPPLY} (V)	V_{TRIP} (V)	R3 (kΩ)	R4 (kΩ)
12	11.25	2150	100
10	9.4	1780	100
8	7.5	1400	100
7.5	7	1300	100
6	5.6	1020	100
5	4.725	845	100
3.3	3.055	511	100
3	2.82	464	100
2.5	2.325	365	100
1.8	1.685	237	100
1.5	1.410	182	100
1.2	1.120	124	100
1	0.933	86.6	100
0.9	0.840	68.1	100

Table 3. Suggested 1% Resistor Values for the –ADJ Input

V_{SUPPLY} (V)	V_{TRIP} (V)	R3 (kΩ)	R4 (kΩ)
–2	–1.87	187	121
–5	–4.64	464	121
–5.2	–4.87	487	121
–10	–9.31	931	121
–12	–11.30	1130	121

APPLICATIONS INFORMATION

Although all four supply monitor comparators have built-in glitch immunity, bypass capacitors on V1 and V2 are recommended because the greater of V1 or V2 is also the V_{CC} for the device. Filter capacitors on the V3 and V4 inputs are allowed.

Power-Down

On power-down, once any of the V_X inputs drop below their threshold, $\overline{RST}$ is held at a logic low. A logic low of 0.4V is guaranteed until both V1 and V2 drop below 1V. If the bandgap reference becomes invalid (V_{CC} < 2V typ), the part will reprogram once V_{CC} rises above 2.4V max.

Monitor Output Rise and Fall Time Estimation

The $\overline{RST}$ output has strong pull-down capability. If the external load capacitance (C_{LOAD}) is known, output fall time (10% to 90%) is estimated using:

$$t_{FALL} \approx 2.2 \bullet R_{PD} \bullet C_{LOAD}$$

where R_{PD} is the on-resistance of the internal pull-down transistor. The typical performance curve (V_{OL} vs I_{SINK}) demonstrates that the pull-down current is somewhat linear versus output voltage. Using the 25°C curve, R_{PD} is estimated to be approximately 40Ω. Assuming a 150pF load capacitance, the fall time is about 13.2ns.

Although the $\overline{RST}$ output of the LTC2900-1 is considered to be "open-drain," it does have weak pull-up capability (see RST Pull-Up Current vs V2 curve). Output rise time (10% to 90%) is estimated using:

$$t_{RISE} \approx 2.2 \bullet R_{PU} \bullet C_{LOAD}$$

where R_{PU} is the on-resistance of the pull-up transistor. The on-resistance as a function of the V2 voltage at room temperature is estimated using:

$$R_{PU} = \frac{6 \bullet 10^5}{V2 - 1}\Omega$$

with V2 = 3.3V, R_{PU} is about 260k. Using 150pF for load capacitance, the rise time is 86µs. If the output needs to pull up faster and/or to a higher voltage, a smaller external pull-up resistor may be used. Using a 10k pull-up resistor, the rise time is reduced to 3.3µs for a 150pF load capacitance.

The LTC2900-2 has an active pull-up to V2 on the $\overline{RST}$ output. The typical performance curve ($\overline{RST}$ Pull-Up Current vs V2 curve) demonstrates that the pull-up current is somewhat linear versus the V2 voltage and R_{PU} is estimated to be approximately 625Ω. A 150pF load capacitance makes the rise time about 206ns.

Selecting the Reset Timing Capacitor

The reset time-out period is adjustable in order to accommodate a variety of microprocessor applications. The reset time-out period, t_{RST}, is adjusted by connecting a capacitor, C_{RT}, between the CRT pin and ground. The value of this capacitor is determined by:

$$C_{RT} = t_{RST} \bullet 217 \bullet 10^{-9}$$

with C_{RT} in Farads and t_{RST} in seconds. The C_{RT} value per millisecond of delay can also be expressed as C_{RT}/ms = 217 (pF/ms).

Leaving the CRT pin unconnected will generate a minimum reset time-out of approximately 50µs. Maximum reset time-out is limited by the largest available low leakage capacitor. The accuracy of the time-out period will be affected by capacitor leakage (the nominal charging current is 2µA) and capacitor tolerance. A low leakage ceramic capacitor is recommended.

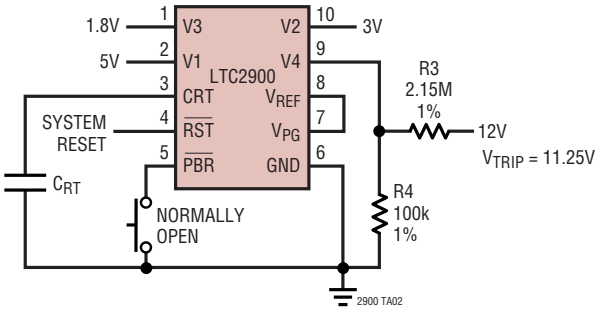
APPLICATIONS INFORMATION

Ensuring Reset Valid for V_{CC} Down to 0V (LTC2900-2)

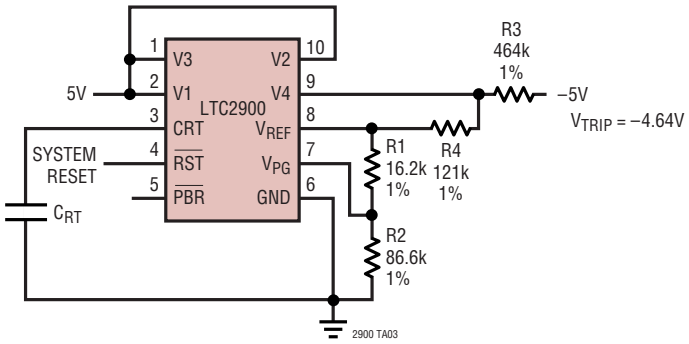
Some applications require the reset output ($\overline{RST}$) to be valid with V_{CC} down to 0V. The LTC2900-2 is designed to handle this requirement with the addition of an external resistor from $\overline{RST}$ to ground. The resistor will provide a path for stray charge and/or leakage currents, preventing the $\overline{RST}$ output from floating to undetermined voltages when connected to high impedance (such as CMOS logic inputs). The resistor value should be small enough to provide effective pull-down without excessively loading the active pull-up circuitry. Too large a value may not pull down well enough. A 100k resistor from $\overline{RST}$ to ground is satisfactory for most applications.

TYPICAL APPLICATIONS

Quad Supply Monitor with Push-Button Reset
5V, 3V, 1.8V, 12V (ADJ)

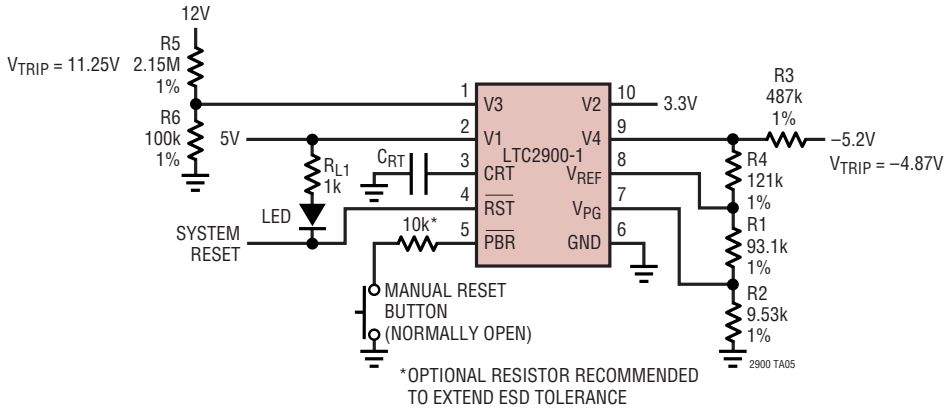


5V, –5V Monitor and Unused V2, V3 Inputs
Pulled Above Trip Thresholds

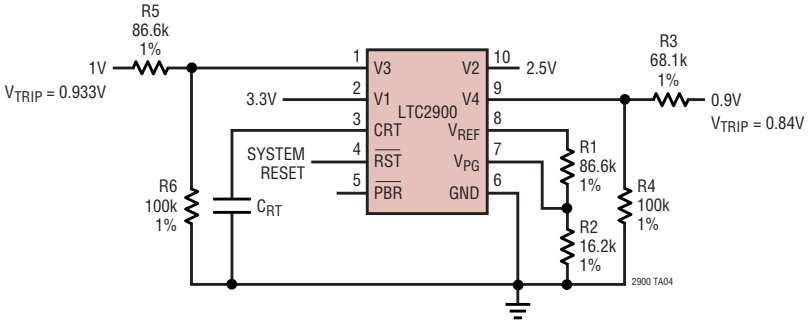


TYPICAL APPLICATIONS

5V, 3.3V, 12V, –5.2V Monitor with Manual Reset and LED Indication on $\overline{RST}$

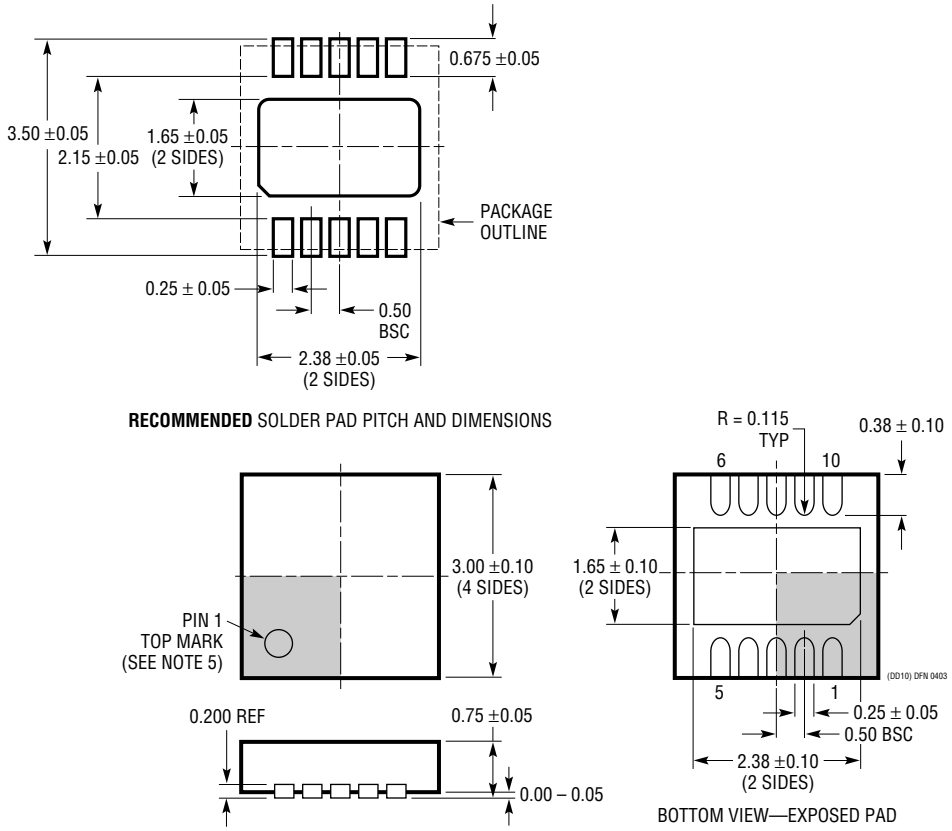


Low Voltage Quad Supply Monitor 3.3V, 2.5V, 1V (ADJ), 0.9V (ADJ)



PACKAGE DESCRIPTION

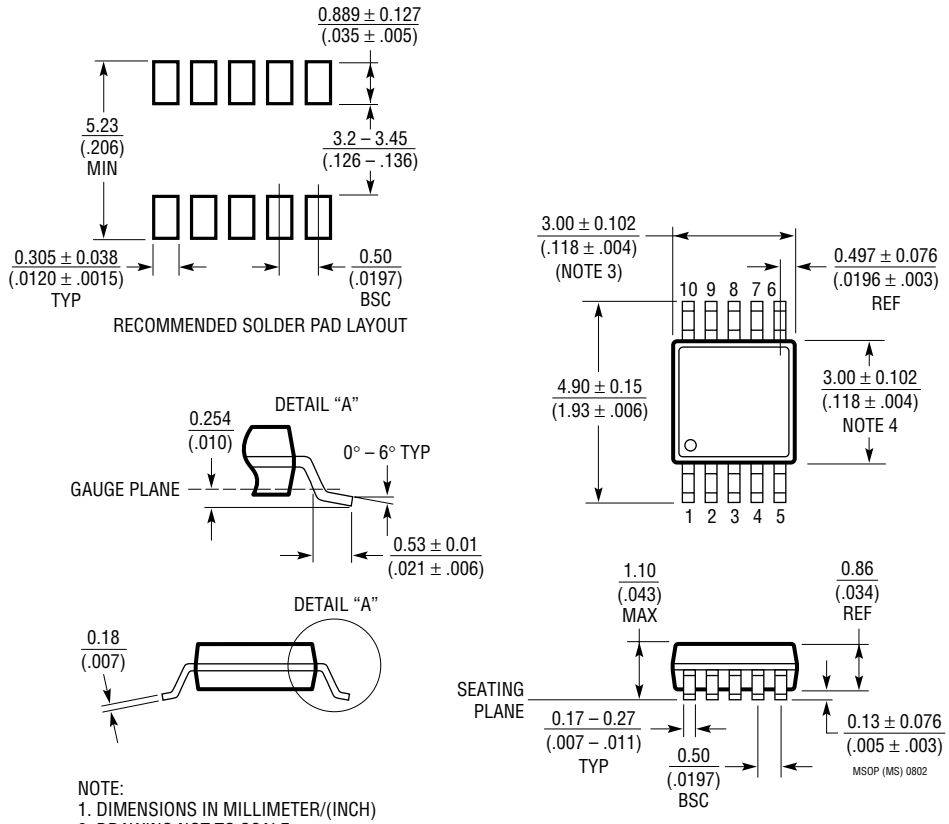
DD Package
10-Lead Plastic DFN (3mm × 3mm)
(Reference LTC DWG # 05-08-1699)



NOTE:
1. DRAWING TO BE MADE A JEDEC PACKAGE OUTLINE M0-229 VARIATION OF (WEED-2).
   CHECK THE LTC WEBSITE DATA SHEET FOR CURRENT STATUS OF VARIATION ASSIGNMENT
2. ALL DIMENSIONS ARE IN MILLIMETERS
3. DIMENSIONS OF EXPOSED PAD ON BOTTOM OF PACKAGE DO NOT INCLUDE
   MOLD FLASH. MOLD FLASH, IF PRESENT, SHALL NOT EXCEED 0.15mm ON ANY SIDE
4. EXPOSED PAD SHALL BE SOLDER PLATED
5. SHADED AREA IS ONLY A REFERENCE FOR PIN 1 LOCATION ON THE
   TOP AND BOTTOM OF PACKAGE

PACKAGE DESCRIPTION

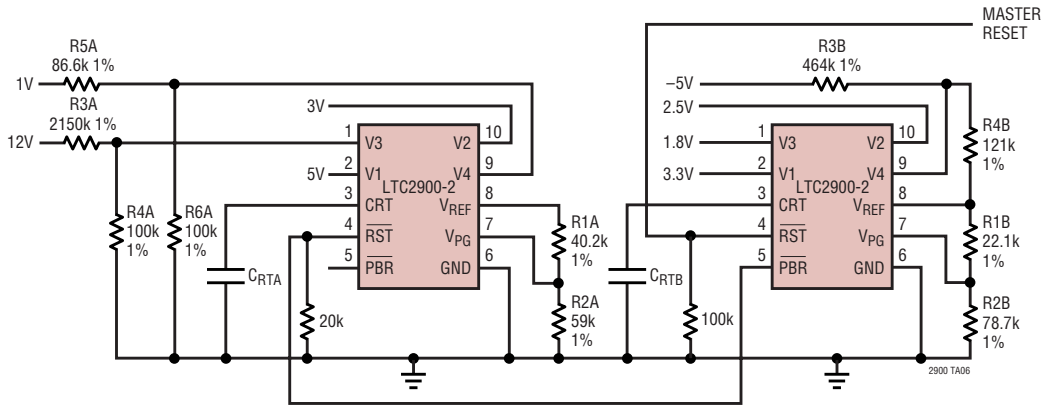
MS Package
10-Lead Plastic MSOP
(Reference LTC DWG # 05-08-1661)



NOTE:
1. DIMENSIONS IN MILLIMETER/(INCH)
2. DRAWING NOT TO SCALE
3. DIMENSION DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
   MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.152mm (.006") PER SIDE
4. DIMENSION DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
   INTERLEAD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.152mm (.006") PER SIDE
5. LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.102mm (.004") MAX

TYPICAL APPLICATION

Monitor Eight Supplies Using Supervisory Cascade
12V (ADJ), 5V, 3.3V, 3V, 2.5V, 1.8V, 1V (ADJ), –5V (–ADJ)



RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LTC690	5V Supply Monitor, Watchdog Timer and Battery Backup	4.65V Threshold
LTC694-3.3	3.3V Supply Monitor, Watchdog Timer and Battery Backup	2.9V Threshold
LTC699	5V Supply Monitor and Watchdog Timer	4.65V Threshold
LTC1232	5V Supply Monitor, Watchdog Timer and Push-Button Reset	4.37V/4.62V Threshold
LTC1326	Micropower Precision Triple Supply Monitor for 5V, 3.3V and ADJ	4.725V, 3.118V, 1V Thresholds (±0.75%)
LTC1326-2.5	Micropower Precision Triple Supply Monitor for 2.5V, 3.3V and ADJ	2.363V, 3.118V, 1V Thresholds (±0.75%)
LTC1536	Precision Triple Supply Monitor for PCI Applications	Meets PCI t_{FAIL} Timing Specifications
LTC1726-2.5	Micropower Triple Supply Monitor for 2.5V, 3.3V and ADJ	Adjustable $\overline{\text{RESET}}$ and Watchdog Time-Outs
LTC1726-5	Micropower Triple Supply Monitor for 5V, 3.3V and ADJ	Adjustable $\overline{\text{RESET}}$ and Watchdog Time-Outs
LTC1727-2.5/LTC1727-5	Micropower Triple Supply Monitors with Open-Drain Reset	Individual Monitor Outputs in MSOP
LTC1728-1.8/LTC1728-3.3	Micropower Triple Supply Monitors with Open-Drain Reset	5-Lead SOT-23 Package
LTC1728-2.5/LTC1728-5	Micropower Triple Supply Monitors with Open-Drain Reset	5-Lead SOT-23 Package
LTC1985-1.8	Micropower Triple Supply Monitor with Push-Pull Reset Output	5-Lead SOT-23 Package
LTC2901	Programmable Quad Supply Monitor	Adjustable Reset and Watchdog Timers, 16-Lead Narrow SSOP Package
LTC2902	Programmable Quad Supply Monitor	Adjustable Reset Timer, Supply Tolerance and Margining Functions, 16-Lead Narrow SSOP Package

Linear Technology Corporation
1630 McCarthy Blvd., Milpitas, CA 95035-7417
(408) 432-1900 • FAX: (408) 434-0507 • www.linear.com

LT/TP 0403 2K • PRINTED IN USA

Looking for pricing, stock, or lifecycle information?

Click below to explore more details on WIN SOURCE:

- View LTC2900-1IMS on WIN SOURCE
- Linear Technology Information

Optimize Your Supply Chain with WIN SOURCE Solutions

- Global Sourcing Solution
- Obsolete Management
- Cost Control Management
- Shortage Management
- Alternative Solution
- Excess Inventory Management