

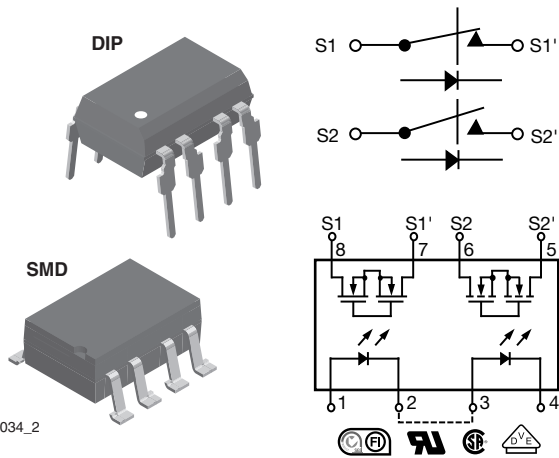


**THE DATASHEET OF
LH1512BB**





Dual 1 Form A/B, C Solid State Relay



i179034_2

DESCRIPTION

The LH1512 relays contain normally open and normally closed switches that can be used independently as a 1 form A and 1 form B relay, or when used together, as a 1 form C relay. The relays are constructed as a mult.-chip hybrid device. Actuation control is via an infrared LED. The output switch is a combination of a photodiode array with MOSFET switches and control circuitry.

FEATURES

- Current limit protection
- Isolation test voltage 3750 V_{RMS}
- Typical R_{ON} 10 Ω
- Load voltage 200 V
- Load current 200 mA
- High surge capability
- Clean bounce free switching
- Low power consumption
- SMD lead available on tape and reel
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC



APPLICATIONS

- General telecom switching
 - On/off hook control
 - Ring delay
 - Dial pulse
 - Ground start
 - Ground fault protection
- Instrumentation
- Industrial controls

AGENCY APPROVALS

UL1577: file no. E52744 system code H, double protection
 CSA: certification no. 093751
 DIN EN: 60747-5-2 (VDE 0884)/60747-5-5 (pending), available with option 1
 FIMKO: 25419

ORDERING INFORMATION	
<div style="display: flex; justify-content: space-around; align-items: center;"> <div style="border: 1px solid black; padding: 2px; margin: 2px;">L</div> <div style="border: 1px solid black; padding: 2px; margin: 2px;">H</div> <div style="border: 1px solid black; padding: 2px; margin: 2px;">1</div> <div style="border: 1px solid black; padding: 2px; margin: 2px;">5</div> <div style="border: 1px solid black; padding: 2px; margin: 2px;">1</div> <div style="border: 1px solid black; padding: 2px; margin: 2px;">2</div> <div style="border: 1px solid black; padding: 2px; margin: 2px;">B</div> <div style="border: 1px solid black; padding: 2px; margin: 2px;">#</div> <div style="border: 1px solid black; padding: 2px; margin: 2px;">#</div> <div style="border: 1px solid black; padding: 2px; margin: 2px;">T</div> <div style="border: 1px solid black; padding: 2px; margin: 2px;">R</div> </div> <p style="text-align: center;"> PART NUMBER ELECTR. VARIATION PACKAGE CONFIG. TAPE AND REEL </p>	<div style="display: flex; justify-content: space-around; align-items: center;"> <div style="text-align: center;"> <p>DIP 7.62 mm</p> </div> <div style="text-align: center;"> <p>SMD > 0.1 mm</p> </div> </div>
PACKAGE	UL, CSA, FIMKO
SMD-8, tubes	LH1512BAC
SMD-8, tape and reel	LH1512BACTR
DIP-8, tubes	LH1512BB



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
LED continuous forward current		I _F	50	mA
LED reverse voltage	I _R ≤ 10 μA	V _R	5	V
OUTPUT				
DC or peak AC load voltage	I _L ≤ 50 μA	V _L	200	V
Continuous DC load current (form C operation)		I _L	200	mA
Peak load current, form A	t = 100 ms	I _P	(2)	
Peak load current (single shot), form B		I _P	400	mA
SSR				
Ambient operating temperature range		T _{amb}	- 40 to + 85	°C
Storage temperature range		T _{stg}	- 40 to + 125	°C
Pin soldering temperature (3)	t = 10 s max.	T _{sld}	260	°C
Input to output isolation test voltage	t = 1 s, I _{ISO} = 10 μA max.	V _{ISO}	3750	V _{RMS}
Pole-to-pole isolation voltage (S1 to S2) (1) (dry air, dust free, at sea level)			1600	V
Output power dissipation (continuous)		P _{diss}	600	mW

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.
- Breakdown occurs between the output pins external to the package.
- Refer to current limit performance application note for a discussion on relay operation during transient currents.
- Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
LED forward current switch turn-on (NO)	I _L = 100 mA, t = 10 ms	I _{Fon}		0.6	2	mA
LED forward current switch turn-off (NO)	V _L = ± 150 V	I _{Foff}	0.2	0.5		mA
LED forward current switch turn-on (NC)	I _L = 100 mA, t = 10 ms	I _{Fon}	0.2	0.9		mA
LED forward current switch turn-off (NC)	V _L = ± 150 V	I _{Foff}		1	2	mA
LED forward voltage	I _F = 10 mA	V _F	1.15	1.26	1.45	V
OUTPUT						
On-resistance: (NO, NC)	I _F = 5 mA (NO), I _F = 0 (NC), I _L = 50 mA (NC)	R _{ON}		10	15	Ω
Off-resistance: (NO)	I _F = 0 mA, V _L = ± 100 V	R _{OFF}	0.35	5000		GΩ
Off-resistance: (NC)	I _F = 5 mA, V _L = ± 100 V	R _{OFF}	0.1	1.4		GΩ
Current limit: (NO)	I _F = 5 mA, t = 5 ms, V _L = ± 5 V	I _{LMT}	270	360	460	mA
Off-state leakage current: (NO)	I _F = 0 mA, V _L = ± 100 V	I _O		0.02	1000	nA
Off-state leakage current: (NC)	I _F = 5 mA, V _L = ± 100 V	I _O		0.07	1	μA
Off-state leakage current: (NO, NC)	I _F = 0 mA (NO), I _F = 5 mA, V _L = ± 200 V	I _O			1	μA
Output capacitance: (NO)	I _F = 0 mA, V _L = 50 V	C _O		60		pF
Output capacitance: (NC)	I _F = 5 mA, V _L = 50 V	C _O		60		pF
TRANSFER						
Capacitance (input to output)	V _{ISO} = 1 V	C _{IO}		3		pF

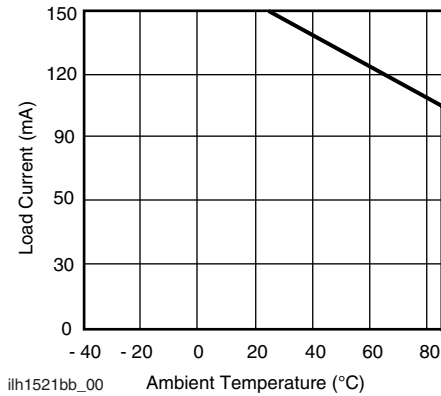
Note

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time (NO)	I _F = 10 mA, I _L = 50 mA	t _{on}		1.4	3	ms
Turn-on time (NC)	I _F = 10 mA, I _L = 50 mA	t _{on}		1.2	3	ms
Turn-off time (NO)	I _F = 10 mA, I _L = 50 mA	t _{off}		0.7	3	ms
Turn-off time (NC)	I _F = 10 mA, I _L = 50 mA	t _{off}		2	3	ms

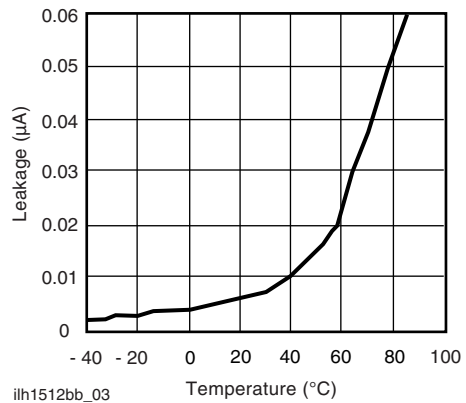


TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)



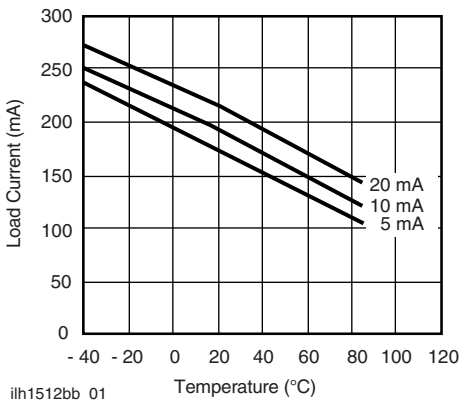
ilh1512bb_00

Fig. 1 - Recommended Operating Conditions



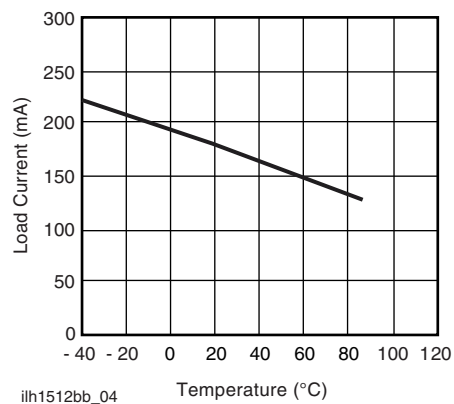
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Fig. 4 - Typical Leakage vs. Temperature (Measured across Pin 5 and 6 or 7 and 8)



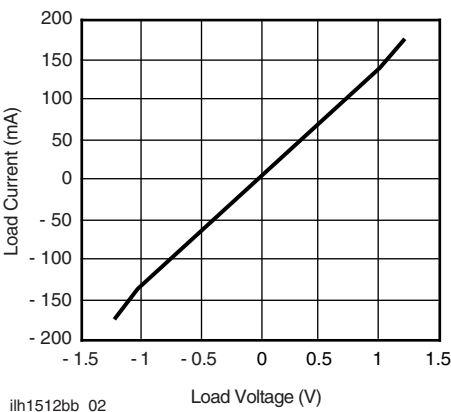
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Fig. 2 - Form A Typical Load Current vs. Temperature



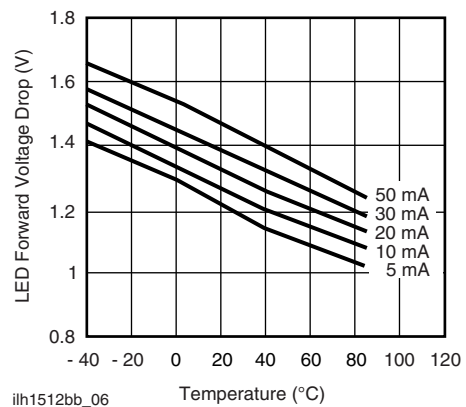
ilh1512bb_04

Fig. 5 - Form B Typical Load Current vs. Temperature



ilh1512bb_02

Fig. 3 - Form A Typical Load Current vs. Load Voltage



ilh1512bb_06

Fig. 6 - Typical LED Forward Voltage Drop vs. Temperature

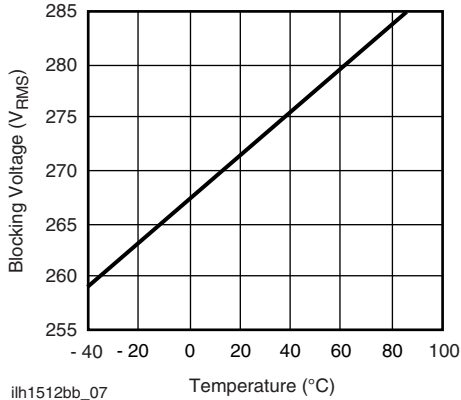


Fig. 7 - Form A Typical Blocking Voltage vs. Temperature

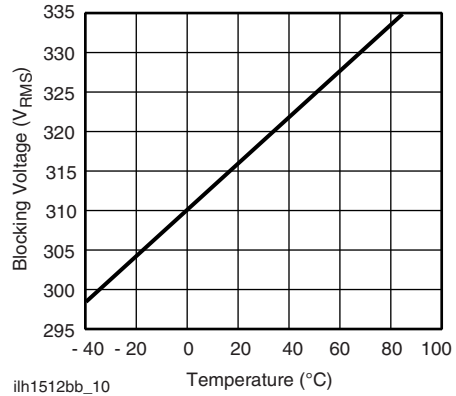


Fig. 10 - Form B Typical Blocking Voltage vs. Temperature

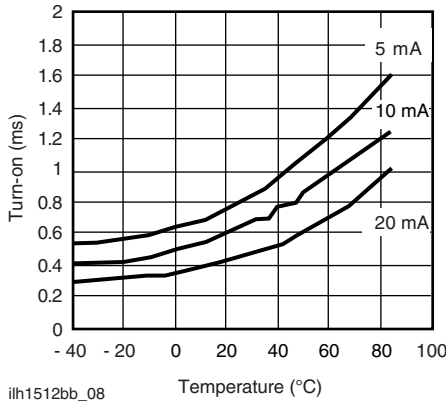


Fig. 8 - Form A Typical Turn-On vs. Temperature

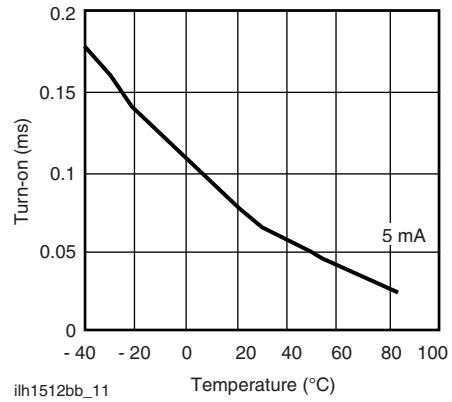


Fig. 11 - Form B Typical Turn-On vs. Temperature

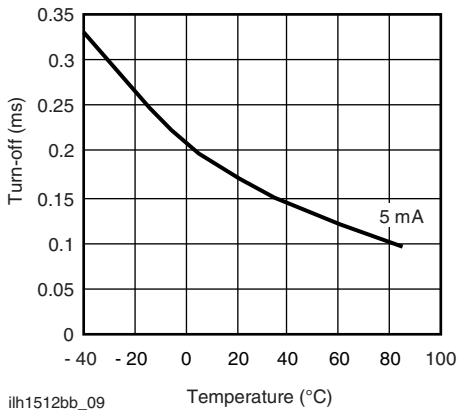


Fig. 9 - Form A Typical Turn-Off vs. Temperature

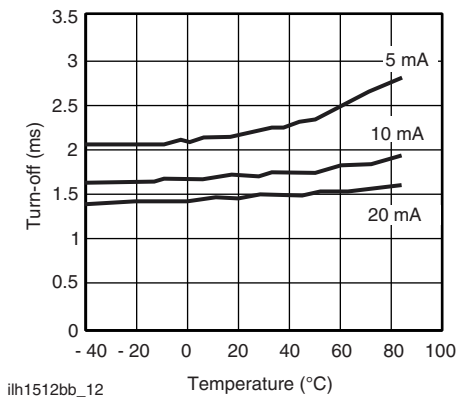
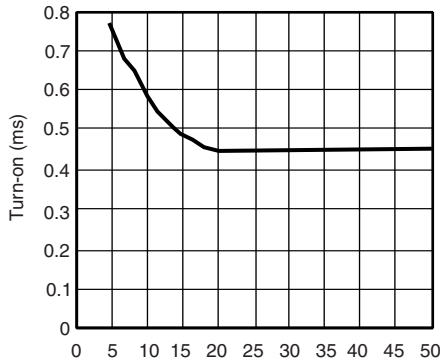
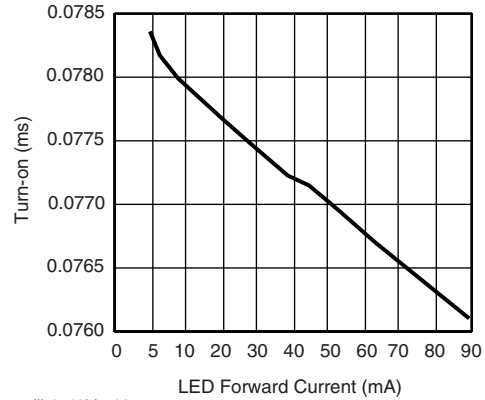


Fig. 12 - Form B Typical Turn-Off vs. Temperature



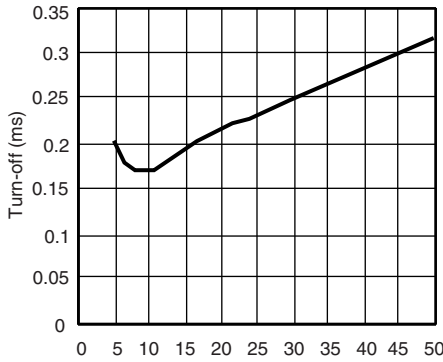
ilh1512bb_13 LED Forward Current (mA)

Fig. 13 - Form A Typical Turn-On vs. LED Forward Current



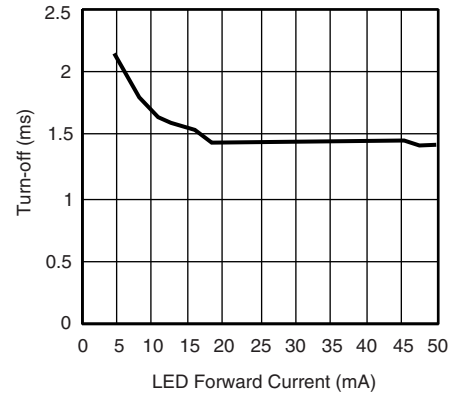
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Fig. 16 - Form B Typical Turn-On vs. LED Forward Current



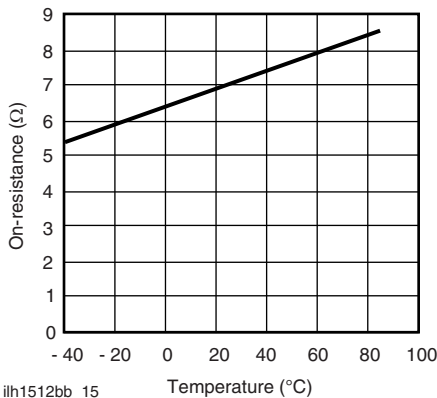
ilh1512bb_14 LED Forward Current (mA)

Fig. 14 - Form A Typical Turn-Off vs. LED Forward Current



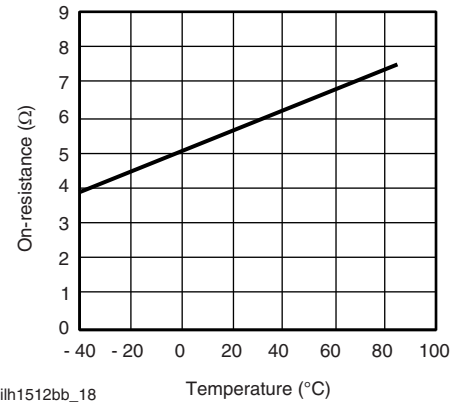
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Fig. 17 - Form B Typical Turn-Off vs. LED Forward Current



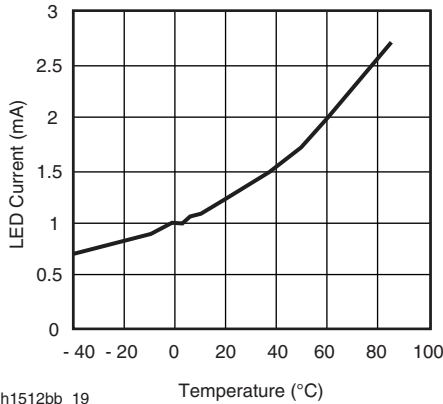
ilh1512bb_15

Fig. 15 - Form A Typical On-Resistance vs. Temperature



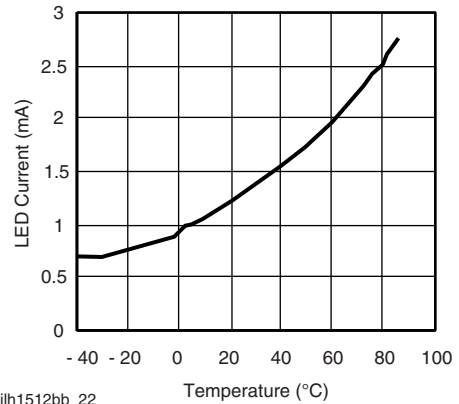
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Fig. 18 - Form B Typical On-Resistance vs. Temperature



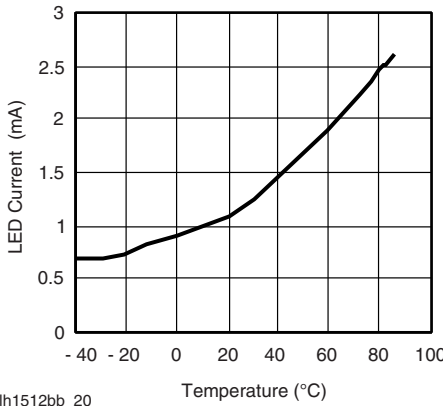
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Fig. 19 - Form A Typical I_F for Switch Operation vs. Temperature



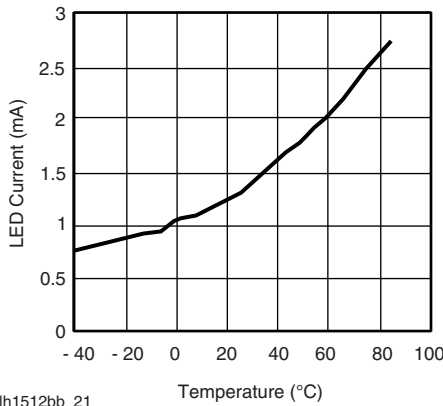
ih1512bb_22

Fig. 22 - Form B Typical I_F for Switch Dropout vs. Temperature



ih1512bb_20

Fig. 20 - Form A Typical I_F for Switch Dropout vs. Temperature



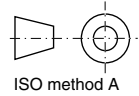
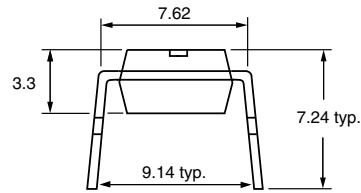
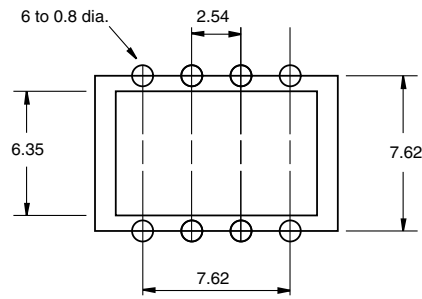
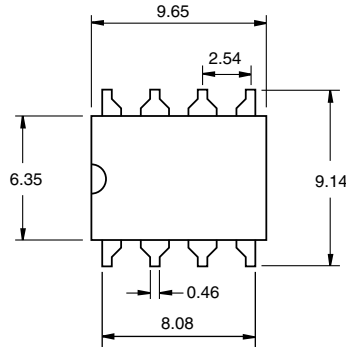
ih1512bb_21

Fig. 21 - Form B Typical I_F for Switch Operation vs. Temperature



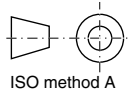
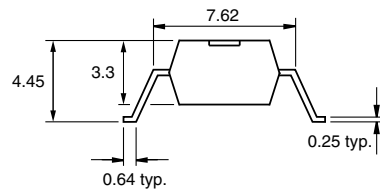
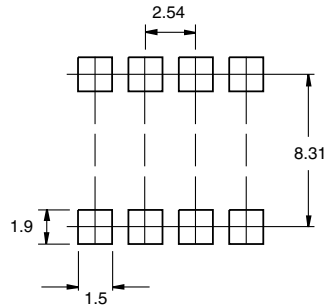
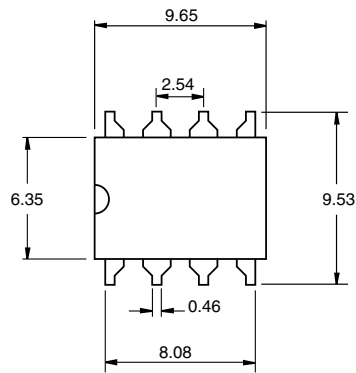
PACKAGE DIMENSIONS in millimeters

DIP



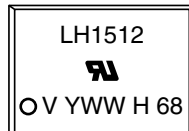
i178017

SMD



i178018

PACKAGE MARKING (example)



Note

- Tape and reel suffix (TR) is not part of the package marking.



Footprint and Schematic Information for LH1512BAC, LH1512BACTR, LH1512BB

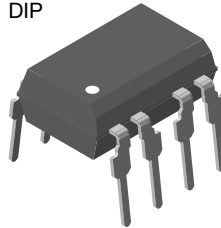
The footprint and schematic symbols for the following parts can be accessed using the associated links. They are available in Eagle, Altium, KiCad, OrCAD / Allegro, Pulsonix, and PADS.

Note that the 3D models for these parts can be found on the Vishay product page.

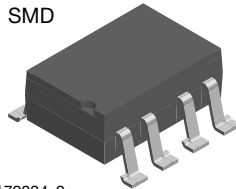
PART NUMBER	FOOTPRINT / SCHEMATIC
LH1512BAC	www.snapeda.com/parts/LH1512BAC/Vishay/view-part
LH1512BACTR	www.snapeda.com/parts/LH1512BACTR/Vishay/view-part
LH1512BB	www.snapeda.com/parts/LH1512BB/Vishay/view-part

For technical issues and product support, please contact optocoupleranswers@vishay.com.

DIP



SMD



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