THE DATASHEET OF

TISP61089MDR-S

BOURNS®

Featured Products Bulletin

TISP® THYRISTOR SURGE PROTECTORS

September, 2013

Bourns Announces a New High Surge Current IC for SLIC Overvoltage Protection

- Model TISP61089M -

Bourns is pleased to announce the release of a new programmable overvoltage protection device designed to protect SLICs (Subscriber Line Interface Circuits) against overvoltage conditions on the telephone line.

The Bourns® Model TISP61089M, a high-surge current protector, is rated at 70 A for a 5/310 surge, and is specified to support equipment compliance with Bellcore GR-1089-CORE, ITU-T K.21 and K.45 and YD/T-950.

The Model TISP61089M includes two negative protection structures and two anti-parallel diodes which provide optimum protection during Metallic (Differential) and Longitudinal (Common Mode) surge conditions. The device supports a negative supply voltage down to -155 V and requires a low gate triggering current of 5 mA maximum.

Wave Shape	Standard	Non-Repetitive Peak Impulse Current (A)
2/10	GR-1089-CORE	120
10/700-5/310	ITU-T K.20/21/45 YD/T-950	70
10/1000	GR-1089-CORE	30

The Bourns® Model TISP61089M is packaged in an RoHS compliant* 8-SOIC package. The product data sheet can be viewed on the Bourns website at www.bourns.com.

Should you have any questions or need additional information, please feel free to contact Customer Service/Inside Sales.

Features

- High 70 A 5/310 capability
- Dual voltage-programmable protector
- Supports voltages down to -155 V
- Low 5 mA max. gate triggering current
- High 150 mA min. holding current
- RoHS compliant*

Applications

- SLICs

*RoHS Directive 2002/95/EC Jan. 27, 2003 including annex and RoHS Recast 2011/65/EU June 8, 2011.

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