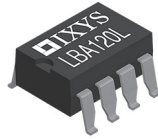




THE DATASHEET OF LBA120LSTR





Parameter	Ratings	Units
Blocking Voltage	250	V_P
Load Current	150	mA_{rms} / mA_{DC}
On-Resistance (max)	25	Ω

Features

- Current Limited
- 3750V_{rms} Input/Output Isolation
- Low Drive Power Requirements
- High Reliability
- Arc-Free With No Snubbing Circuits
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- Small 8-Pin Package
- Surface Mount, Tape & Reel Version Available
- Flammability Rating UL 94 V-0

Applications

- Telecommunications
 - Telecom Switching
 - Tip/Ring Circuits
 - Modem Switching (Laptop, Notebook, Pocket Size)
 - Hook Switch
 - Dial Pulsing
 - Ground Start
 - Ringing Injection
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

Description

The LBA120L comprises two independent, single-pole, 250V, 170mA, 20 Ω solid state relays: one single-pole, normally open (1-Form-A) current limiting relay and one single-pole, normally closed (1-Form-B) relay.

The LBA120L is designed to provide an ideal solution where a complementary Form-A/Form-B relay pair is required.

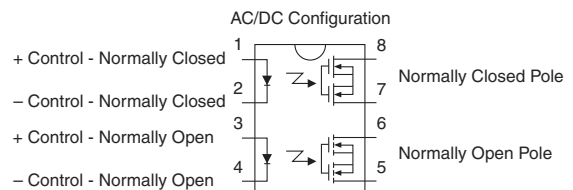
Approvals

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1172007
- EN/IEC 60950-1 Certified Component:
Certificate available on our website

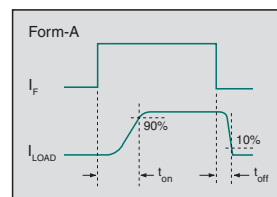
Ordering Information

Part #	Description
LBA120L	8-Pin DIP (50/Tube)
LBA120LS	8-Pin Surface Mount (50/Tube)
LBA120LSTR	8-Pin Surface Mount (1,000/Reel)

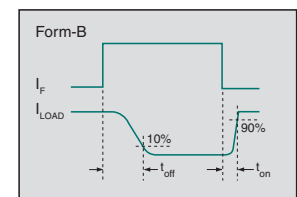
Pin Configuration



Switching Characteristics of Normally Open Devices



Switching Characteristics of Normally Closed Devices



Absolute Maximum Ratings $T_A=25^\circ\text{C}$

Parameter	Symbol	Ratings	Units
Blocking Voltage	V_B	250	V_P
Reverse Input Voltage	V_R	5	V
Input Control Current	I_F	50	mA
Peak (10ms)		1	A
Input Power Dissipation ¹	P_I	150	mW
Total Power Dissipation ²	P_D	800	mW
Isolation Voltage, Input to Output	V_{ISO}	3750	V_{rms}
Operational Temperature	T_A	-40 to +85	$^\circ\text{C}$
Storage Temperature	T_{STG}	-40 to +125	$^\circ\text{C}$

¹ Derate linearly 1.33 mW / $^\circ\text{C}$
² Derate linearly 6.67 mW / $^\circ\text{C}$

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25 $^\circ\text{C}$, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

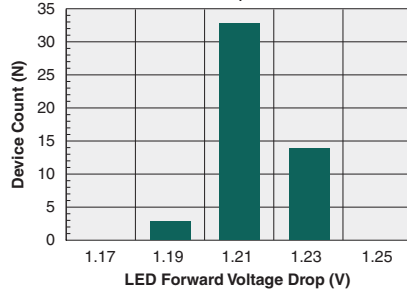
Electrical Characteristics $T_A=25^\circ\text{C}$

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Load Current						
Continuous ¹	-	I_L	-	-	150	$\text{mA}_{rms} / \text{mA}_{DC}$
Load Current Limiting (1-Form-A Only)	-	I_{CL}	± 190	± 235	± 280	mA
On-Resistance	$I_L=170\text{mA}$	R_{ON}	-	21	25	Ω
Off-State Leakage Current	$V_L=250V_P$	I_{LEAK}	-	-	1	μA
Switching Speeds						
Turn-On	$I_F=5\text{mA}, V_L=10\text{V}$	t_{on}	-	-	5	ms
Turn-Off		t_{off}	-	-	5	
Output Capacitance	$V_L=50\text{V}, f=1\text{MHz}$	C_{OUT}	-	50	-	pF
Input Characteristics						
Input Control Current to Activate	$I_L=120\text{mA}$	I_F	-	-	5	mA
Input Control Current to Deactivate	-	I_F	0.4	0.7	-	mA
Input Voltage Drop	$I_F=5\text{mA}$	V_F	0.9	1.2	1.5	V
Reverse Input Current	$V_R=5\text{V}$	I_R	-	-	10	μA
Common Characteristics						
Input to Output Capacitance	$V_{IO}=0\text{V}, f=1\text{MHz}$	C_{IO}	-	3	-	pF

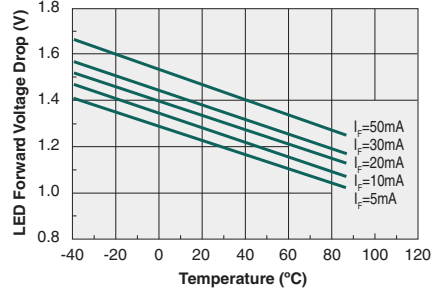
¹ If both poles operate, then the load current must be derated so as not to exceed the package's power dissipation rating.

Form-A/Form-B PERFORMANCE DATA*

Typical LED Forward Voltage Drop
(N=50, $I_F=5mA$)

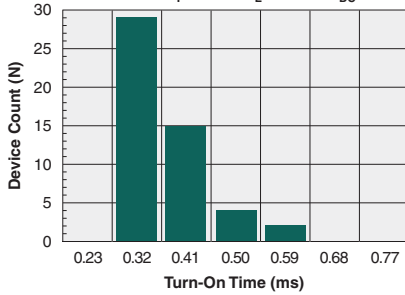


Typical LED Forward Voltage Drop vs. Temperature

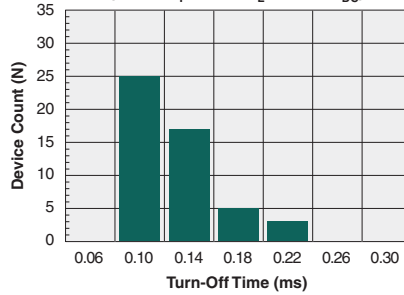


Form-A RELAY PERFORMANCE DATA*

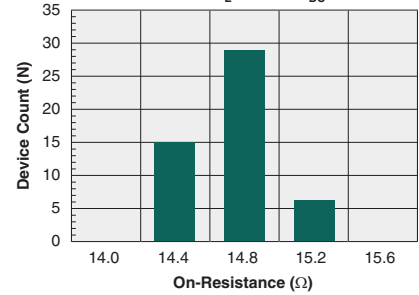
Form-A
Typical Turn-On Time
(N=50, $I_F=5mA$, $I_L=170mA_{DC}$)



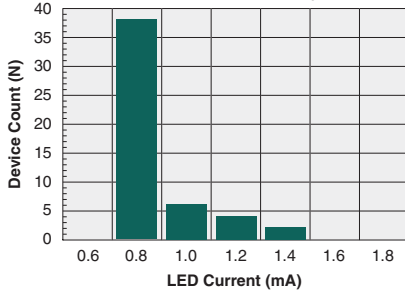
Form-A
Typical Turn-Off Time
(N=50, $I_F=5mA$, $I_L=170mA_{DC}$)



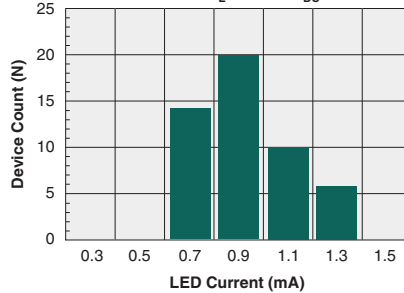
Form-A
Typical On-Resistance Distribution
(N=50, $I_L=170mA_{DC}$)



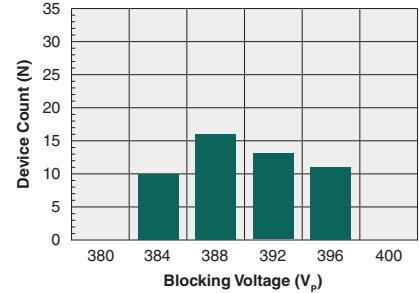
Form-A
Typical I_F for Switch Operation
(N=50, $I_L=170mA_{DC}$)



Form-A
Typical I_F for Switch Dropout
(N=50, $I_L=170mA_{DC}$)

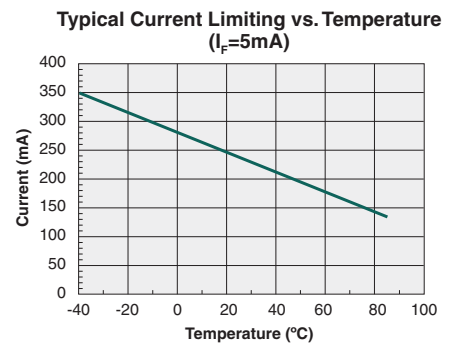
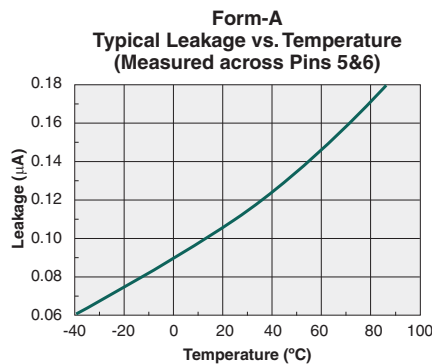
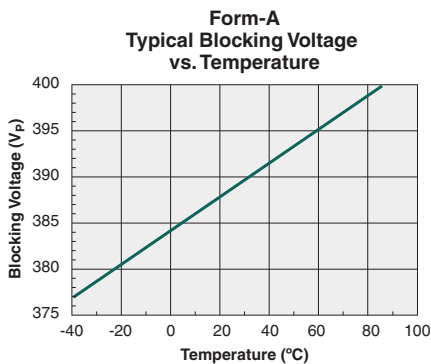
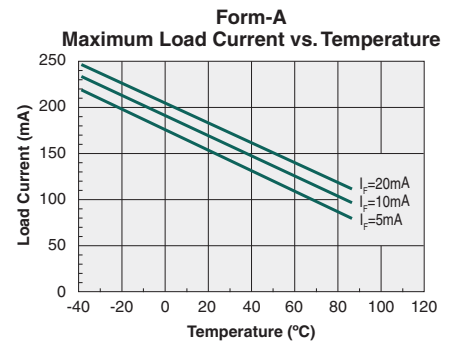
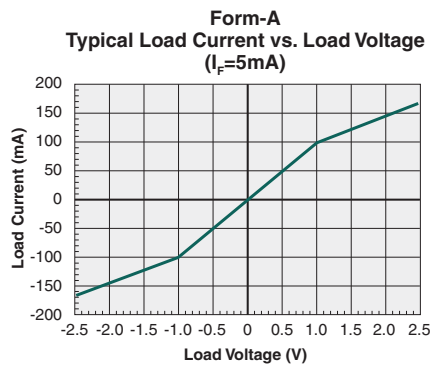
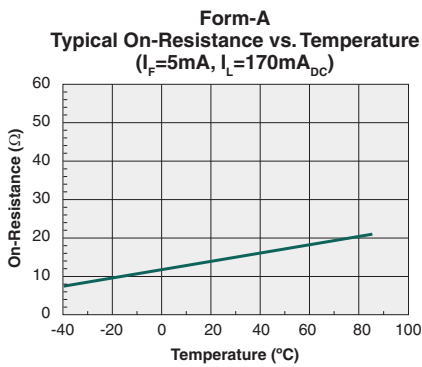
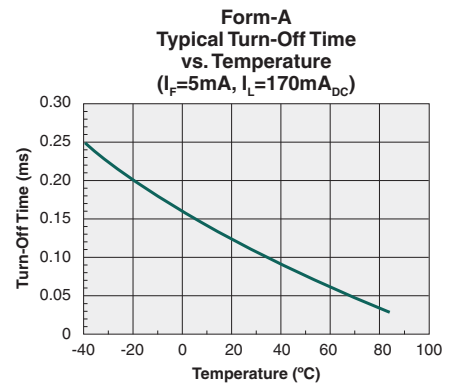
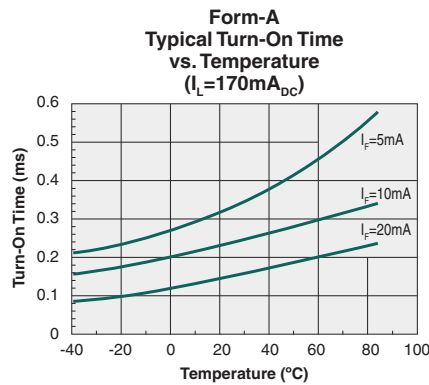
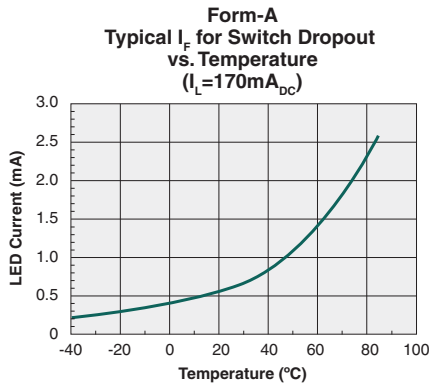
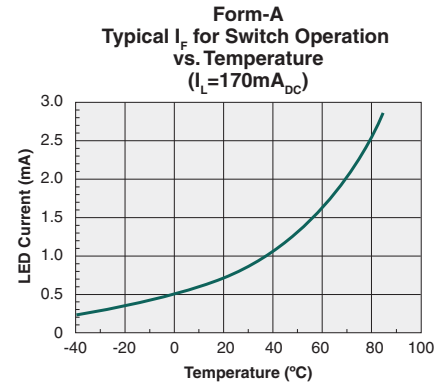
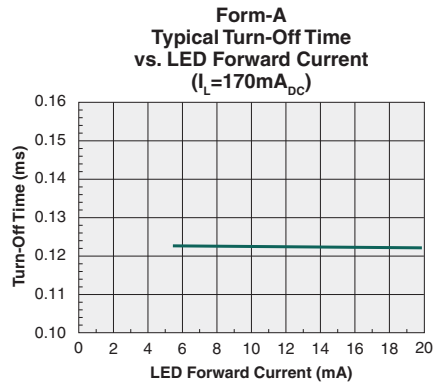
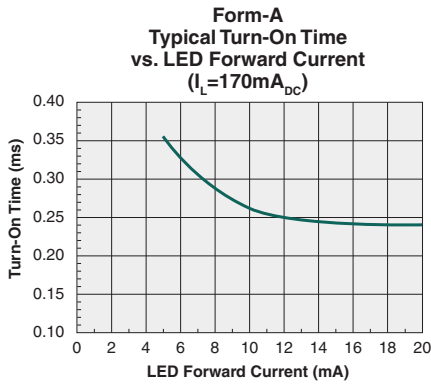


Form-A
Typical Blocking Voltage Distribution
(N=50)



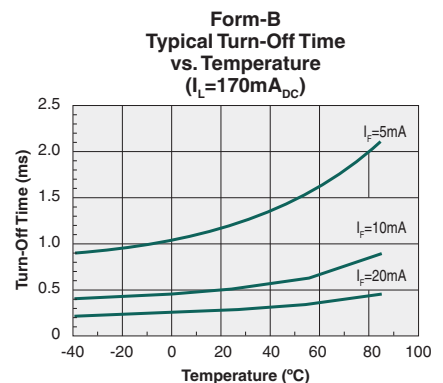
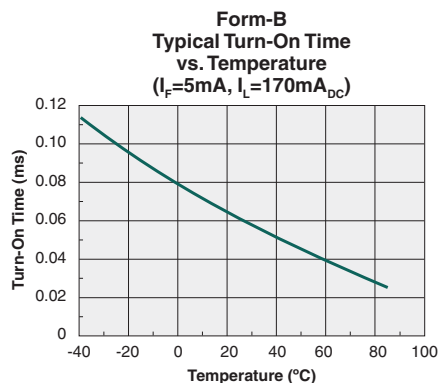
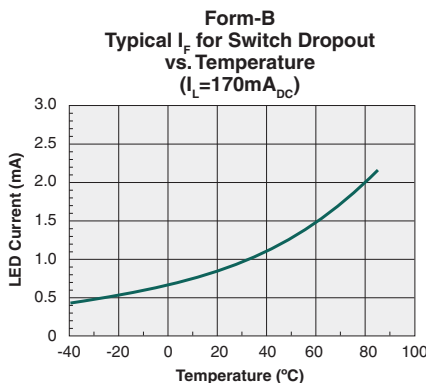
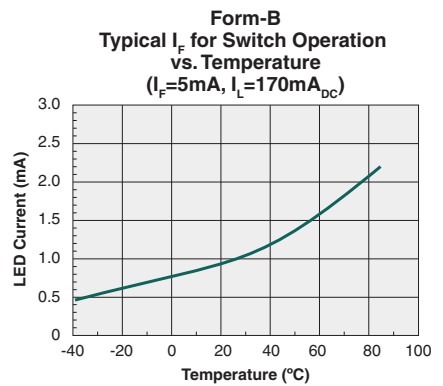
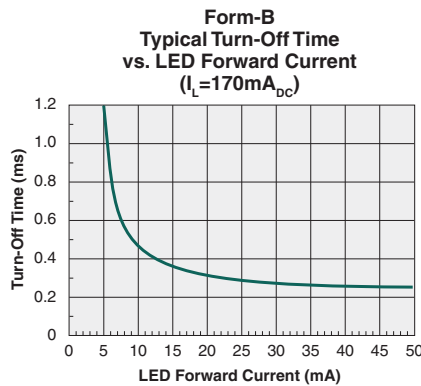
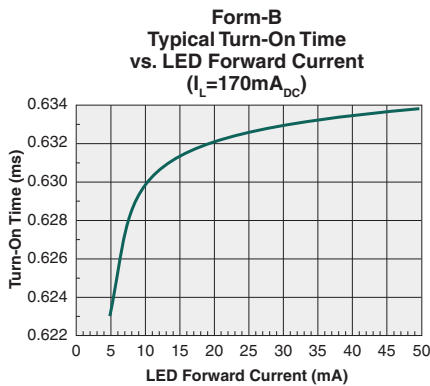
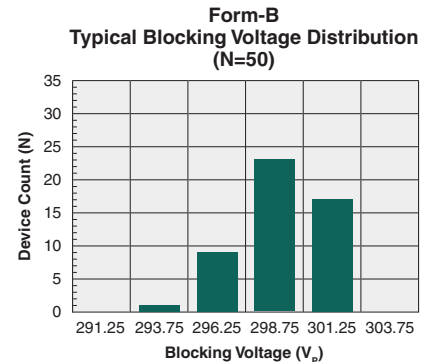
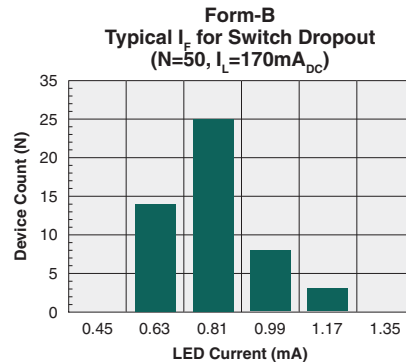
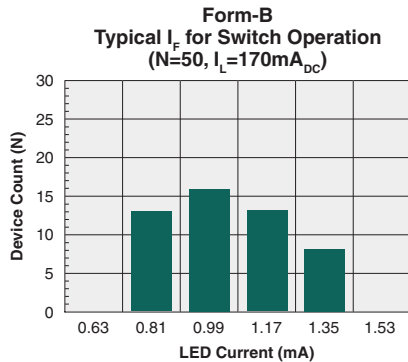
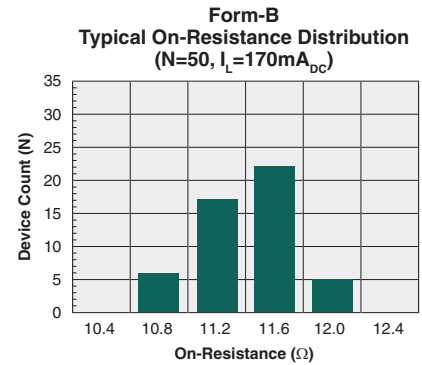
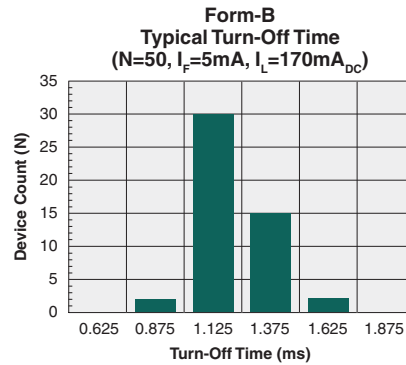
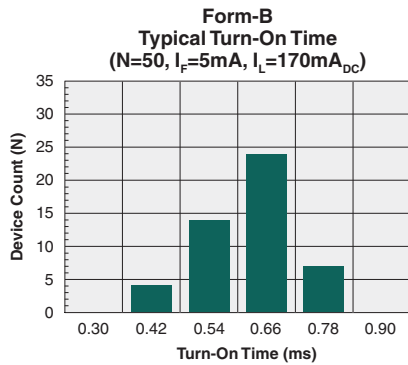
*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.
For guaranteed parameters not indicated in the written specifications, please contact our application department.

Form-A PERFORMANCE DATA*



*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C. For guaranteed parameters not indicated in the written specifications, please contact our application department.

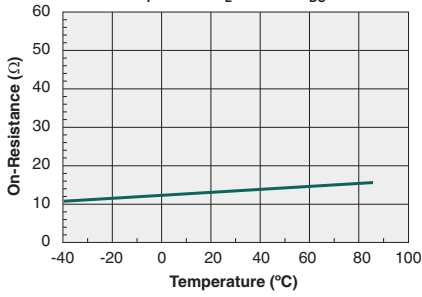
Form-B PERFORMANCE DATA*



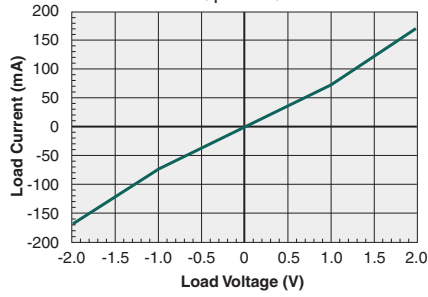
*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.
For guaranteed parameters not indicated in the written specifications, please contact our application department.

Form-B PERFORMANCE DATA*

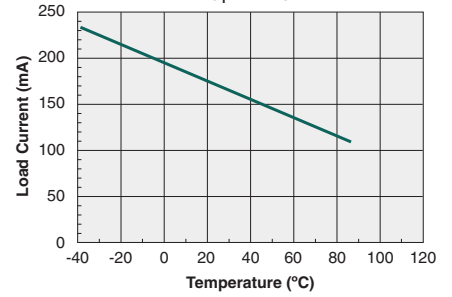
Form-B
Typical On-Resistance vs. Temperature
($I_F=5\text{mA}$, $I_L=170\text{mA}_{DC}$)



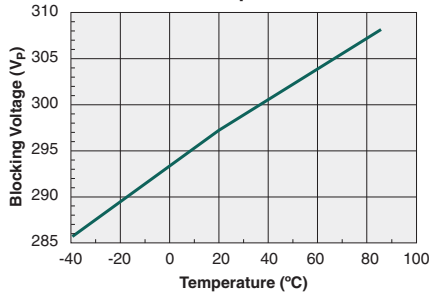
Form-B
Typical Load Current vs. Load Voltage
($I_F=5\text{mA}$)



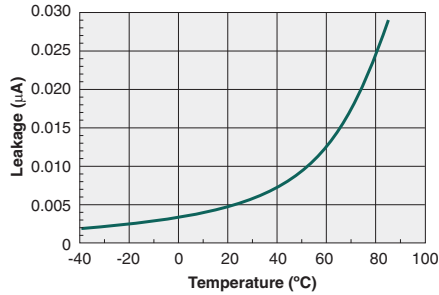
Form-B
Maximum Load Current vs. Temperature
($I_F=5\text{mA}$)



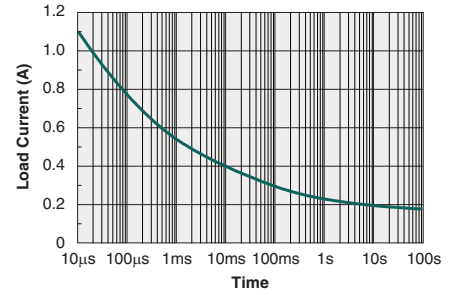
Form-B
Typical Blocking Voltage vs. Temperature



Form-B
Typical Leakage vs. Temperature
Measured across Pins 7&8



Energy Rating Curve



*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.
For guaranteed parameters not indicated in the written specifications, please contact our application department.

Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification
LBA120L / LBA120LS	MSL 1

ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

Soldering Profile

Provided in the table below is the Classification Temperature (T_C) of this product and the maximum dwell time the body temperature of this device may be ($T_C - 5$)°C or greater. The classification temperature sets the Maximum Body Temperature allowed for this device during lead-free reflow processes. For through-hole devices, and any other processes, the guidelines of **J-STD-020** must be observed.

Device	Classification Temperature (T_C)	Dwell Time (t_p)	Max Reflow Cycles
LBA120L	250°C	30 seconds	1
LBA120LS	250°C	30 seconds	3

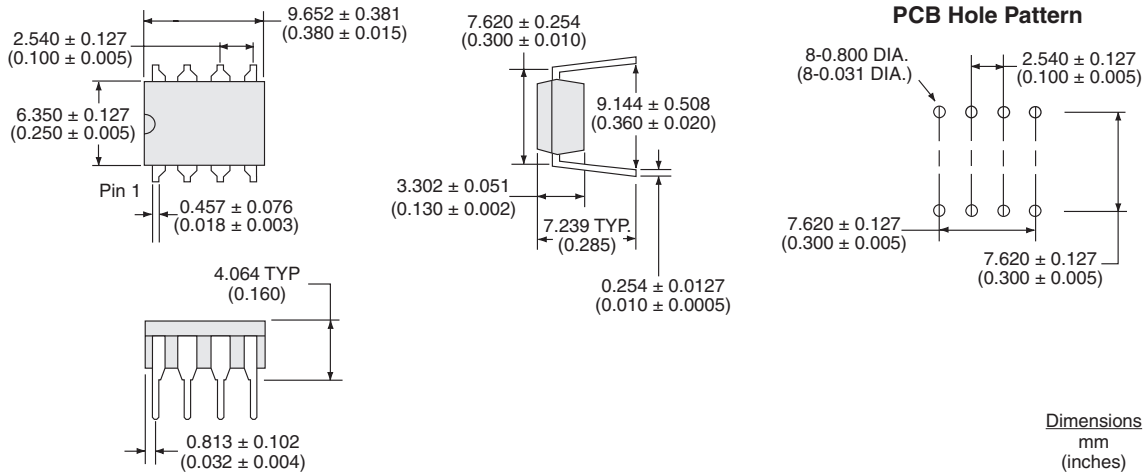
Board Wash

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include, but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to flux or solvents that are Chlorine- or Fluorine-based.

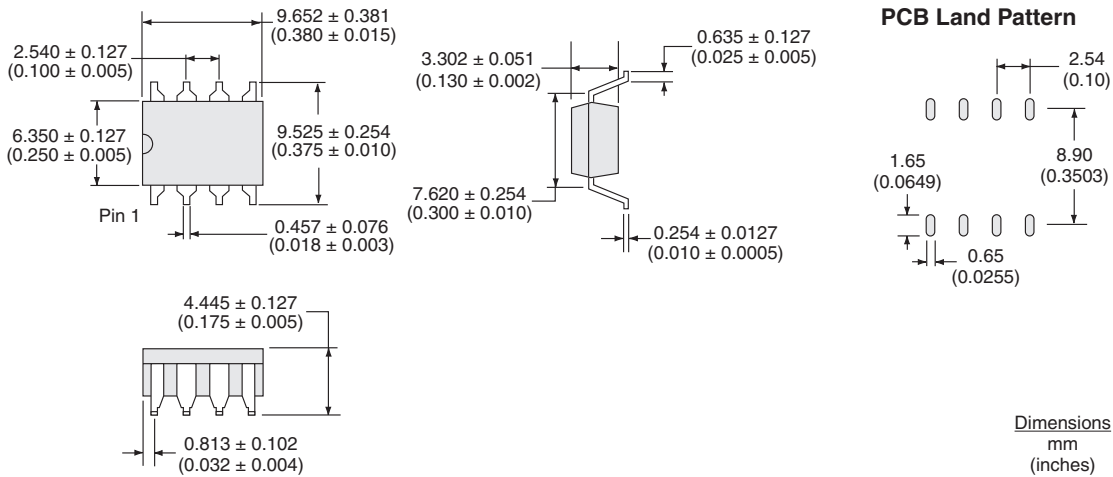


MECHANICAL DIMENSIONS

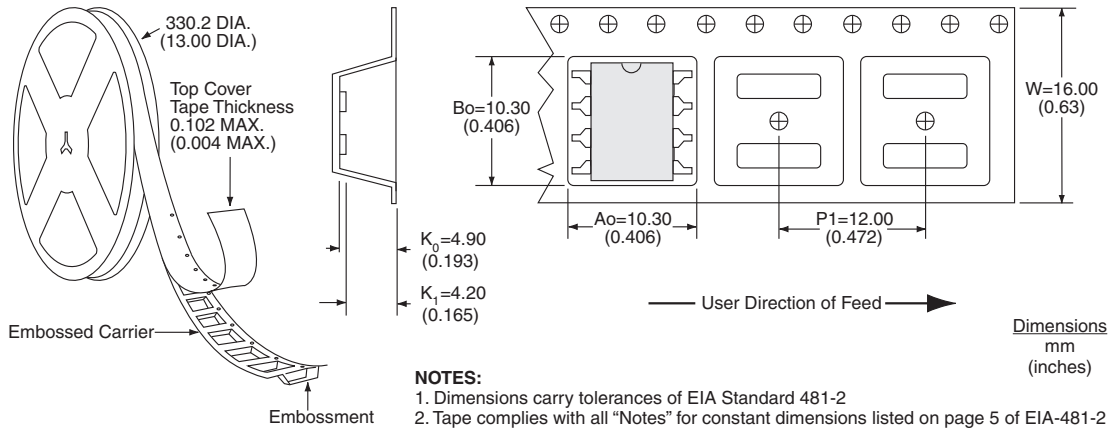
LBA120L



LBA120LS



LBA120LSTR Tape & Reel



For additional information please visit our website at: www.ixysic.com

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 [IXYS Information](#)

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