

Mobile Phone Module Driver

Parallel Interface Type Lens Driver for Stepping Motor

BD6360GUL

General Description

The BD6360GUL is a motor driver with dual, built-in Full-ON type two H-Bridges. It also has a power supply function for photo-interrupter to detect motor position and wave-shaping circuit built-in. This small surface mounting package CSP is most suitable for mobile system and home appliance.

Features

- Low ON-Resistance Power CMOS Output
- ESD Protection: 8kV, Human Body Model (HBM)
- Drive Mode Switch Function
- Control Input Pins Fit the Signal of 1.8V System
- Voltage-Regulator for Photo-Interrupter and Comparator with Hysteresis to Convert to Sharp Waveform
- Under Voltage Locked Out protection & Thermal Shut Down Circuit

Applications

- Mobile system
- Home appliance
- Amusement system, etc

Key Specifications

- Power Supply Voltage Range: 2.3V to 5.5V
- Circuit Current (No load): 1.1mA(Typ)
- Stand-By Current: 5µA (Max)
- Control Input Voltage Range: 0V to V_{CC}V
- H-Bridge Output Current: -0.4A to +0.4A
- Output ON-Resistance (Total): 1.0Ω(Typ)
- Operating Temperature Range: -25°C to +85°C

Package

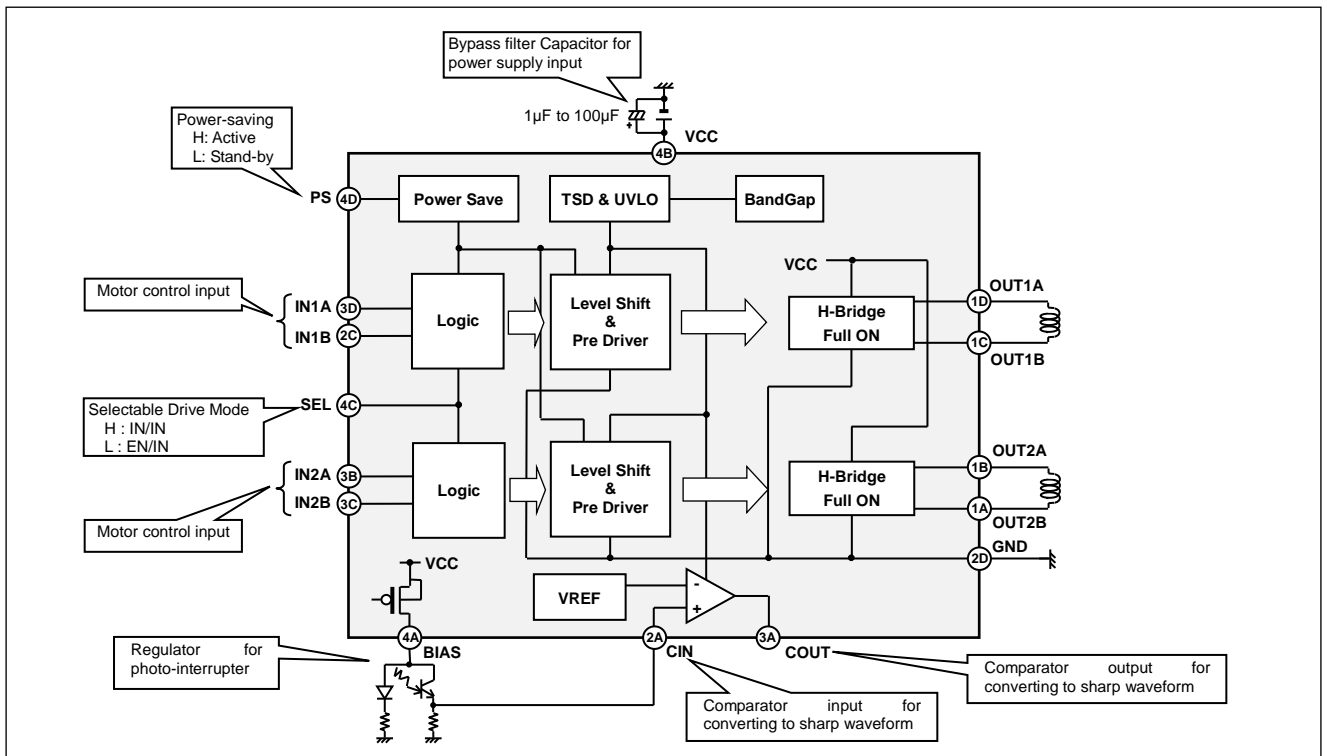
VCSP50L2

W(Typ) x D(Typ) x H(Max)

2.10mm x 2.10mm x 0.55mm



Typical Application Circuit



Pin Configuration

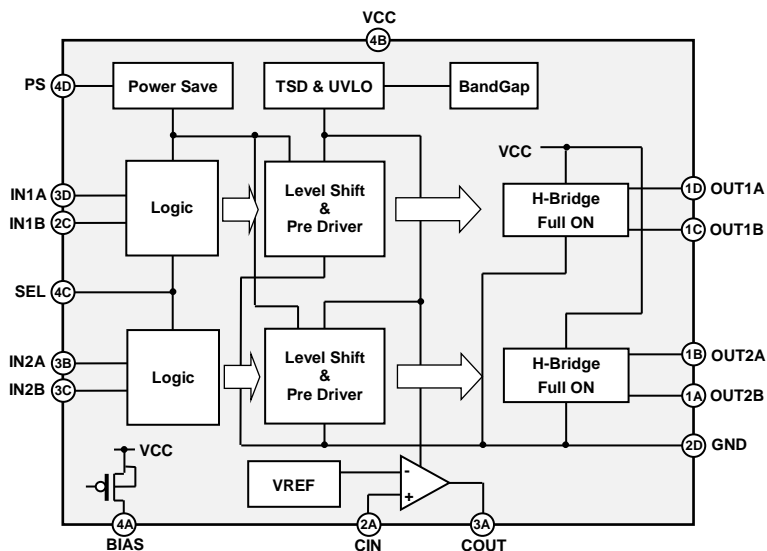
(TOP VIEW)

	1	2	3	4
A	OUT2B	CIN	COUT	BIAS
B	OUT2A	INDEX POST	IN2A	VCC
C	OUT1B	IN1B	IN2B	SEL
D	OUT1A	GND	IN1A	PS

Pin Descriptions

Pin No.	Pin Name	Function
1A	OUT2B	H-bridge output 2B
2A	CIN	Comparator input for sharp waveform
3A	COUT	Comparator output for sharp waveform
4A	BIAS	Voltage-regulator for photo-interrupter
1B	OUT2A	H-bridge output 2A
2B	INDEX POST	
3B	IN2A	Control logic input 2A
4B	VCC	Power supply
1C	OUT1B	H-bridge output 1B
2C	IN1B	Control logic input 1B
3C	IN2B	Control logic input 2B
4C	SEL	Drive mode switch function
1D	OUT1A	H-bridge output 1A
2D	GND	Ground
3D	IN1A	Control logic input 1A
4D	PS	Power-saving function

Block Diagram



Description of Blocks

1. Power-Saving Function
A power-saving function is included, which allows the system to save power when not driving the motor. The voltage level on this pin should be set high so as to keep the operation mode. (See the Electrical Characteristics; p.4/14)
2. Motor Control Input
 - (a) IN1A, IN1B, IN2A and IN2B pin
Logic level controls the output logic of H-Bridge.
(See the Electrical Characteristics; p.4/14, and I/O Truth Table; p.7/14)
 - (b) SEL pin
Logic level sets the IN/IN or EN/IN drive mode.
(See the Electrical Characteristics; p.4/14 and I/O Truth Table; p.7/14)
3. Photo-Interrupter Regulator
The BIAS pin can output VCC voltage for photo-interrupter during the operation mode.
When connecting photo-interrupter to this pin, pay attention to the ON resistance of internal power transistor.
(See the Typical Performance Curves; p.6/14)

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limit	Unit
Power Supply Voltage	V _{CC}	-0.3 to +6.5	V
Control Input Voltage	V _{IN}	-0.3 to +V _{CC} +0.3	V
Power Dissipation	P _d	0.73 (Note 1)	W
H-bridge Output Current	I _{OUT}	-0.5 to +0.5 (Note 2)	A/ch
Storage Temperature Range	T _{stg}	-55 to +150	°C
Junction Temperature	T _{jmax}	150	°C

(Note 1) Reduced by 5.84mW/°C over 25°C, when mounted on a glass epoxy board (50mm x 58mm x 1.75mm; 8layers)

(Note 2) Must not exceed P_d, A_{SO}, or T_{jmax} of 150°C

Caution: Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.

Recommended Operating Conditions

Parameter	Symbol	Min	Typ	Max	Unit
Power Supply Voltage	V _{CC}	2.3	-	5.5	V
Control Input Voltage	V _{IN}	0	-	V _{CC}	V
H-bridge Output Current ^(Note 3)	I _{OUT}	-0.4	-	+0.4	A/ch
Operating Temperature Range	T _{opr}	-25	-	+85	°C

(Note 3) Must not exceed P_d, A_{SO}, or T_{jmax} of 150°C

Electrical Characteristics (Unless otherwise specified $V_{CC}=3.0V$, $T_a=25^{\circ}C$)

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
All Circuits						
Stand-by Current	I_{CCST}	-	0	5	μA	$V_{PS}=0V$
Circuit Current	I_{CC}	-	1.1	1.8	mA	$V_{PS}=3V$ with no signal & no load
Control Input (IN=PS, IN1A, IN1B, IN2A, IN2B, SEL)						
High Level Input Voltage	V_{INH}	1.5	-	V_{CC}	V	
Low Level Input Voltage	V_{INL}	0	-	0.5	V	
High Level Input Current	I_{INH}	15	30	60	μA	$V_{IN}=3V$
Low Level Input Current	I_{INL}	-1	0	-	μA	$V_{IN}=0V$
Under Voltage Locked Out (UVLO)						
UVLO Voltage	V_{UVLO}	1.6	-	2.2	V	
Comparator for Photo-Interrupter						
Input Bias Current	I_{BIPI}	-3	0	+3	μA	
Output Low Level Voltage	V_{LOPI}	0	-	0.5	V	$I_{OUT}=+1mA$
Output High Level Voltage	V_{HIPI}	$V_{CC}-0.5$	-	V_{CC}	V	$I_{OUT}=-1mA$
Threshold Voltage	V_{THPI}	1.2	1.3	1.4	V	Low to High threshold voltage
Hysteresis Voltage	V_{HYSPI}	200	300	400	mV	High to Low threshold voltage - V_{THPI}
Regulator for Photo-Interrupter						
ON-Resistance	R_{ONSW}	-	-	10	Ω	$I_{OUT}=-30mA$
OFF Current	I_{LSW}	-1.0	0	-	μA	$V_{BIAS}=0V$
Full-ON Drive Block						
Output ON-Resistance	R_{ON}	-	1.00	1.25	Ω	$I_{OUT}=\pm 400mA$, High & Low-side total
Turn-ON Time	t_{ON}	-	0.6	2.0	μs	$I_{OUT}=\pm 400mA$
Turn-OFF Time	t_{OFF}	-	0.08	0.5	μs	$I_{OUT}=\pm 400mA$
Rise Time	t_R	0.1	0.15	1.0	μs	$I_{OUT}=\pm 400mA$
Fall Time	t_F	-	0.03	0.2	μs	$I_{OUT}=\pm 400mA$

Typical Performance Curves (Reference Data)

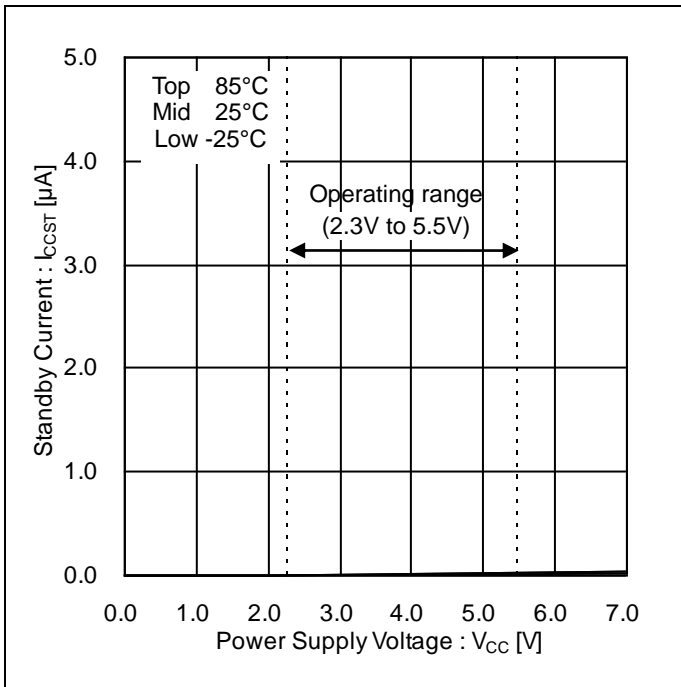


Figure 1.
Standby Current vs Supply Voltage

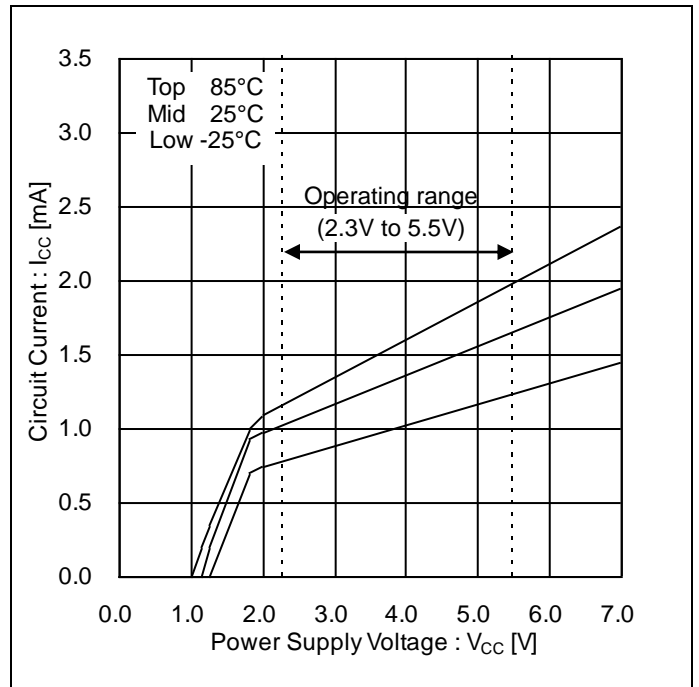


Figure 2.
Circuit Current vs Supply Voltage

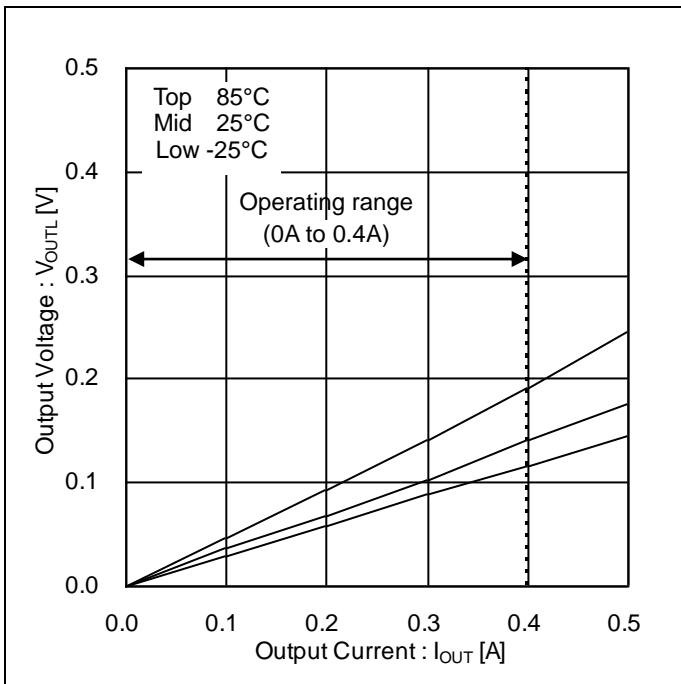


Figure 3.
Low Side Output Voltage vs Output Current

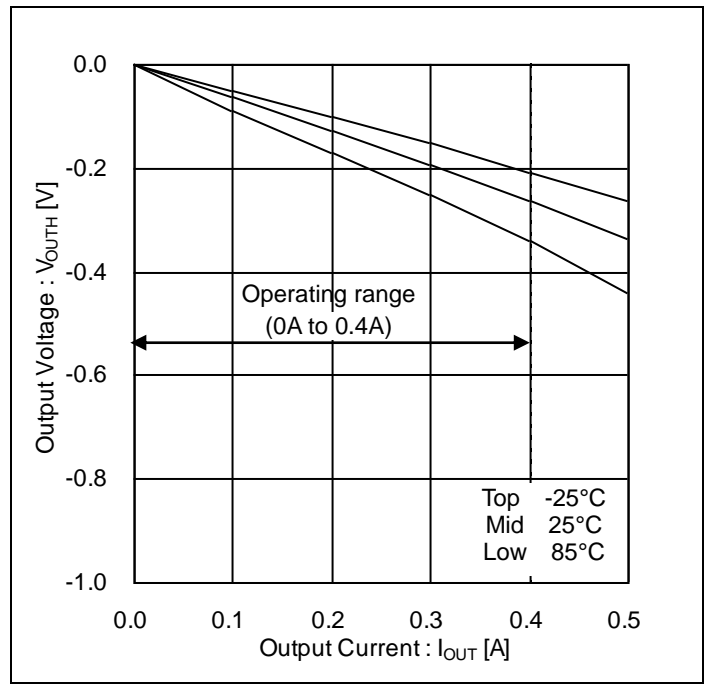


Figure 4.
High Side Output Voltage vs Output Current

Typical Performance Curves (Reference Data) - continued

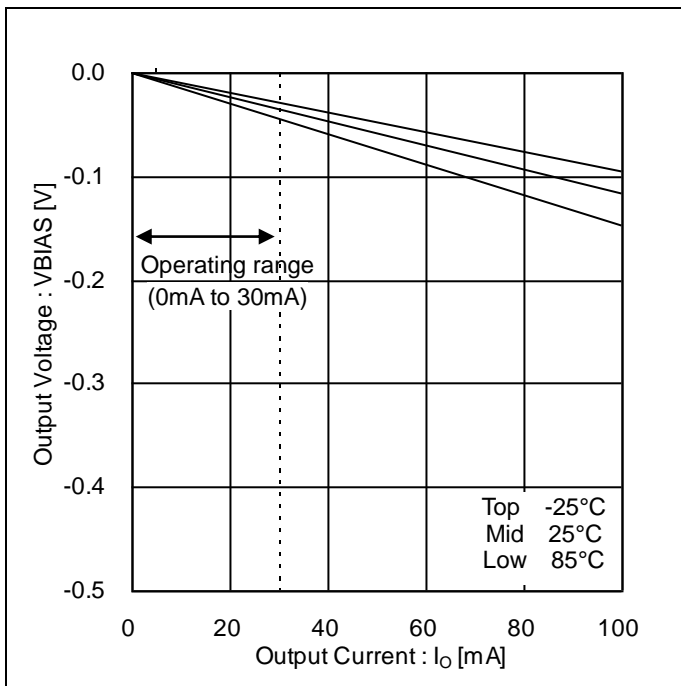


Figure 5.
Regulator Output Voltage vs Output Current

Timing Chart

Table 1. I/O Truth Table

Input Mode	INPUT				OUTPUT			
	PS ^(Note 4)	SEL	IN1A/2A	IN1B/2B	OUT1A/2A	OUT1B/2B	Output Mode ^(Note 5)	
EN/IN	H	L	L	X	Z	Z	Open	
			H	L	H	L	CW	
			H	H	L	H	CCW	
IN/IN		H	H	L	L	Z	Z	Open
				H	L	H	L	CW
				L	H	L	H	CCW
	H			H	L	L	Short Brake	
-	L	X	X	X	Z	Z	Open	

L: Low, H: High, X: Don't care, Z: Hi impedance

(Note 4)PS=High: Operation Mode, PS=Low: Stand-by Mode

(Note 5)CW: Current flows from OUTxA to OUTxB, CCW: Current flows from OUTxB to OUTxA (x=1, 2)

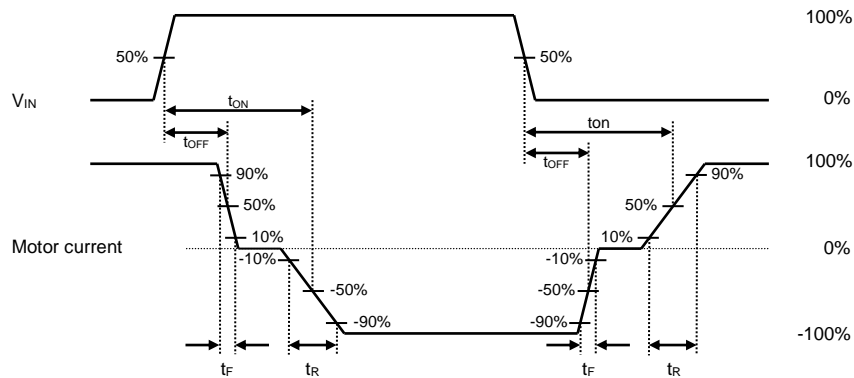


Figure 6. Input-Output AC Characteristic

The Wave-Shaping Circuit Convert

The wave-shaping circuit converts the distorted output signal of the photo-interrupter into rectangular wave. The hysteresis function prevents chatter of the output signal by the noise included in the input signal. This function enables to output the rectangular signal.

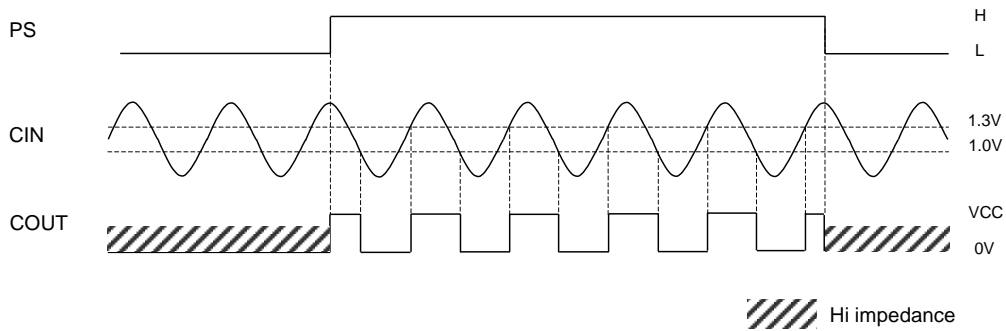
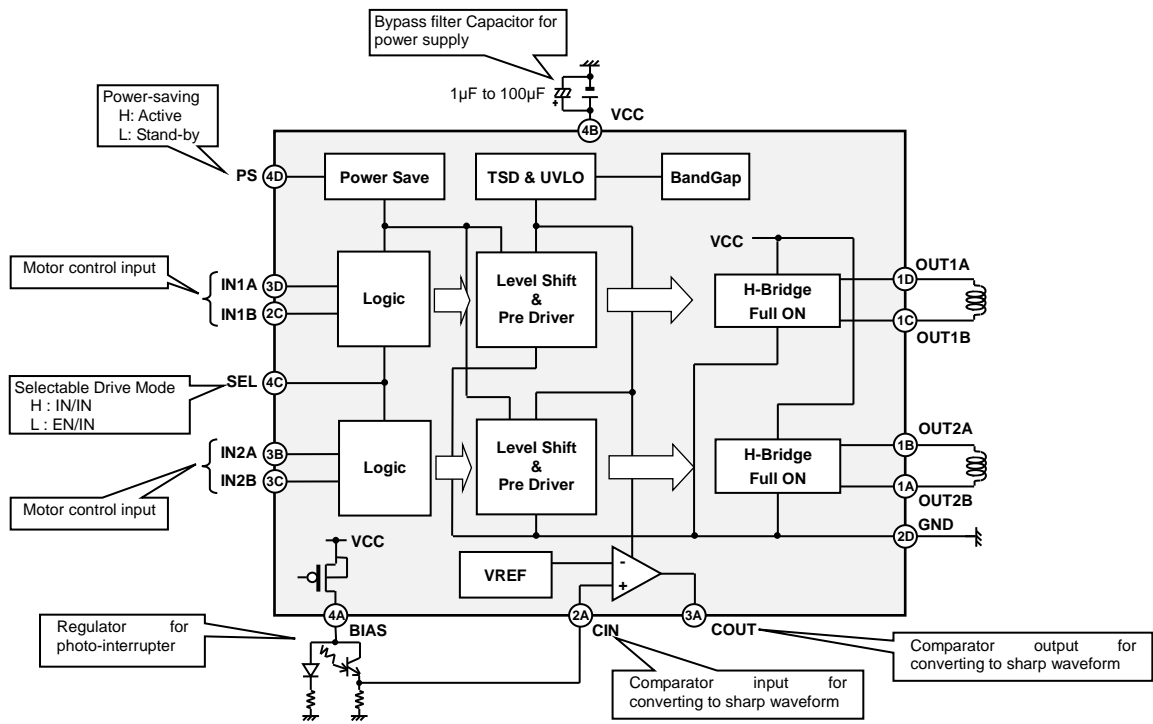


Figure 7. Photo-Interrupter I/O Timing Chart

Application Example



Selection of Components Externally Connected

When using the circuit with changes to the external circuit constants, make sure to leave an adequate margin for external components including static and transitional characteristics as well as dispersion of the IC.

Power Dissipation

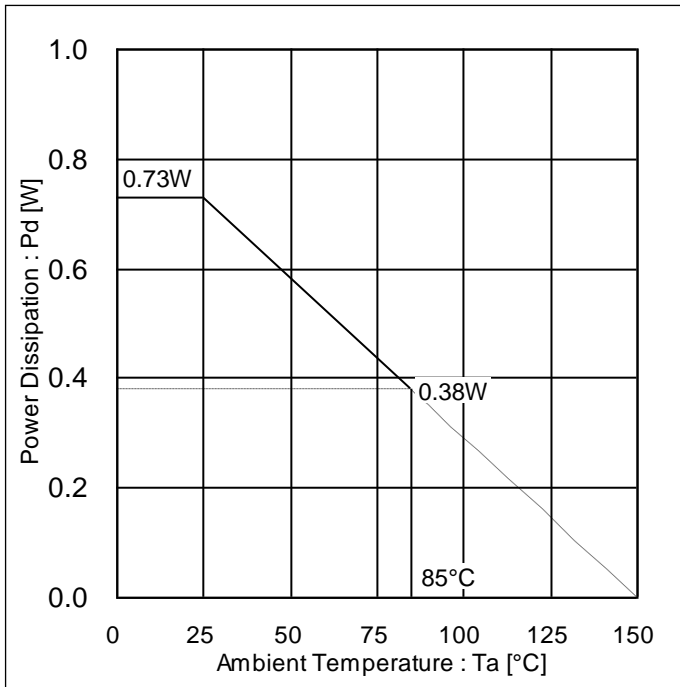
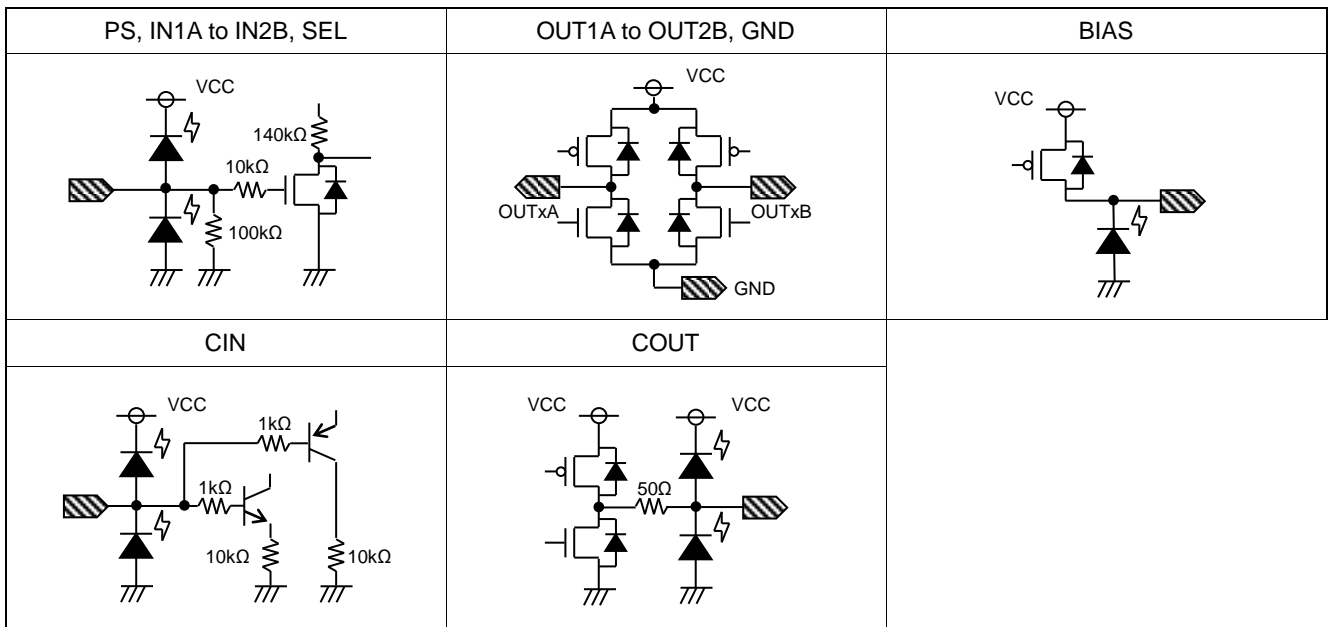


Figure 8.
Power Dissipation vs Ambient Temperature

I/O Equivalence Circuits



Operational Notes

1. Reverse Connection of Power Supply

Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply pins.

2. Power Supply Lines

Design the PCB layout pattern to provide low impedance supply lines. Separate the ground and supply lines of the digital and analog blocks to prevent noise in the ground and supply lines of the digital block from affecting the analog block. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

3. Ground Voltage

Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

4. Ground Wiring Pattern

When using both small-signal(GND) and large-current ground(PGND) traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

5. Thermal Consideration

Should by any chance the power dissipation rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. In case of exceeding this absolute maximum rating, increase the board size and copper area to prevent exceeding the Pd rating.

6. Recommended Operating Conditions

These conditions represent a range within which the expected characteristics of the IC can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

7. Inrush Current

When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

8. Operation Under Strong Electromagnetic Field

Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

9. Testing on Application Boards

When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

10. Inter-pin Short and Mounting Errors

Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

11. Unused Input Pins

Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.

Operational Notes – continued

12. Regarding the Input Pin of the IC

This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of the P layers with the N layers of other elements, creating a parasitic diode or transistor. For example (refer to figure below):

When $GND > Pin A$ and $GND > Pin B$, the P-N junction operates as a parasitic diode.
 When $GND > Pin B$, the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions that cause these diodes to operate, such as applying a voltage lower than the GND voltage to an input pin (and thus to the P substrate) should be avoided.

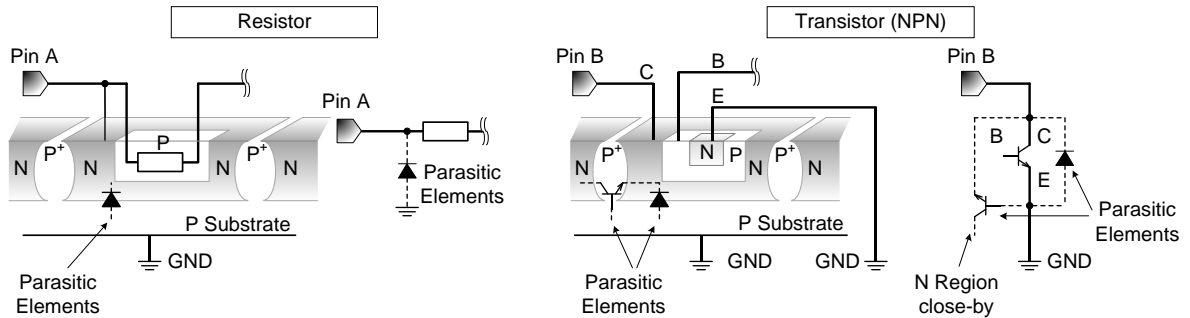


Figure 9.

Example of monolithic IC structure

13. Ceramic Capacitor

When using a ceramic capacitor, determine the dielectric constant considering the change of capacitance with temperature and the decrease in nominal capacitance due to DC bias and others.

14. Area of Safe Operation (ASO)

Operate the IC such that the output voltage, output current, and power dissipation are all within the Area of Safe Operation (ASO).

15. Thermal Shutdown Circuit(TSD)

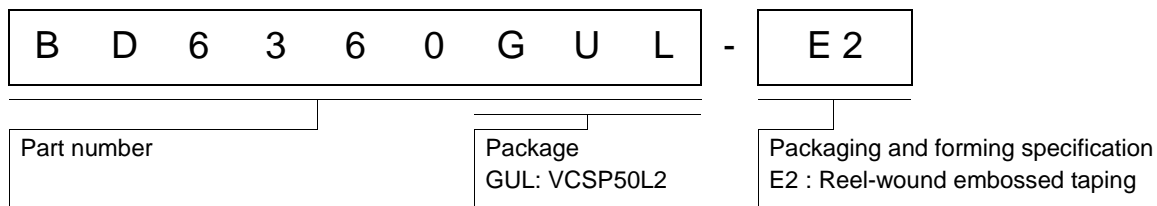
This IC has a built-in thermal shutdown circuit that prevents heat damage to the IC. Normal operation should always be within the IC's power dissipation rating. If however the rating is exceeded for a continued period, the junction temperature (T_j) will rise which will activate the TSD circuit that will turn OFF all output pins. When the T_j falls below the TSD threshold, the circuits are automatically restored to normal operation.

Note that the TSD circuit operates in a situation that exceeds the absolute maximum ratings and therefore, under no circumstances, should the TSD circuit be used in a set design or for any purpose other than protecting the IC from heat damage.

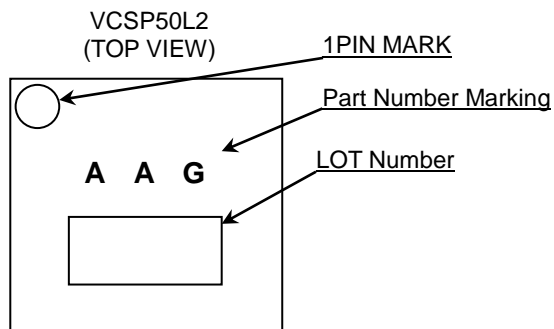
16. Disturbance light

In a device where a portion of silicon is exposed to light such as in a WL-CSP, IC characteristics may be affected due to photoelectric effect. For this reason, it is recommended to come up with countermeasures that will prevent the chip from being exposed to light.

Ordering Information



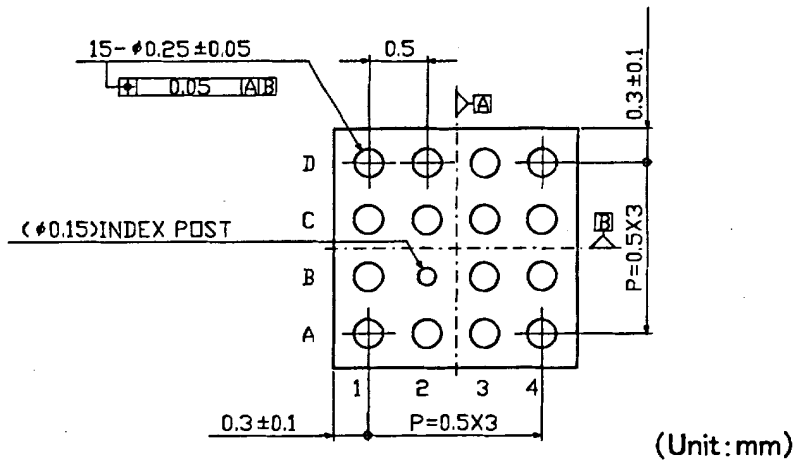
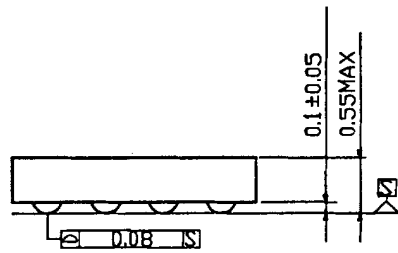
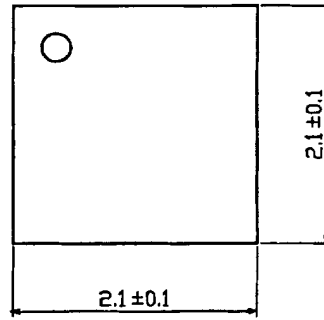
Marking Diagram



Part Number Marking	Package	Orderable Part Number
AAG	VCSP50L2	BD6360GUL-E2

Physical Dimension, Tape and Reel Information

Package Name	VCSP50L2 (BD6360GUL)
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< Tape and Reel information >

Tape	Embossed carrier tape (with dry pack)
Quantity	3000pcs
Direction of feed	E2 (The direction is the 1pin of product is at the upper left when you hold reel on the left hand and you pull out the tape on the right hand.)

※When you order, please order in times the amount of package quantity.

Revision History

Date	Revision	Changes
09.Dec.2015	001	New Release

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JAPAN	USA	EU	CHINA
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CLASS IV		CLASS III	

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 - Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - Sealing or coating our Products with resin or other coating materials
 - Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - Use of the Products in places subject to dew condensation
- The Products are not subject to radiation-proof design.
- Please verify and confirm characteristics of the final or mounted products in using the Products.
- In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
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4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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-  Shortage Management
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