



**THE DATASHEET OF  
LTC1059CS#PBF**





## ABSOLUTE MAXIMUM RATINGS

(Note 1)

Supply Voltage .....	18V
Power Dissipation .....	500mW
Operating Temperature Range	
LTC1059C .....	$-40^{\circ}\text{C} \leq T_A \leq 85^{\circ}\text{C}$
LTC1059AM, LTC1059M .....	$-55^{\circ}\text{C} \leq T_A \leq 125^{\circ}\text{C}$
Storage Temperature Range .....	$-65^{\circ}\text{C}$ to $150^{\circ}\text{C}$
Lead Temperature (Soldering, 10 sec) .....	300°C

## PACKAGE/ORDER INFORMATION

TOP VIEW		ORDER PART NUMBER
BP 1	14 LP	LTC1059CN LTC1059CS
N/AP/HP 2	13 V <sub>O2</sub>	
INV1 3	12 INV2	
S1 4	11 AGND	
S <sub>A</sub> 5	10 V <sup>-</sup>	
V <sup>+</sup> 6	9 50/100/HOLD	
LSh 7	8 CLK	
<p>N PACKAGE      S PACKAGE 14-LEAD PDIP      14-LEAD PLASTIC SO</p> <p>T<sub>JMAX</sub> = 110°C, θ<sub>JA</sub> = 130°C/W (N) T<sub>JMAX</sub> = 110°C, θ<sub>JA</sub> = 110°C/W (S)</p>		LTC1059ACJ LTC1059AMJ LTC1059CJ LTC1059MJ
<p>J PACKAGE 14-LEAD CERDIP</p> <p>T<sub>JMAX</sub> = 150°C, θ<sub>JA</sub> = 80°C/W</p>		
<p><b>OBSELETE PACKAGE</b> Consider the N or S Package for Alternate Source</p>		

Consult LTC Marketing for parts specified with wider operating temperature ranges.

## ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at T<sub>A</sub> = 25°C.

(Complete Filter) V<sub>S</sub> = ±5V, T<sup>2</sup>L clock input level unless otherwise specified.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Center Frequency Range, f <sub>0</sub>	f <sub>0</sub> • Q ≤ 400kHz, Mode 1		0.1 - 40k		Hz
	f <sub>0</sub> • Q ≤ 1.6MHz, Mode 1		0.1 - 18k		Hz
	f <sub>0</sub> • Q ≤ 250kHz, Mode 3, V <sub>S</sub> = ±7.5V		0.1 - 20k		Hz
	f <sub>0</sub> • Q ≤ 1MHz, Mode 3, V <sub>S</sub> = ±7.5V		0.1 - 16k		Hz
Input Frequency Range			0 - 200k		Hz
Clock-to-Center Frequency Ratio	Mode 1, 50:1, f <sub>CLK</sub> = 250kHz, Q = 10	●		50 ± 0.8%	
	Mode 1, 100:1, f <sub>CLK</sub> = 500kHz, Q = 10	●		100 ± 0.8%	
Q Accuracy	Mode 1, 50:1 or 100:1, f <sub>0</sub> = 5kHz Q = 10	●	±0.5	5	%
f <sub>0</sub> Temperature Coefficient	Mode 1, f <sub>CLK</sub> < 500kHz		5		ppm/°C
Q Temperature Coefficient	Mode 1, f <sub>CLK</sub> < 500kHz, Q = 10		15		ppm/°C
DC Offset	V <sub>OS1</sub>	●	2	15	mV
	V <sub>OS2</sub>	●	3	30	mV
	V <sub>OS2</sub>	●	3	40	mV
	V <sub>OS2</sub>	●	6	60	mV
	V <sub>OS2</sub>	●	6	80	mV
	V <sub>OS2</sub>	●	2	20	mV
	V <sub>OS2</sub>	●	2	30	mV
	V <sub>OS2</sub>	●	4	40	mV
	V <sub>OS2</sub>	●	4	60	mV
	V <sub>OS3</sub>	●	2	20	mV
	V <sub>OS3</sub>	●	2	30	mV
	V <sub>OS3</sub>	●	4	40	mV
	V <sub>OS3</sub>	●	4	60	mV

## ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at  $T_A = 25^\circ\text{C}$ .

(Complete Filter)  $V_S = \pm 5\text{V}$ ,  $T^2\text{L}$  Clock Input Level unless otherwise specified.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
DC Lowpass Gain Accuracy	Mode 1, $R_1 = R_2 = 50\text{k}\Omega$	●	$\pm 0.1$	2	%
BP Gain Accuracy at $f_0$	Mode 1, $Q = 10$ , $f_0 = 5\text{kHz}$		$\pm 0.1$		%
Clock Feedthrough	$f_{\text{CLK}} \leq 1\text{MHz}$		10		mV
Max Clock Frequency	Mode 1, $Q < 5$ , $V_S \geq \pm 5\text{V}$		2		MHz
Power Supply Current		●	3.5	5.5 7	mA mA

(Complete Filter)  $V_S = \pm 2.37\text{V}$  unless otherwise specified.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Center Frequency Range	$f_0 \cdot Q \leq 120\text{kHz}$ , Mode 1, 50:1 $f_0 \cdot Q \leq 120\text{kHz}$ , Mode 3, 50:1		0.1 - 12k 0.1 - 10k		Hz Hz
Input Frequency Range			60k		Hz
Clock-to-Center Frequency Ratio	Mode 1, 50:1, $f_{\text{CLK}} = 250\text{kHz}$ , $Q = 10$ Mode 1, 100:1, $f_{\text{CLK}} = 250\text{kHz}$ , $Q = 10$		$50 \pm 0.8\%$ $100 \pm 0.8\%$		
Q Accuracy	Mode 1, $f_{\text{CLK}} = 250\text{kHz}$ , $Q = 10$ 50:1 and 100:1		$\pm 2$		%
Max Clock Frequency			700		kHz
Power Supply Current			1.5	2.5	mA

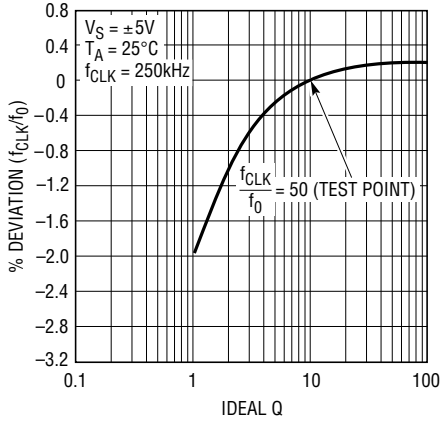
(Internal Op Amps) The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at  $T_A = 25^\circ\text{C}$ .

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Supply Voltage Range		$\pm 2.375$		$\pm 8$	V
Voltage Swings	$V_S = \pm 5\text{V}$ , $R_L = 5\text{k}$ (Pins 1, 14) $R_L = 3.5\text{k}$ (Pins 2, 13)	●	$\pm 3.8$ $\pm 3.6$	$\pm 4.2$	V V
Input Offset Voltage		●	1	15	mV
Input Bias Current			3		pA
Output Short-Circuit Current Source/Sink	$V_S = \pm 5\text{V}$ (N Package) $V_S = \pm 5\text{V}$ (S Package)		40/3 25/3		mA mA
DC Open Loop Gain	$V_S = \pm 5\text{V}$		80		dB
GBW	$V_S = \pm 5\text{V}$		2		MHz
Slew Rate	$V_S = \pm 5\text{V}$		7		V/ $\mu\text{s}$

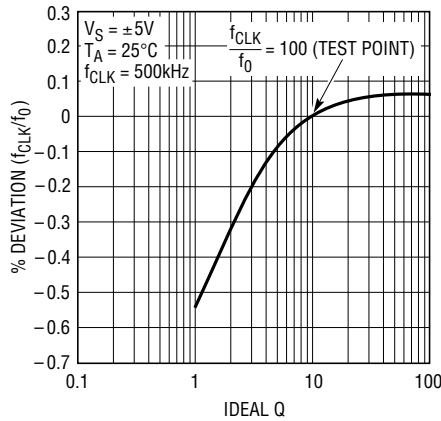
**Note 1:** Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

# TYPICAL PERFORMANCE CHARACTERISTICS

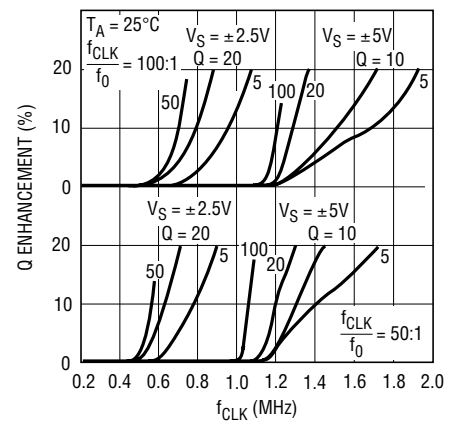
**Graph 1. Mode 1:  
( $f_{CLK}/f_0$ ) Deviation vs Q**



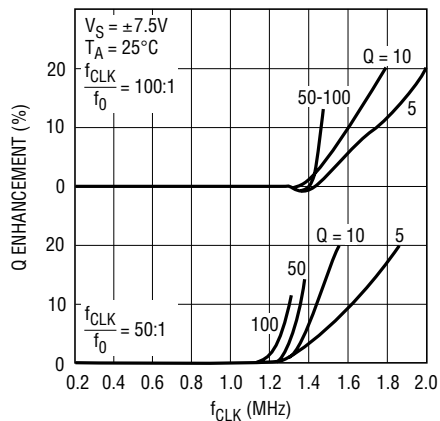
**Graph 2. Mode 1:  
( $f_{CLK}/f_0$ ) Deviation vs Q**



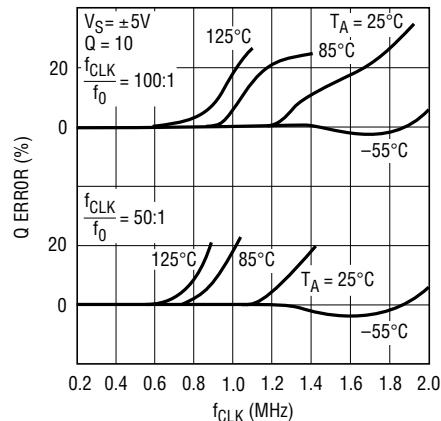
**Graph 3. Mode 1: Q Error  
vs Clock Frequency**



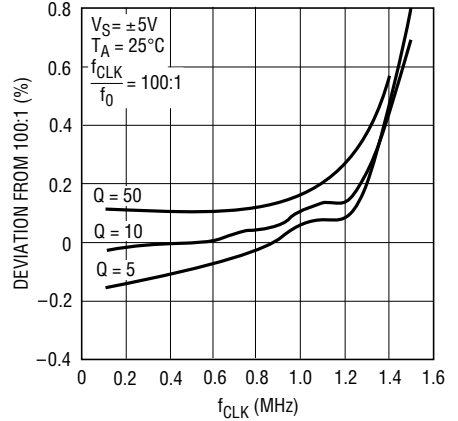
**Graph 4. Mode 1: Q Error  
vs Clock Frequency**



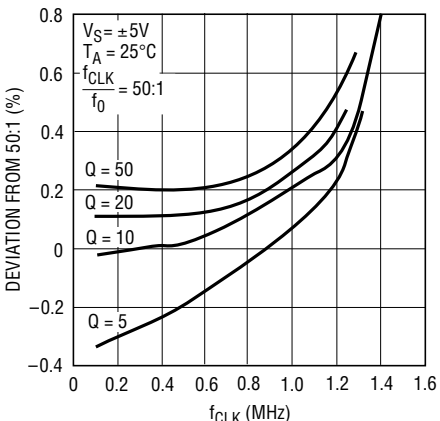
**Graph 5. Mode 1: Measured Q  
vs  $f_{CLK}$  and Temperature**



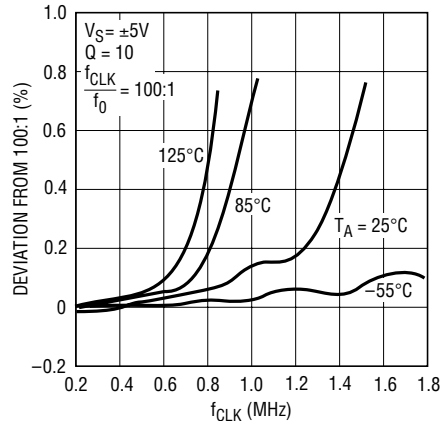
**Graph 6. Mode 1: ( $f_{CLK}/f_0$ )  
vs  $f_{CLK}$  and Q**



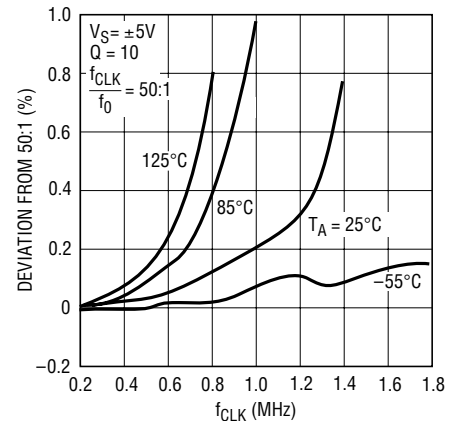
**Graph 7. Mode 1: ( $f_{CLK}/f_0$ )  
vs  $f_{CLK}$  and Q**



**Graph 8. Mode 1: ( $f_{CLK}/f_0$ )  
vs  $f_{CLK}$  and Temperature**

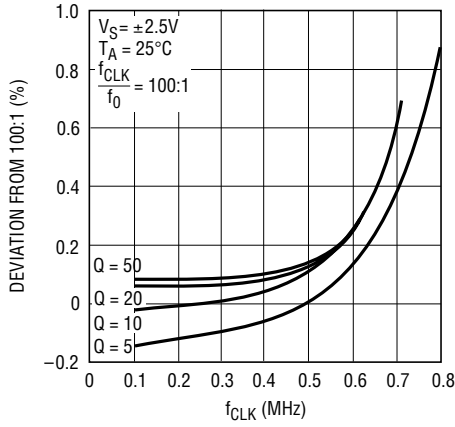


**Graph 9. Mode 1: ( $f_{CLK}/f_0$ )  
vs  $f_{CLK}$  and Temperature**



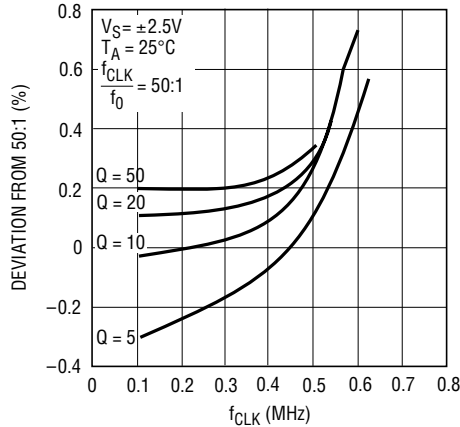
# TYPICAL PERFORMANCE CHARACTERISTICS

**Graph 10. Mode 1: ( $f_{CLK}/f_0$ ) vs  $f_{CLK}$  and Q**



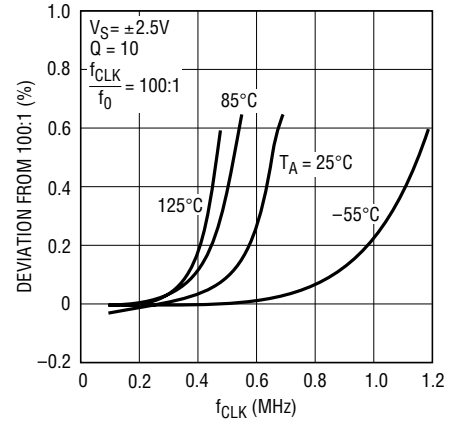
1059 G10

**Graph 11. Mode 1: ( $f_{CLK}/f_0$ ) vs  $f_{CLK}$  and Q**



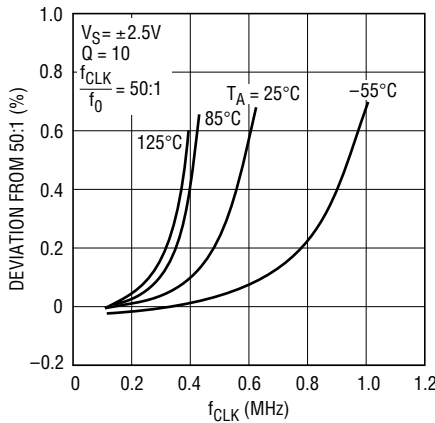
1059 G11

**Graph 12. Mode 1: ( $f_{CLK}/f_0$ ) vs  $f_{CLK}$  and Temperature**



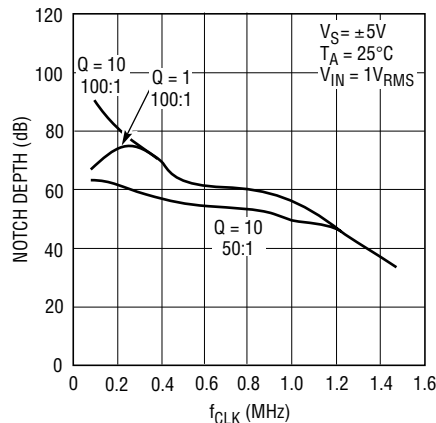
1059 • G12

**Graph 13. Mode 1: ( $f_{CLK}/f_0$ ) vs  $f_{CLK}$  and Temperature**



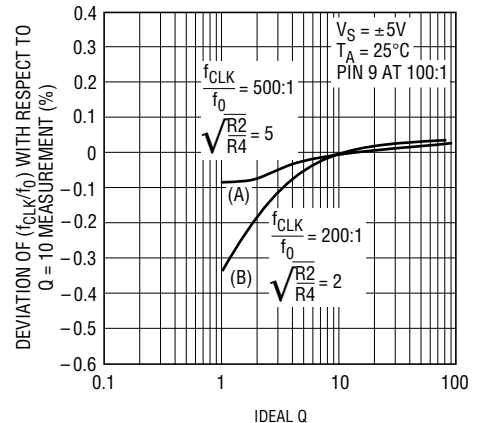
1059 G13

**Graph 14. Mode 1: Notch Depth vs Clock Frequency**



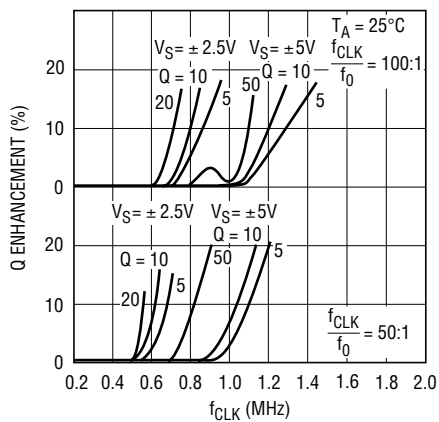
1059 G14

**Graph 15. Mode 3: Deviation of ( $f_{CLK}/f_0$ ) with Respect to Q = 10 Measurement**



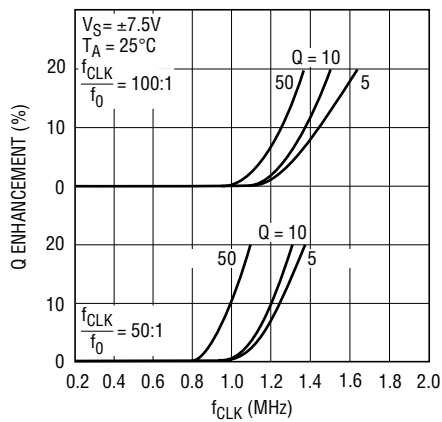
1059 G15

**Graph 16. Mode 3: Q Error vs Clock Frequency**



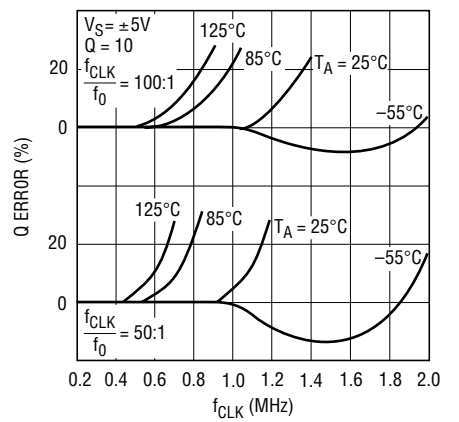
1059 G16

**Graph 17. Mode 3 (R2 = R4): Q Error vs Clock Frequency**



1059 G17

**Graph 18. Mode 3 (R2 = R4): Measured Q vs  $f_{CLK}$  and Temperature**

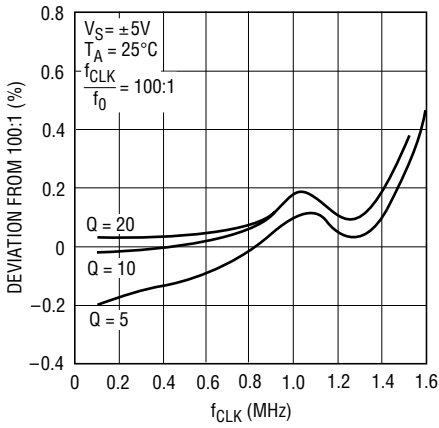


1059 G18

1059fd

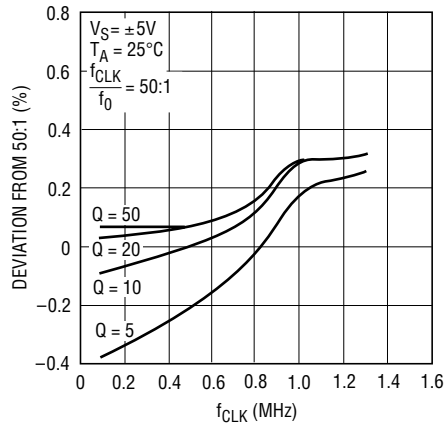
**TYPICAL PERFORMANCE CHARACTERISTICS**

**Graph 19. Mode 3 (R2 = R4):  
(f<sub>CLK</sub>/f<sub>0</sub>) vs f<sub>CLK</sub> and Q**



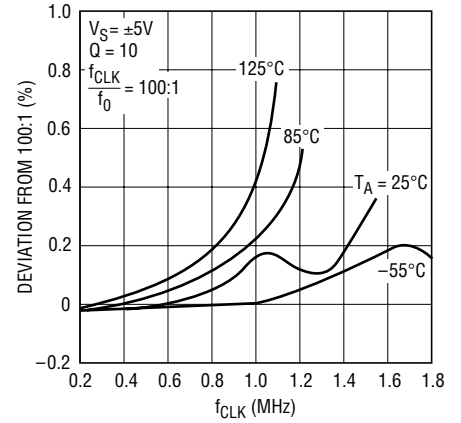
1059 G19

**Graph 20. Mode 3 (R2 = R4):  
(f<sub>CLK</sub>/f<sub>0</sub>) vs f<sub>CLK</sub> and Q**



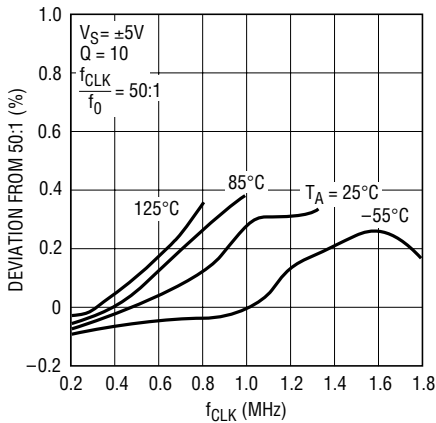
1059 G20

**Graph 21. Mode 3 (R2 = R4):  
(f<sub>CLK</sub>/f<sub>0</sub>) vs f<sub>CLK</sub> and Temperature**



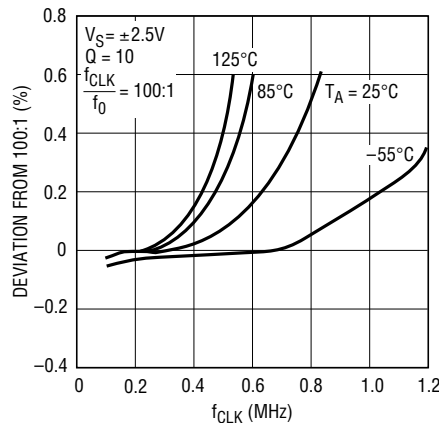
1059 G21

**Graph 22. Mode 3 (R2 = R4):  
(f<sub>CLK</sub>/f<sub>0</sub>) vs f<sub>CLK</sub> and Temperature**



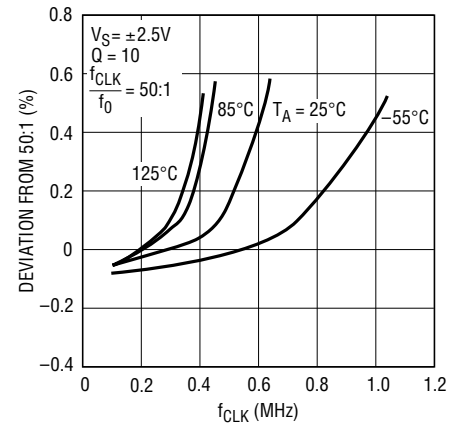
1059 G22

**Graph 23. Mode 3 (R2 = R4):  
(f<sub>CLK</sub>/f<sub>0</sub>) vs f<sub>CLK</sub> and Temperature**



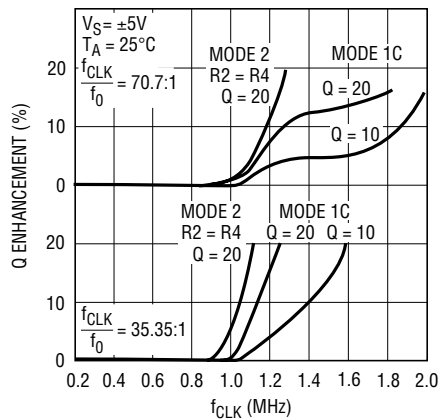
1059 G23

**Graph 24. Mode 3 (R2 = R4):  
(f<sub>CLK</sub>/f<sub>0</sub>) vs f<sub>CLK</sub> and Temperature**



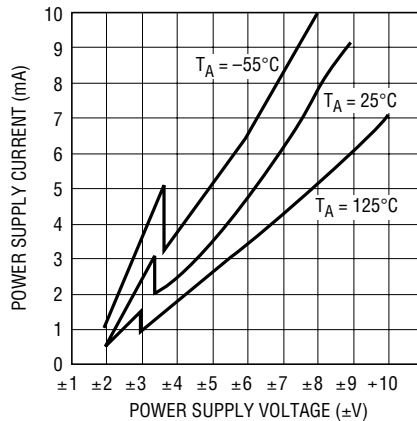
1059 G24

**Graph 25. Mode 1c (R5 = 0),  
Mode 2 (R2 = R4): Q Error vs  
Clock Frequency**



1059 G25

**Graph 26. Supply Current  
vs Supply Voltage**



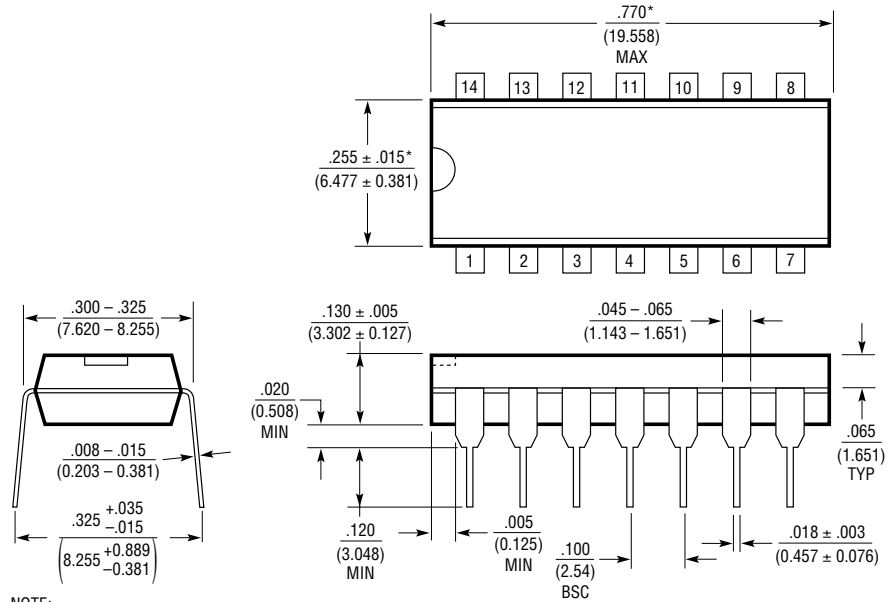
1059 G26

1059fd



**PACKAGE DESCRIPTION**

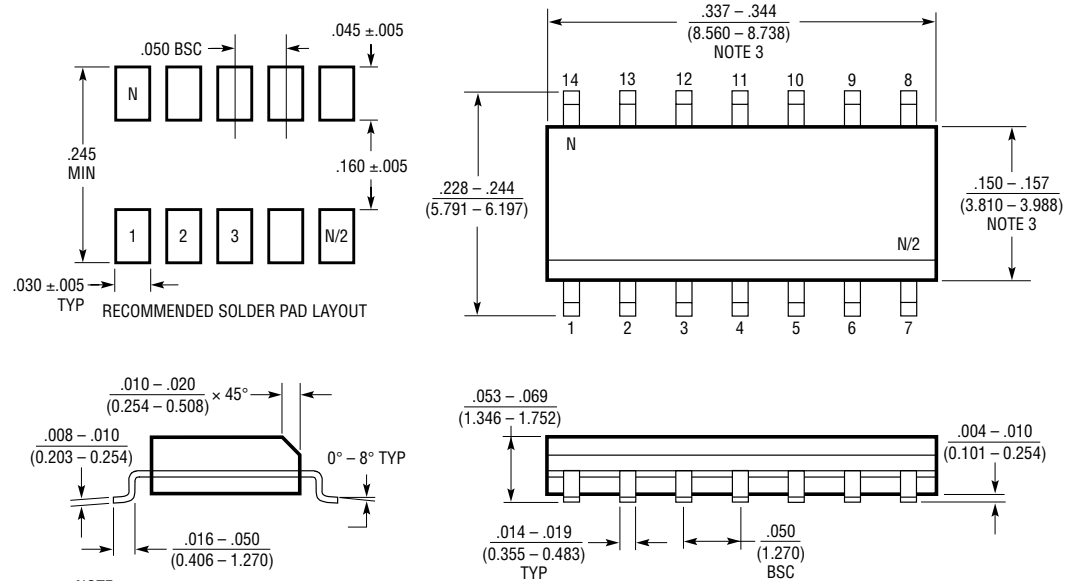
**N Package**  
**14-Lead PDIP (Narrow .300 Inch)**  
 (Reference LTC DWG # 05-08-1510)



NOTE:  
 1. DIMENSIONS ARE  $\frac{\text{INCHES}}{\text{MILLIMETERS}}$   
 \*THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.  
 MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED .010 INCH (0.254mm)

N14 1002

**S Package**  
**14-Lead Plastic Small Outline (Narrow .150 Inch)**  
 (Reference LTC DWG # 05-08-1610)



NOTE:  
 1. DIMENSIONS IN  $\frac{\text{INCHES}}{\text{MILLIMETERS}}$   
 2. DRAWING NOT TO SCALE  
 3. THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.  
 MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED .006" (0.15mm)

S14 0502

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