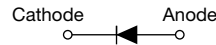




**THE DATASHEET OF
VS-15MQ040NTRPBF**



High Performance Schottky Rectifier, 1.5 A


SMA (DO-214AC)


FEATURES

- Surface mountable
- Extremely low forward voltage
- Compact size
- Improved reverse blocking voltage capability relative to other similar size Schottky
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
COMPLIANT

APPLICATIONS

- Switching power supplies
- Meter protection
- Reverse protection for power input to PC board circuits
- Battery isolation and charging
- Low threshold voltage diode
- Freewheeling or by-pass diode
- Low voltage clamp

DESCRIPTION

The VS-15MQ040NTRPbF Schottky rectifier is designed to be used for low power applications where a reverse voltage of 40 V is encountered and surface mountable is required.

| PRODUCT SUMMARY | |
|-----------------|-----------------|
| Package | SMA (DO-214AC) |
| $I_{F(AV)}$ | 1.5 A |
| V_R | 40 V |
| V_F at I_F | 0.43 V |
| I_{RM} | 20 mA at 125 °C |
| T_J max. | 150 °C |
| Diode variation | Single |
| E_{AS} | 6.0 mJ |

| MAJOR RATINGS AND CHARACTERISTICS | | | |
|-----------------------------------|-------------------------------------|-------------|-------|
| SYMBOL | CHARACTERISTICS | VALUES | UNITS |
| $I_{F(AV)}$ | Rectangular waveform | 1.5 | A |
| V_{RRM} | | 40 | V |
| I_{FSM} | $t_p = 5 \mu s$ sine | 330 | A |
| V_F | $2 A_{pk}$, $T_J = 125 \text{ °C}$ | 0.43 | V |
| T_J | Range | -40 to +150 | °C |

| VOLTAGE RATINGS | | | |
|--------------------------------------|-----------|------------------|-------|
| PARAMETER | SYMBOL | VS-15MQ040NTRPbF | UNITS |
| Maximum DC reverse voltage | V_R | 40 | V |
| Maximum working peak reverse voltage | V_{RWM} | | |

| ABSOLUTE MAXIMUM RATINGS | | | | |
|--|-------------|---|--------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | VALUES | UNITS |
| Maximum average forward current See fig. 4 | $I_{F(AV)}$ | 50 % duty cycle at $T_L = 105 \text{ °C}$, rectangular waveform On PC board 9 mm ² island (0.013 mm thick copper pad area) | 2.1 | A |
| | $I_{F(AV)}$ | 50 % duty cycle at $T_L = 114 \text{ °C}$, rectangular waveform On PC board 9 mm ² island (0.013 mm thick copper pad area) | 1.5 | |
| Maximum peak one cycle non-repetitive surge current See fig. 6 | I_{FSM} | 5 μs sine or 3 μs rect. pulse | 330 | A |
| | | 10 ms sine or 6 ms rect. pulse | | |
| Non-repetitive avalanche energy | E_{AS} | $T_J = 25 \text{ °C}$, $I_{AS} = 1 \text{ A}$, $L = 12 \text{ mH}$ | 6.0 | mJ |
| Repetitive avalanche current | I_{AR} | Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical | 1.0 | A |



| ELECTRICAL SPECIFICATIONS | | | | | |
|---|--------------------------------|---|---------------------------------------|--------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | VALUES | UNITS |
| Maximum forward voltage drop See fig. 1 | V _{FM} ⁽¹⁾ | 1 A | T _J = 25 °C | 0.42 | V |
| | | 2 A | | 0.49 | |
| | | 1 A | T _J = 125 °C | 0.34 | |
| | | 2 A | | 0.43 | |
| Maximum reverse leakage current See fig. 2 | I _{RM} ⁽¹⁾ | T _J = 25 °C | V _R = Rated V _R | 0.5 | mA |
| | | T _J = 125 °C | | 20 | |
| Threshold voltage | V _{F(TO)} | T _J = T _J maximum | | 0.26 | V |
| Forward slope resistance | r _t | | | 64.6 | mΩ |
| Typical junction capacitance | C _T | V _R = 10 V _{DC} , T _J = 25 °C, test signal = 1 MHz | | 134 | pF |
| Typical series inductance | L _S | Measured lead to lead 5 mm from package body | | 2.0 | nH |
| Maximum voltage rate of change | dV/dt | Rated V _R | | 10 000 | V/μs |

Note

(1) Pulse width < 300 μs, duty cycle < 2 %

| THERMAL - MECHANICAL SPECIFICATIONS | | | | | |
|---|--|--|--|-------------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | VALUES | UNITS |
| Maximum junction and storage temperature range | T _J ⁽¹⁾ , T _{Stg} | | | -40 to +150 | °C |
| Maximum thermal resistance, junction to ambient | R _{thJA} | DC operation | | 80 | °C/W |
| Approximate weight | | | | 0.07 | g |
| | | | | 0.002 | oz. |
| Marking device | | Case style SMA (DO-214AC) (similar D-64) | | XF | |

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

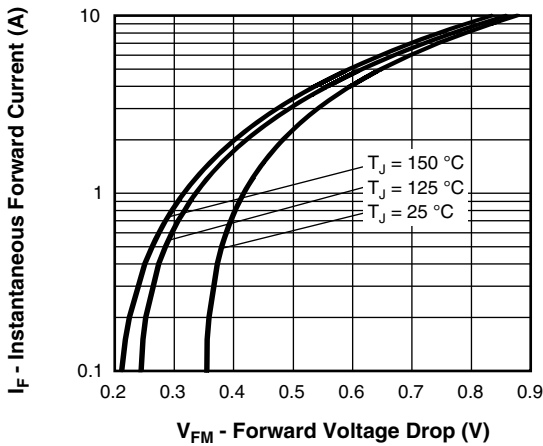


Fig. 1 - Maximum Forward Voltage Drop Characteristics

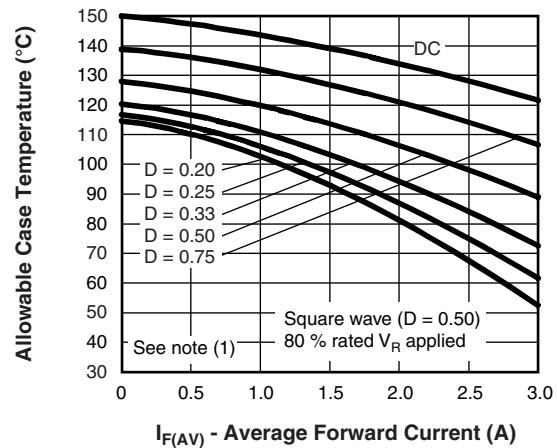


Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature

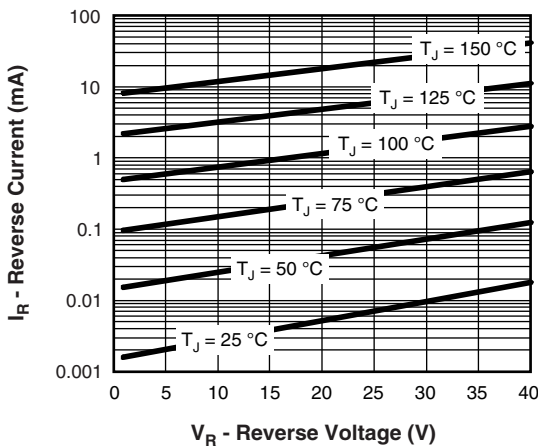


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

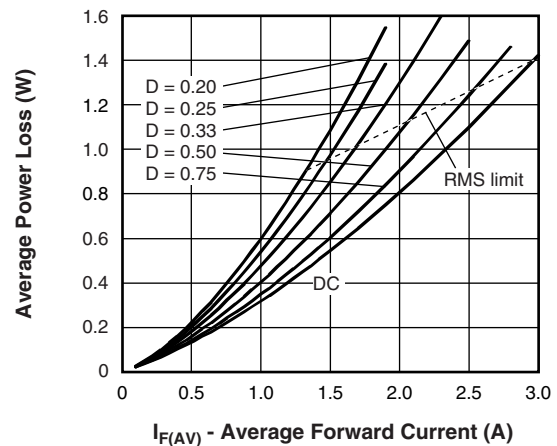


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current

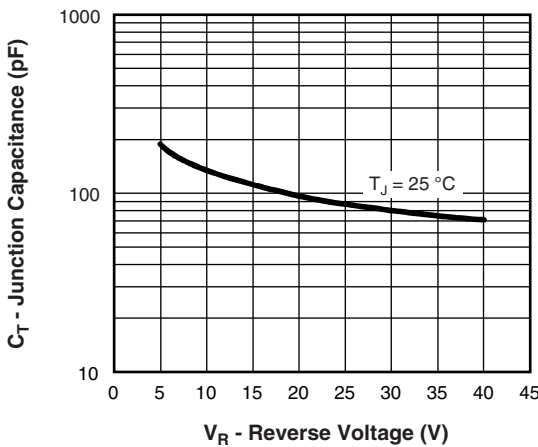


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

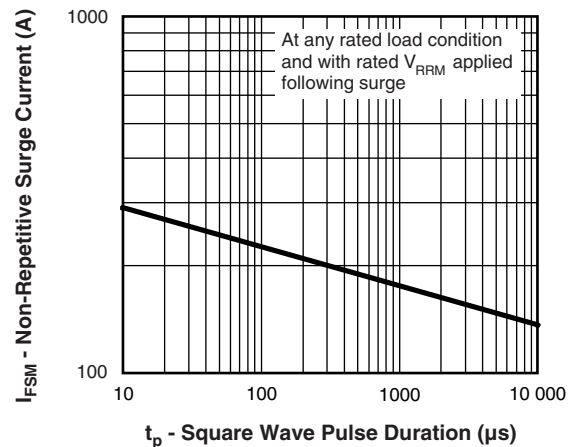


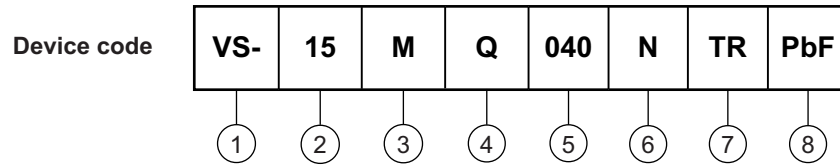
Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{d_{REV}}) \times R_{thJC}$;
 P_d = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 $P_{d_{REV}}$ = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R



ORDERING INFORMATION TABLE



- 1** - Vishay Semiconductors products
- 2** - Current rating (15 = 1.5 A)
- 3** - M = SMA
- 4** - Q = Schottky "Q" series
- 5** - Voltage rating (040 = 40 V)
- 6** - N = new SMA
- 7** - TR = tape and reel
- 8** - PbF = lead (Pb)-free

| ORDERING INFORMATION (Example) | | | |
|--------------------------------|------------------------|------------------------|------------------------------------|
| PREFERRED P/N | PREFERRED PACKAGE CODE | MINIMUM ORDER QUANTITY | PACKAGING DESCRIPTION |
| VS-15MQ040NTRPbF | 5AT | 7500 | 13" diameter plastic tape and reel |

| LINKS TO RELATED DOCUMENTS | |
|----------------------------|--|
| Dimensions | www.vishay.com/doc?95400 |
| Part marking information | www.vishay.com/doc?95403 |
| Packaging information | www.vishay.com/doc?95404 |
| SPICE model | www.vishay.com/doc?95273 |

SMA

DIMENSIONS in inches (millimeters)





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