



**THE DATASHEET OF
9P956AFLF**



9P956AFLF

Category: Fan-out Buffers
Generic Part: 9P956
Market Group: PC CLOCK
Description: PC BUFFER
 DDR II fanout buffer for VIA Chipset

Output Features:

- Low skew, fanout buffer
- I2C for functional and output control
- Single bank 1-6 differential clock distribution
- 1 pair of differential feedback pins for input to output synchronization
- Supports up to 2 DDR DIMMs
- 400MHz (DDRII 800) output frequency support
- Programmable skew through SMBus
- Frequency defect control through SMBus
- Individual output control programmable through SMBus



Parameters

Temperature	C
Voltage	3.3 V
Package	SSOP 28
Speed	NA
Status	Active
Sample	Yes
Minimum Order Quantity	141
Factory Order Increment	47

Distributor Inventory

No Pricing information is available from our Distributors at this time.

Documents


Type	Title	Size	Revision Date
Misc	PC Clocks Contact Info	61 KB	05/29/2007

Package

Package	SSOP 28 (PYG28)
Description	SSOP 5.3 MM
Status	Active
Class	PLASTIC
Category	Green
Pb (Lead) Free	Yes
Mark	F
Package Type	SSOP
Lead Count	28
Length	10.2 mm
Width	5.3 mm
Thickness	1.73 mm
Pitch	0.65 mm
Tube / Tray	TUBE
Quantity per Tube/Tray	47
Quantity per Reel	1000
Moisture Sensitivity Level (MSL)	1
Moisture Exposure Floor Life	Unlimited@<30C/85% RH
Peak Reflow Temperature	260Å°C
Rebake Conditions	NA
Related Package Documents	view documents

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-  [IDT, Integrated Device Technology Inc Information](#)

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