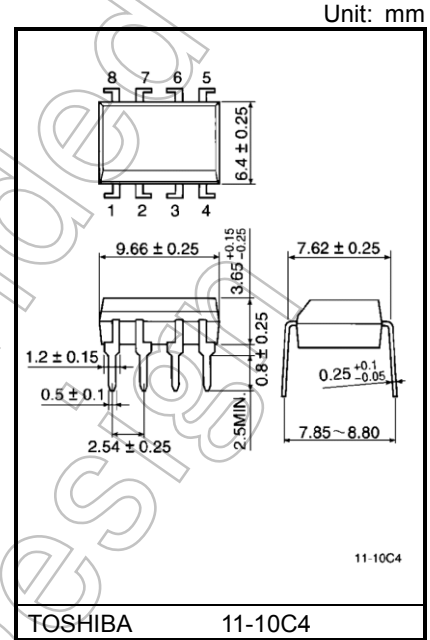


TLP250

Industrial Inverter
 Inverter For Air Conditioner
 IGBT Gate Drive
 Power MOS FET Gate Drive

The TOSHIBA TLP250 consists of a GaAlAs light emitting diode and a integrated photodetector.
 This unit is 8-lead DIP package.
 TLP250 is suitable for gate driving circuit of IGBT or power MOS FET.

- Input threshold current: 5mA(max)
- Supply current : 11mA(max)
- Supply voltage : 10-35V
- Output current : $\pm 1.5A$ (max)
- Switching time t_{pLH}/t_{pHL} : 0.5 μ s(max)
- Isolation voltage: 2500Vrms(min)
- UL recognized: UL1577, file No.E67349
- c-UL approved : CSA Component Acceptance Service No. 5A, File No.E67349
- Option(D4)
 VDE Approved : EN60747-5- 5
Note: When a EN60747-5-5 approved type is needed, Please designate "Option(D4)"

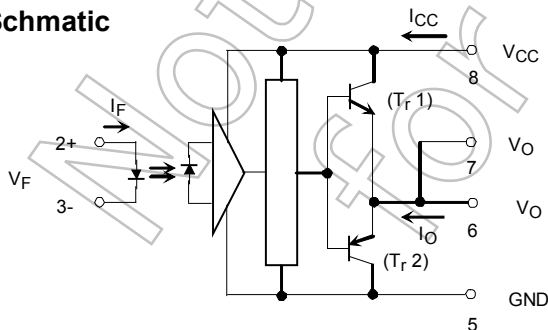


Weight: 0.54 g (typ.)

Truth Table

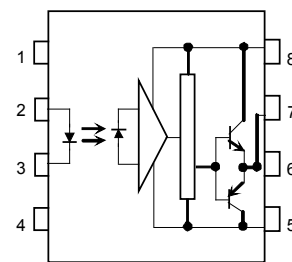
		Tr1	Tr2
Input LED	On	On	Off
	Off	Off	On

Schematic



A 0.1 μ F bypass capacitor must be connected between pin 8 and 5

Pin Configuration (top view)



- 1 : N.C.
- 2 : Anode
- 3 : Cathode
- 4 : N.C.
- 5 : GND
- 6 : V_O (Output)
- 7 : V_O
- 8 : V_{CC}

Start of commercial production
 1990-11

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
LED	Forward current	IF	20	mA	
	Forward current derating (Ta ≥ 70°C)	ΔIF / ΔTa	-0.36	mA / °C	
	Peak transient forward current (Note 1)	IFPT	1	A	
	Reverse voltage	VR	5	V	
	Diode power dissipation	PD	40	mW	
	Diode power dissipation derating (Ta ≥ 70°C)	ΔPD / °C	-0.72	mW / °C	
	Junction temperature	TJ	125	°C	
Detector	"H" peak output current (PW ≤ 2.5μs, f ≤ 15kHz) (Note 2)	IOPH	-1.5	A	
	"L" peak output current (PW ≤ 2.5μs, f ≤ 15kHz) (Note 2)	IOPL	+1.5	A	
	Output voltage	(Ta ≤ 70°C)	VO	35	V
		(Ta ≤ 85°C)		24	
	Supply voltage	(Ta ≤ 70°C)	VCC	35	V
		(Ta ≤ 85°C)		24	
	Output voltage derating (Ta ≥ 70°C)	ΔVO / ΔTa	-0.73	V / °C	
	Supply voltage derating (Ta ≥ 70°C)	ΔVCC / ΔTa	-0.73	V / °C	
	Power dissipation	PC	800	mW	
	Power dissipation derating (Ta ≥ 70°C)	ΔPC / °C	-14.5	mW / °C	
	Junction temperature	TJ	125	°C	
Operating frequency (Note 3)	f	25	kHz		
Operating temperature range	Topr	-20 to 85	°C		
Storage temperature range	Tstg	-55 to 125	°C		
Lead soldering temperature (10 s)	Tsol	260	°C		
Isolation voltage (AC, 60 s., R.H. ≤ 60%) (Note 4)	BVS	2500	Vrms		

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pulse width PW ≤ 1μs, 300pps

Note 2: Exponential waveform

Note 3: Exponential waveform, IOPH ≤ -1.0A (≤ 2.5μs), IOPL ≤ +1.0A (≤ 2.5μs)

Note 4: Device considered a two terminal device: Pins 1, 2, 3 and 4 shorted together, and pins 5, 6, 7 and 8 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min	Typ.	Max	Unit
Input current, on	IF(ON)	7	8	10	mA
Input voltage, off	VF(OFF)	0	—	0.8	V
Supply voltage	VCC	15	—	30	V
Peak output current	IOPH/IOPL	—	—	±0.5	A
Operating temperature	Topr	-20	25	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Note : A ceramic capacitor(0.1μF) should be connected from pin 8 to pin 5 to stabilize the operation of the high gain linear amplifier. Failure to provide the bypassing may impair the switching property. The total lead length between capacitor and coupler should not exceed 1cm.

Note : Input signal rise time(fall time)<0.5μs.

Electrical Characteristics (Ta = -20 to 70°C, unless otherwise specified)

Characteristic		Symbol	Test Circuit	Test Condition	Min	Typ.*	Max	Unit	
Input forward voltage		V _F	—	I _F = 10 mA, Ta = 25°C	—	1.6	1.8	V	
Temperature coefficient of forward voltage		ΔV _F / ΔTa	—	I _F = 10 mA	—	-2.0	—	mV / °C	
Input reverse current		I _R	—	V _R = 5V, Ta = 25°C	—	—	10	μA	
Input capacitance		C _T	—	V = 0 V, f = 1MHz, Ta = 25°C	—	45	250	pF	
Output current	"H" level	I _{OPH}	1	V _{CC} = 30V (Note 1)	I _F = 10 mA V ₈₋₆ = 4V	-0.5	-1.5	—	A
	"L" level	I _{OPL}	2		I _F = 0 mA V ₆₋₅ = 2.5V	0.5	2	—	
Output voltage	"H" level	V _{OH}	3	V _{CC1} = +15V, V _{EE1} = -15V R _L = 200Ω, I _F = 5mA	11	12.8	—	V	
	"L" level	V _{OL}	4	V _{CC1} = +15V, V _{EE1} = -15V R _L = 200Ω, V _F = 0.8V	—	-14.2	-12.5		
Supply current	"H" level	I _{CCH}	—	V _{CC} = 30V, I _F = 10mA Ta = 25°C	—	7	—	mA	
				V _{CC} = 30V, I _F = 10mA	—	—	11		
	"L" level	I _{CCL}	—	V _{CC} = 30V, I _F = 0mA Ta = 25°C	—	7.5	—		
				V _{CC} = 30V, I _F = 0mA	—	—	11		
Threshold input current	"Output L→H"	I _{FLH}	—	V _{CC1} = +15V, V _{EE1} = -15V R _L = 200Ω, V _O > 0V	—	1.2	5	mA	
Threshold input voltage	"Output H→L"	V _{FHL}	—	V _{CC1} = +15V, V _{EE1} = -15V R _L = 200Ω, V _O < 0V	0.8	—	—	V	
Supply voltage		V _{CC}	—	—	10	—	35	V	
Capacitance (input-output)		C _S	—	V _S = 0 V, f = 1MHz Ta = 25°C	—	1.0	2.0	pF	
Resistance(input-output)		R _S	—	V _S = 500V, Ta = 25°C R.H. ≤ 60%	1×10 ¹²	10 ¹⁴	—	Ω	

* All typical values are at Ta = 25°C

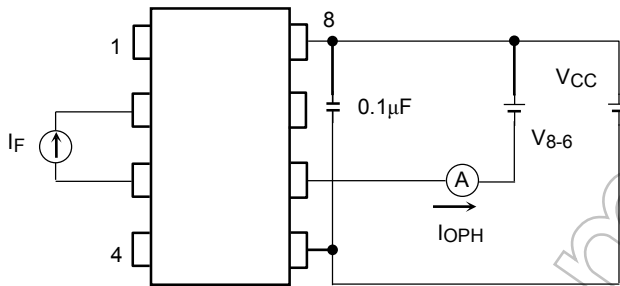
Note 1: Duration of I_O time ≤ 50μs

Switching Characteristics (Ta = -20 to 70°C, unless otherwise specified)

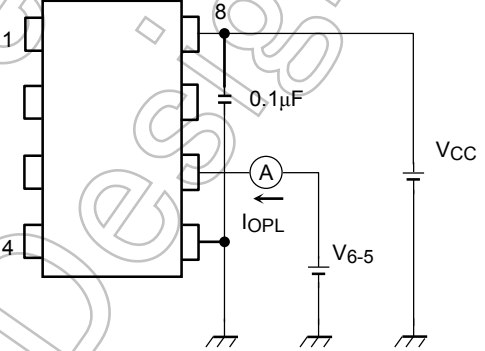
Characteristic	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Propagation delay time	L→H	5	IF = 8mA VCC1 = +15V, VEE1 = -15V RL = 200Ω	—	0.15	0.5	μs
	H→L			—	0.15	0.5	
Common mode transient immunity at high level output	CMH	6	VCM = 600V, IF = 8mA VCC = 30V, Ta = 25°C	-5000	—	—	V / μs
Common mode transient immunity at low level output	CML		VCM = 600V, IF = 0mA VCC = 30V, Ta = 25°C	5000	—	—	V / μs

Note: All typical values are at Ta = 25°C

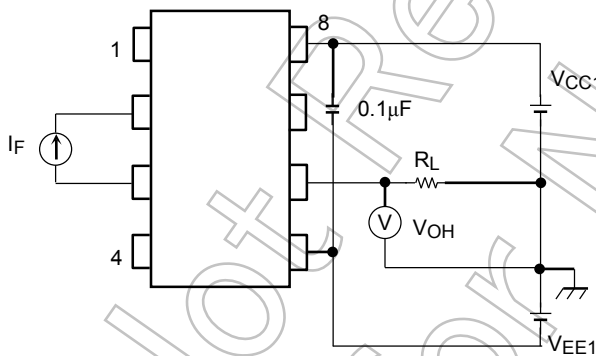
Test Circuit 1 : IOPH



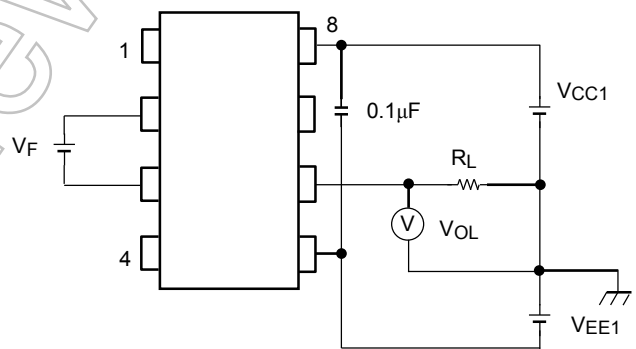
Test Circuit 2 : IOPL



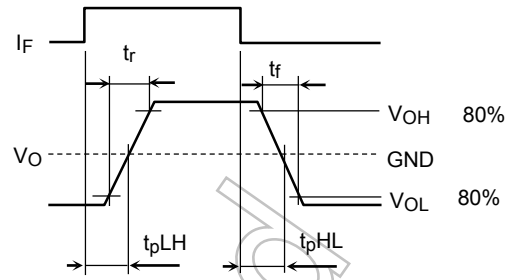
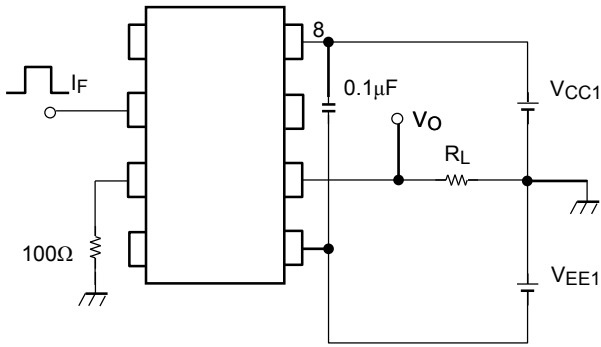
Test Circuit 3 : VOH



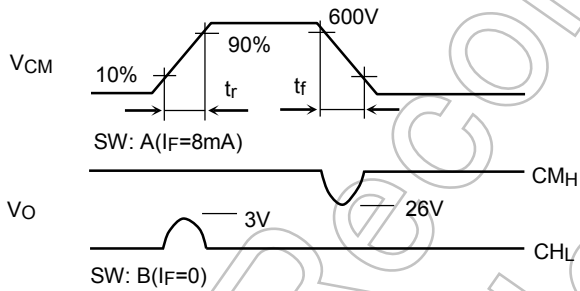
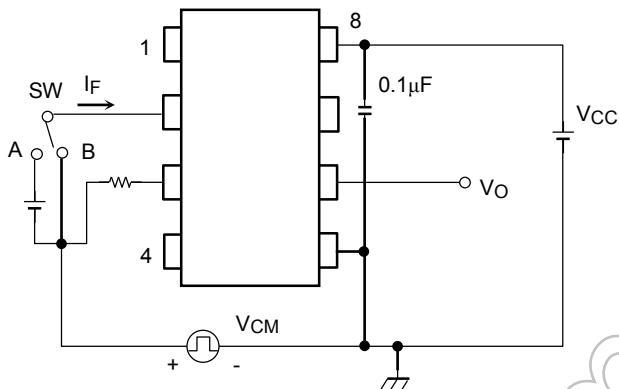
Test Circuit 4 : VOL



Test Circuit 5: t_{pLH} , t_{pHL} , t_r , t_f



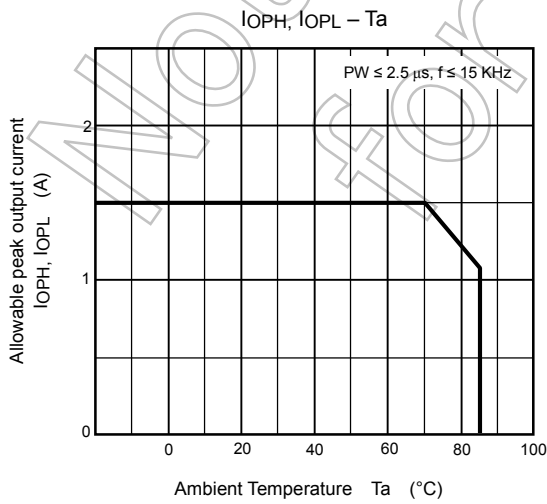
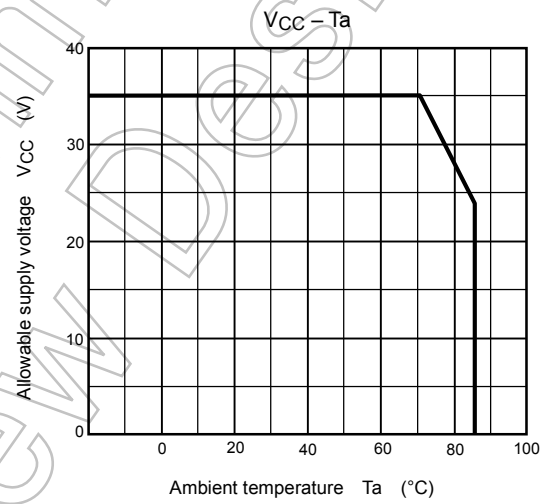
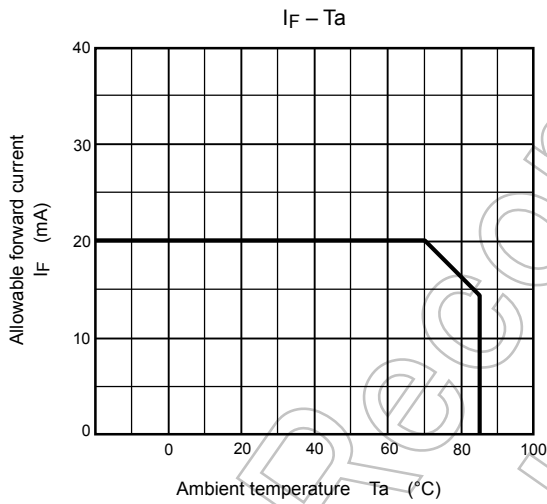
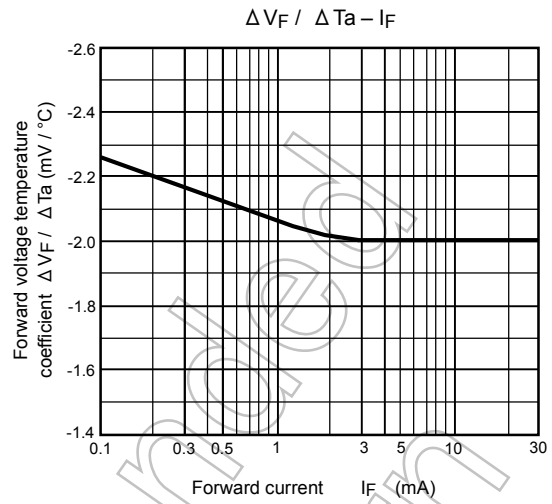
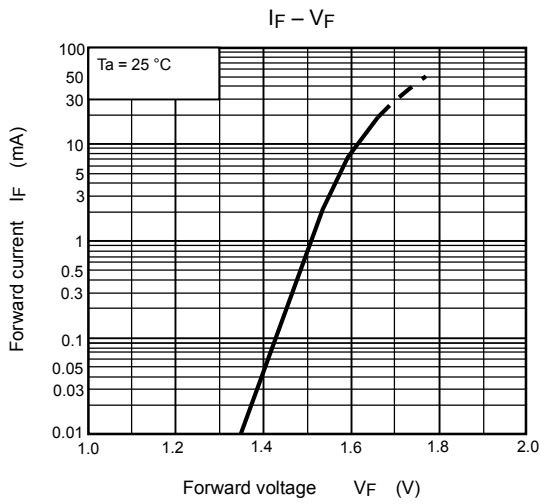
Test Circuit 6: CM_H , CM_L



$$CM_L = \frac{480 \text{ (V)}}{t_r \text{ (\mu s)}}$$

$$CM_H = \frac{480 \text{ (V)}}{t_f \text{ (\mu s)}}$$

CM_L (CM_H) is the maximum rate of rise (fall) of the common mode voltage that can be sustained with the output voltage in the low (high) state.



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