



THE DATASHEET OF BAT54A-QR





BAT54A-Q

Schottky barrier diode

15 June 2021

Product data sheet

1. General description

Planar Schottky barrier diode with an integrated guard ring for stress protection, encapsulated in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Low forward voltage
- Low capacitance
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Ultra high-speed switching
- Line termination
- Voltage clamping
- Reverse polarity protection

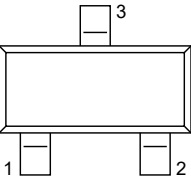
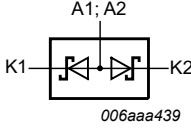
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_R	reverse voltage		-	-	30	V
V_F	forward voltage	$I_F = 100 \text{ mA}$; $t_p \leq 300 \text{ } \mu\text{s}$; $\delta \leq 0.02$; pulsed; $T_{amb} = 25 \text{ } ^\circ\text{C}$	-	-	800	mV
I_R	reverse current	$V_R = 25 \text{ V}$; $t_p \leq 300 \text{ } \mu\text{s}$; $\delta \leq 0.02$; pulsed; $T_{amb} = 25 \text{ } ^\circ\text{C}$	-	-	2	μA

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)	 <p style="text-align: center;">SOT23</p>	 <p style="text-align: center;">006aaa439</p>
2	K2	cathode (diode 2)		
3	A1, A2	common anode		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BAT54A-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code ^[1]
BAT54A-Q	%V3

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_R	reverse voltage			-	30	V
I_F	forward current	$T_{amb} = 25\text{ °C}$		-	200	mA
I_{FRM}	repetitive peak forward current	$t_p \leq 1\text{ s}; \delta \leq 0.5; T_{amb} = 25\text{ °C}$		-	300	mA
I_{FSM}	non-repetitive peak forward current	$t_p < 10\text{ ms}; T_{j(init)} = 25\text{ °C}$		-	600	mA
Per device; one diode loaded						
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ °C}$	[1]	-	250	mW
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-55	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	500	K/W

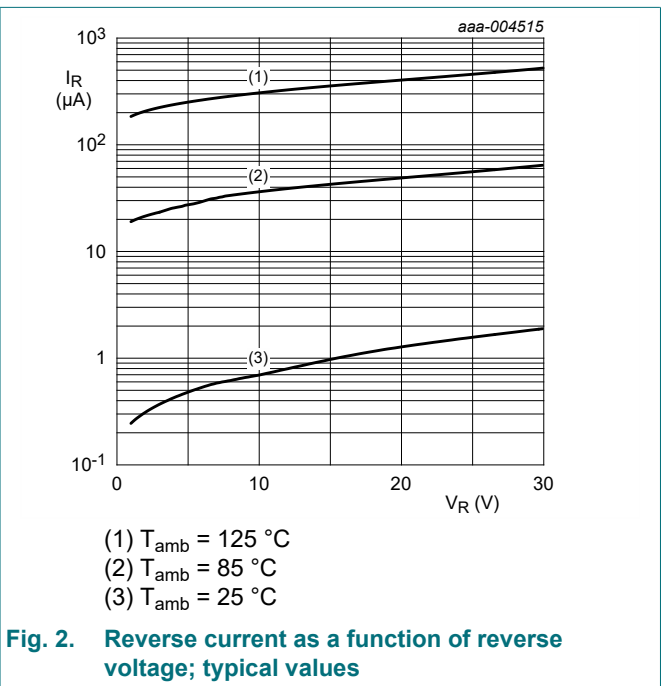
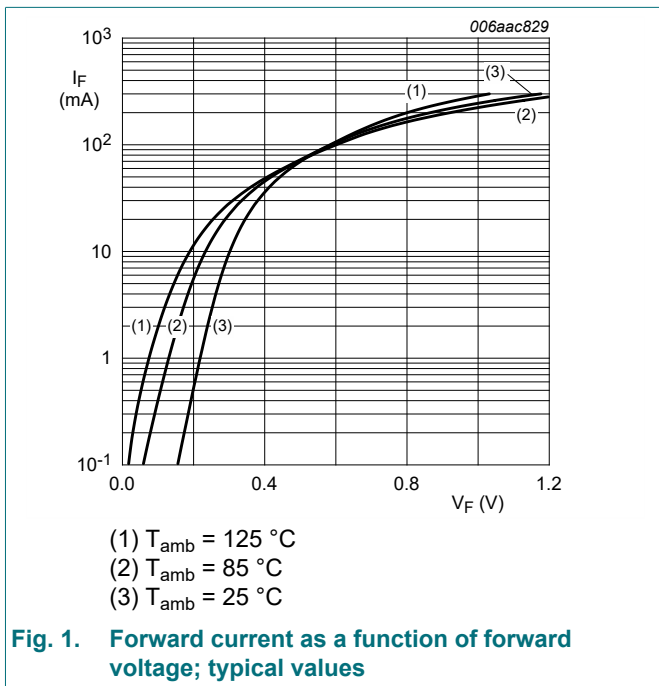
[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses PR are a significant part of the total power losses.

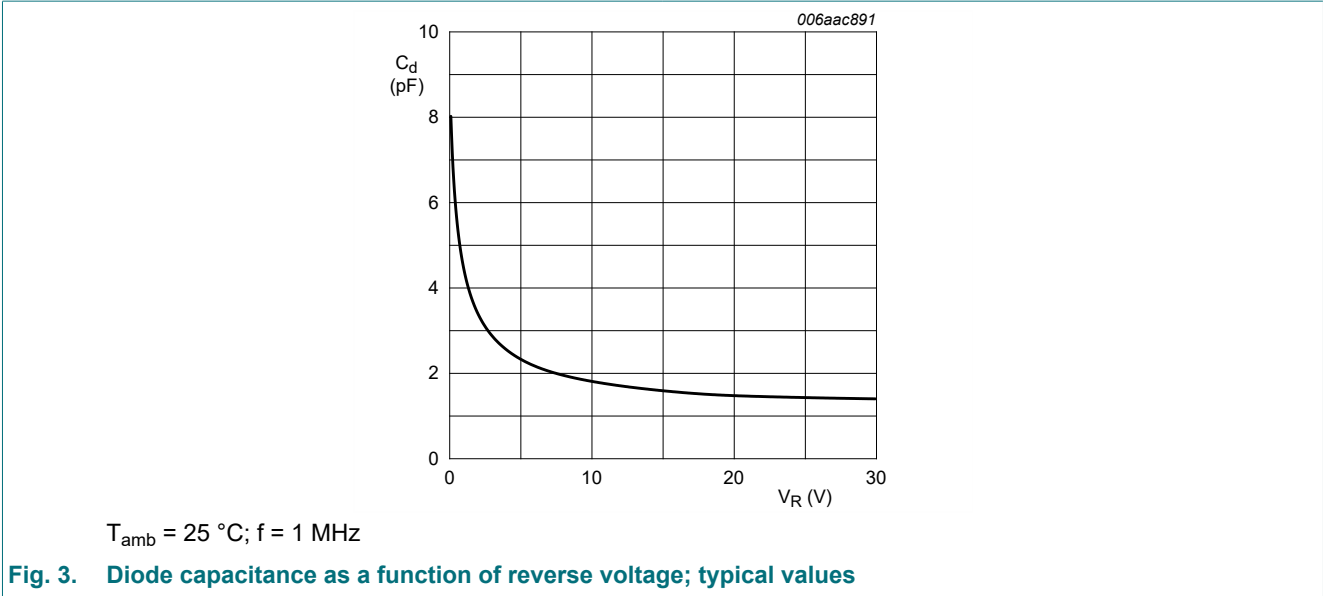
[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

10. Characteristics

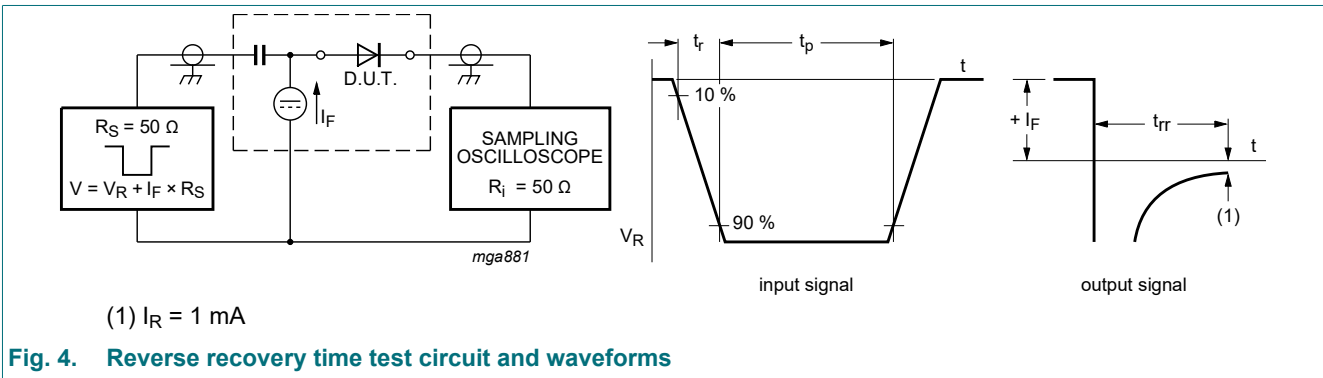
Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_F	forward voltage	$I_F = 0.1 \text{ mA}$; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; pulsed; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	240	mV
		$I_F = 1 \text{ mA}$; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; pulsed; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	320	mV
		$I_F = 10 \text{ mA}$; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; pulsed; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	400	mV
		$I_F = 30 \text{ mA}$; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; pulsed; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	500	mV
		$I_F = 100 \text{ mA}$; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; pulsed; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	800	mV
I_R	reverse current	$V_R = 25 \text{ V}$; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; pulsed; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	2	μA
C_d	diode capacitance	$V_R = 1 \text{ V}$; $f = 1 \text{ MHz}$; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	10	pF
t_{rr}	reverse recovery time	$I_F = 10 \text{ mA}$; $I_R = 10 \text{ mA}$; $I_{R(\text{meas})} = 1 \text{ mA}$; $R_L = 100 \Omega$; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	5	ns





11. Test information



Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

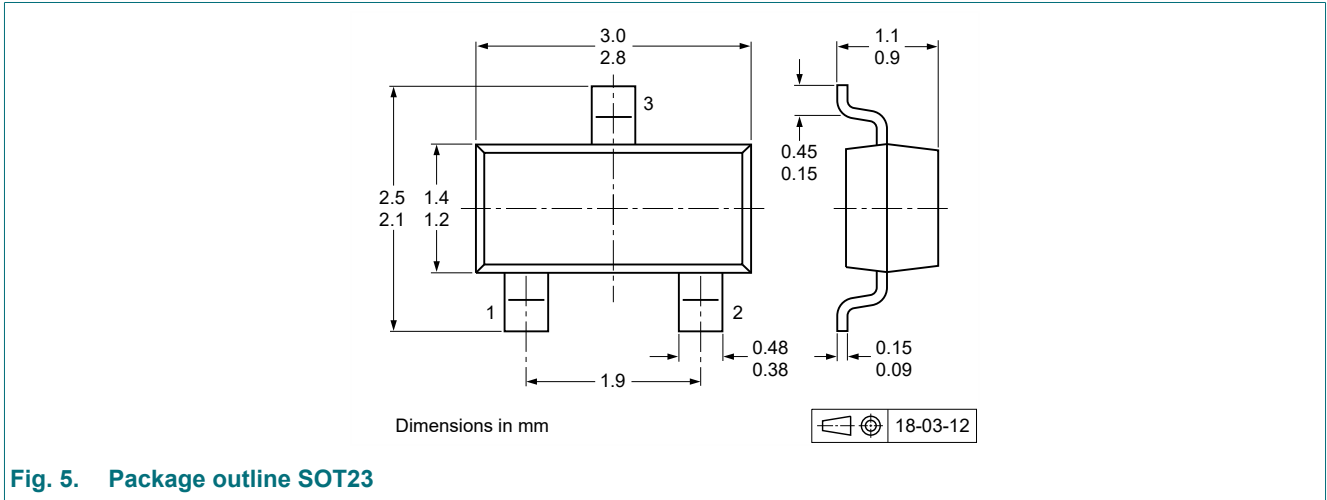


Fig. 5. Package outline SOT23

13. Soldering

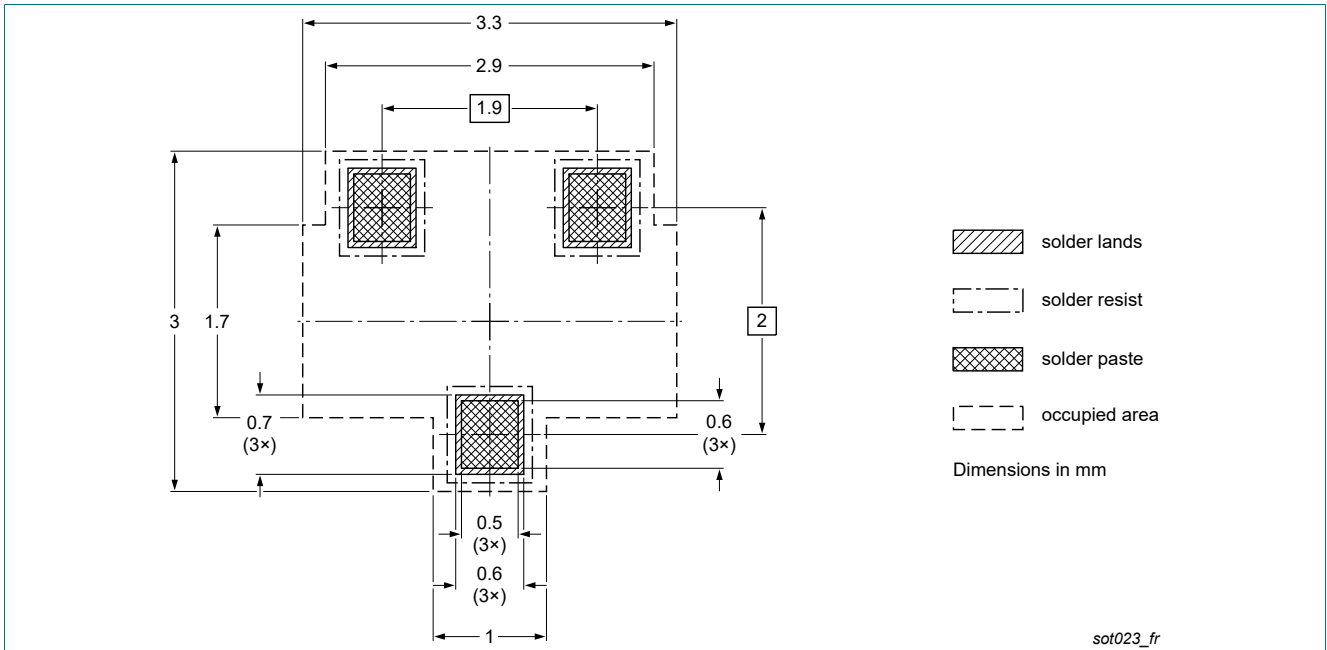


Fig. 6. Reflow soldering footprint for SOT23

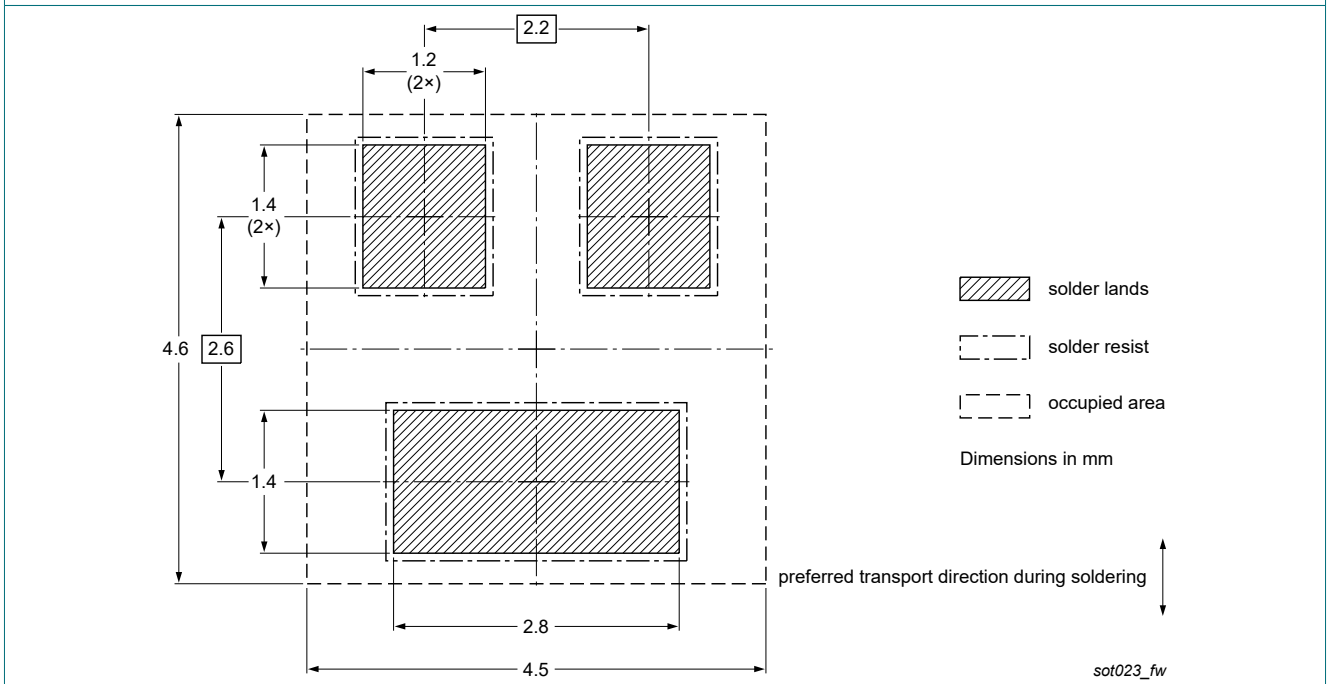


Fig. 7. Wave soldering footprint for SOT23

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BAT54A-Q v.1	20210615	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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