GLF73912

Ultra-Efficient, I_QSmart™ Battery Protection IC

Product Specification

DESCRIPTION

The GLF73912 is an I_QSmart™ ultra-efficient, full battery protection IC with an accurate over charge voltage, over discharge voltage, over charge current, and short circuit protection for lithium-Ion/Polymer battery safety.

The over charge and discharge voltage protections keep a rechargeable battery working within the desired safe operating condition. When the battery is charged past the over voltage detection level, the GLF73912 charging switch opens in a preset delay time. As the battery voltage decreases below the over discharge detection voltage level, the GLF73912 discharging switch is turned off immediately to cut off the battery power rail, consuming an ultra-low leakage current (I_{SD}) to save the battery. In addition, when the load current reaches the I_{SC} short circuit protection level, the GLF73912 is turned off and will maintain the off state to avoid any serious damage to system. The short circuit delay time avoids any false trigger which might open the switch.

When a charged battery cell is connected the GLF73912 remains in the off state and consumes an ultra-low leakage current (I_{SD}) until the V_{ON} voltage is applied to VOUT pin. Note that the GLF73912 is activated only by a V_{ON} voltage from a charger output.

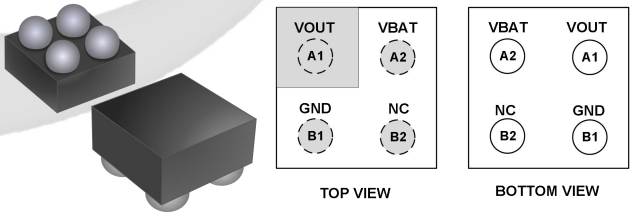
FEATURES

- Over Charge Detection, V_{OC} : 4.35 V_{BAT}
- Over Discharge Detection, V_{OD} : 2.80 V_{BAT}
- I_{OC}, Over Charge Current Detection
- Load Short Circuit Protection with Delay Time to avoid a false trigger
- Activated by Applying VON to the VOUT Pin from Charger
- 1.5 A Continuous Charging Current Capability from VOUT to VBAT Pin
- Low R_{ON} : 57 mΩ Typ. at 3.6 V_{BAT}
- Quiescent Current, I_Q = 900 nA Typ. at 3.6 V_{BAT}
- Shutdown Current
  - I_{SD} = 7 nA Typ. at V_{BAT} < V_{OD}
- Latch-off at Over Discharge Detection and Short Circuit Protection. Apply V_{ON} to VOUT pin to turn on
- 0 V Battery Minimum Voltage for Charging
- Reverse Polarity Connection Protection
- Patent Pending Circuit Architecture
- HBM : 8 kV, CDM : 2 kV
- 0.97 mm x 0.97 mm x 0.55 mm Chip Scale Package 4 Bumps, 0.5 mm Pitch

APPLICATIONS

- BLE Wireless Earphone
- Wearables and Smart IoT Devices

PACKAGE

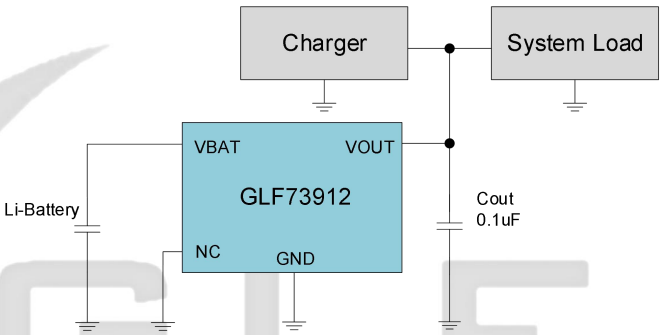


0.97 mm x 0.97 mm x 0.55 mm WLCSP

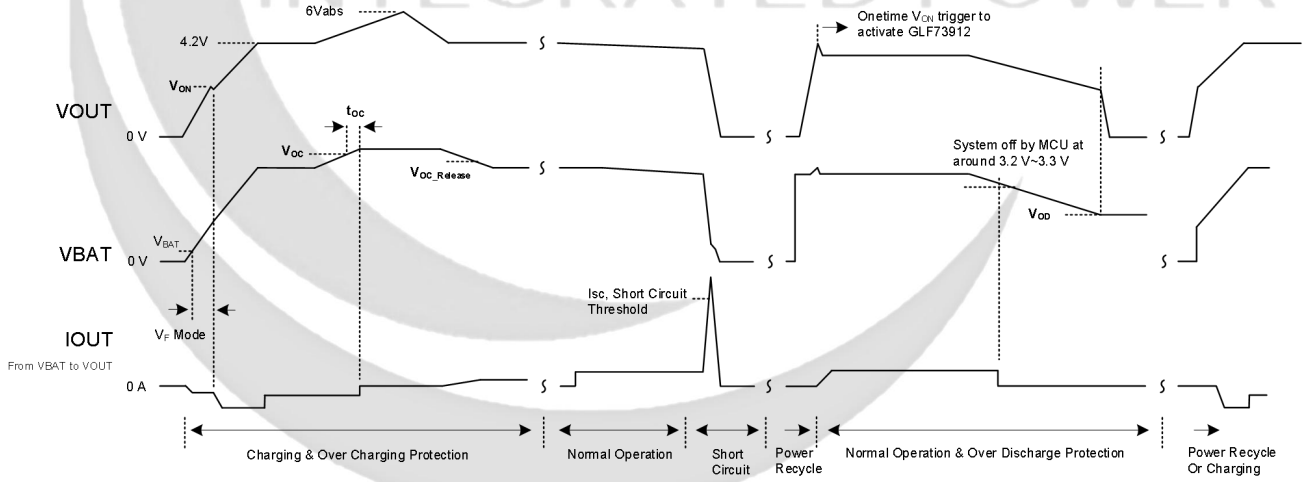
DEVICE INFORMATION

Part Number	Top Mark	R_{ON} (Typ.) V_{BAT}=3.6 V	Over Charge Detection, V_{OC}	Over Discharge Detection, V_{OD}	Over Charge Current, I_{OC}	Short Circuit Current, I_{SC}	V_{BAT}=0 V Charging
GLF73912	NC	57 mΩ	4.35 V	2.80 V	300 mA	0.5 A	Available

APPLICATION DIAGRAM



OPERATION DIAGRAM



FUNCTIONAL BLOCK DIAGRAM

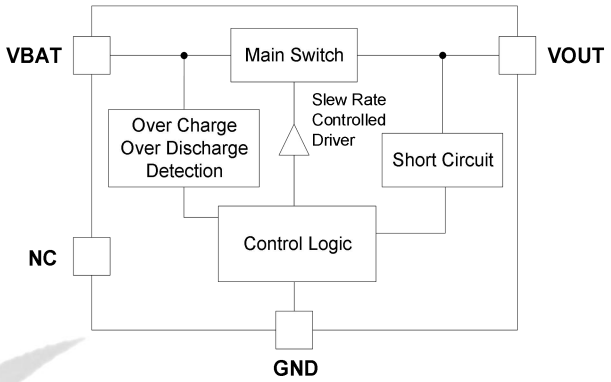


Figure 1. Functional Block Diagram

PIN CONFIGURATION

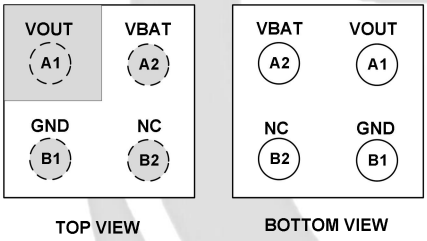


Figure 2. 0.97mm x 0.97mm x 0.55mm WLCSP

PIN DEFINITION

Pin #	Name	Description
A1	VOUT	VOUT pin is connected to the charger output and system load. If the switch is in the off state, applying the appropriate voltage (V_{ON}) to V_{OUT} turns the switch back on.
A2	VBAT	VBAT pin is connected to the positive terminal of a battery pack to monitor the battery voltage. When the V_{BAT} voltage reaches the V_{OD}, the main switch is turned off and maintains the off state to save the battery from discharging.
B1	GND	Ground
B2	NC	No Connection. Tie this pin to GND directly.

ABSOLUTE MAXIMUM RATINGS

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions; extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter		Min.	Max.	Unit
V_{BAT}, V_{OUT}	Each Pin Voltage Range to GND		- 0.3	6	V
I_{BAT}	Switch Continuous Current between VBAT and VOUT			1.5	A
P_D	Power Dissipation at T_A = 25°C			1.2	W
T_{STG}	Storage Junction Temperature		- 65	150	°C
T_A	Operating Temperature Range		- 40	85	°C
θ_{JA}	Thermal Resistance, Junction to Ambient			85	°C/W
ESD	Electrostatic Discharge Capability	Human Body Model, JESD22-A114	8		kV
		Charged Device Model, JESD22-C101	2		

ELECTRICAL CHARACTERISTICS

Values are at T_A = 25 °C unless otherwise noted.

Symbol	Parameter	Conditions		Min.	Typ.	Max.	Units
V_{OC}	Over Charge Voltage Detection	V_{BAT} increases until switch turns off		4.25	4.35	4.45	V
		V_{BAT} increases until switch turns off, Ta = 55 °C (1)			4.39		
V_{OC_HYS}	Over Charge Voltage Protection Release Hysteresis	VBAT decreases and switch turns on			80		mV
t_{OCV}	Over Charge Voltage Protection Delay	V_{BAT} > V_{OC}, Blanking time until switch turns off			530		ms
V_{OD}	Over Discharge Voltage Detection	V_{BAT} decreases until switch turns off		2.72	2.80	2.88	V
		V_{BAT} decreases until switch turns off, Ta = 55 °C (1)			2.887		
V_{ON}	ON Voltage applied to VOUT Pin to turn on switch	V_{OUT} to turn on switch, V_{BAT} ≥ 3.1 V (1)			3.6		V
		V_{OUT} to turn on switch, V_{BAT} ≥ 3.1 V, Ta = 55 °C (1)			3.6		
V_F	Forward Voltage	From VOUT to VBAT pin, V_{BAT} < 3.0 V			0.4		V
I_{OC}	Over Charge Current Detection	V_{BAT} = 3.6 V		0.21	0.30	0.39	A
t_{OCC}	Over Charge Current Protection Delay				40		ms
I_{SC}	Short Circuit Shutdown	V_{BAT} = 3.6 V			0.5		A
t_{SC}	Short Circuit Protection Delay Time	V_{BAT} = 3.6 V (1)			0.5		ms
I_Q	Quiescent Current with Switch On	V_{BAT} = 3.6 V, I_{OUT} = 0 mA, Switch = ON			0.9		μA
		V_{BAT} = 4.2 V, I_{OUT} = 0 mA, Switch = ON			1.0		
		V_{BAT} = 4.2 V, I_{OUT} = 0 mA, Switch = ON, Ta = 55°C (1)			1.1		
I_{SD}	Shutdown Current from VBAT	V_{BAT} = 3.6 V, V_{OUT} = 0 V,			8		nA
		V_{BAT} = 2.5 V, V_{OUT} = 0 V			7		
		V_{BAT} = 2.5 V, V_{OUT} = 0 V, Ta = 55 °C (1)			15		
R_{ON}	On-Resistance	V_{BAT} = 4.2 V, I_{OUT} = 200 mA	Ta = 25 °C		53		mΩ
			Ta = 55 °C (1)		57		
		V_{BAT} = 3.6 V, I_{OUT} = 200 mA	Ta = 25 °C		57		
			Ta = 55 °C (1)		62		
		V_{BAT} = 3.3 V, I_{OUT} = 200 mA	Ta =25 °C		60		
R_{DSC}	Output Discharge Resistance (1)	At t_{OFF}, I_{FORCE} = 10mA			85		Ω
t_{OFF}	Turn-Off Time (1)	C_{OUT} = 0.1 μF, R_{OUT} = 150 Ω, V_{OUT} = V_{OD} to 0V			33		μs

Notes: 1. By design; characterized, not production tested.

TYPICAL PERFORMANCE CHARACTERISTICS

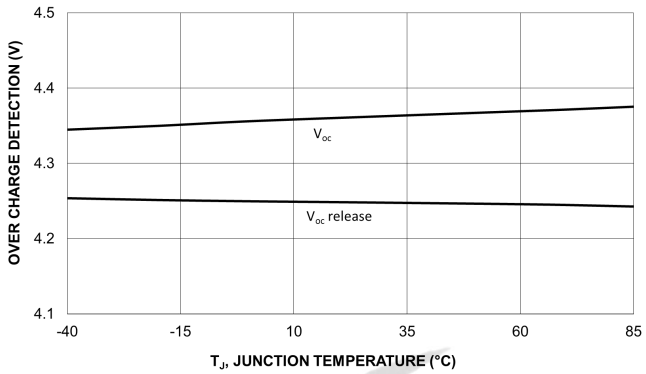


Figure 3. Over Charge Voltage Detection vs. Temperature

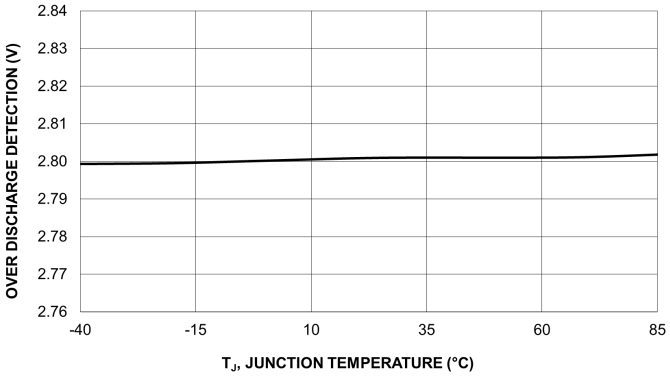


Figure 4. Over Discharge Voltage Detection vs. Temperature

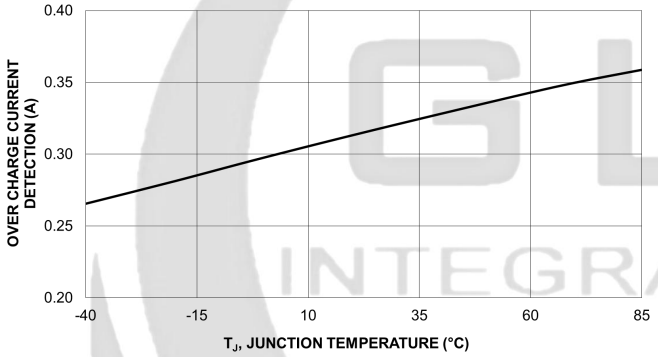


Figure 5. Over Charge Current Detection vs. Temperature

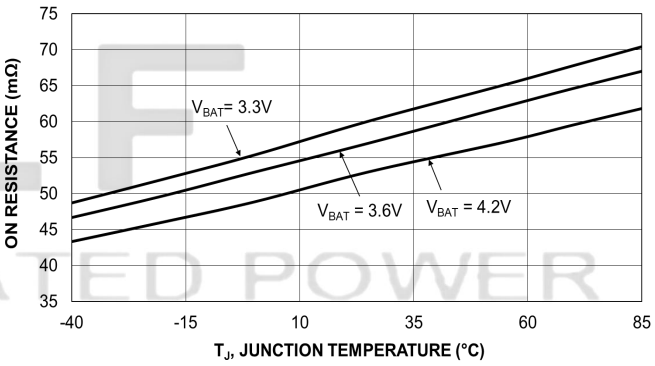


Figure 6. On-Resistance vs. Temperature

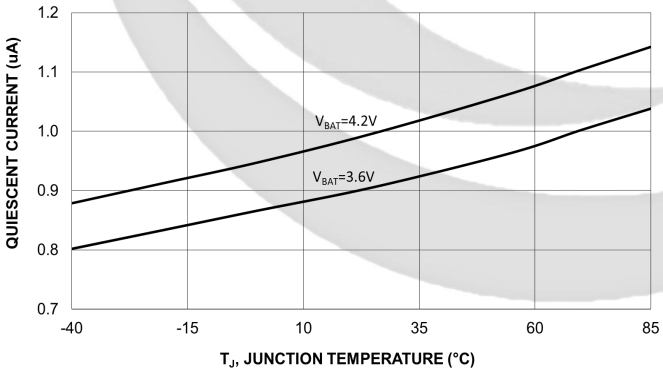


Figure 7. Quiescent Current vs. Temperature

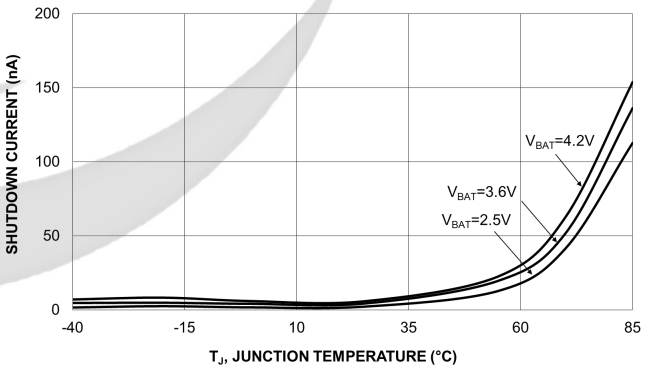


Figure 8. Shutdown Current vs. Temperature

APPLICATION INFORMATION

The GLF73912 is an IQSmart™ ultra-efficient battery protection IC with the accurate over charge voltage, over charge current, and short circuit protection for lithium-Ion/Polymer battery safety. The best in class efficiency makes it ideal for the design of hearing devices, wearable devices, and tiny IoT devices.

Charging Activation and 0 V Battery Charging

The GLF73912 is activated to turn on the main charging switch only by applying the on voltage (V_{ON}) to the VOUT pin, when a charger IC is enabled. The minimum battery voltage to charge is 0 V. With a deeply discharged battery, the GLF73912 does not turn on both the charge and discharge path and the pre-charge current flows through an internal diode (V_F) until the battery voltage reaches 3.1 V. As the battery voltage increases beyond 3.1 V, the charge and discharge path switches will be fully activated to reduce the voltage drop and save power dissipation during both constant-current and constant-voltage charging modes.

Over Charging and Discharging Voltage Protection

When the voltage of a battery increases to the over-charge voltage detection level (V_{OC}), the charge path is turned off to stop charging the battery after a preset over-charge detection delay time (t_{OC}) in order to avoid a false trigger. The charging path is turned on again when the VOUT voltage falls by 80 mV. The charging path is not turned off if the battery voltage returns to a voltage less than the detection level within the delay time. The charging path turns on again as the battery voltage decreases below the over-charge release voltage level ($V_{OC} – V_{OC\_HYS}$). When the voltage of a battery decreases to the over-discharge detection voltage level, the GLF73912 discharging path is turned off consuming an ultra-low leakage current to save the battery. The GLF73912 remains in the off state until a higher voltage is applied to the VOUT pin.

Over Charging Current and Short Circuit Protection

If an overcharging current is detected during the constant current charging mode, the GLF73912 will close the charging path in a preset detection delay time. When the discharge current from the battery exceeds the short circuit detection level (I_{SC}), the discharging path of the GLF73912 will be turned off after a preset delay time (t_{SC}) in order to avoid a false detection. After the short circuit protection event, the GLF73905, GLF73912 maintains in the off state and needs a power recycle of a system to apply VON to VOUT pin in order to be reactivated.

Reverse Polarity Connection

At the reverse polarity connection of either a battery or a charger output, the GLF73912 operates safely by adding an external resistor to the GND pin. A 100 Ω or 1 KΩ resistor is recommended.

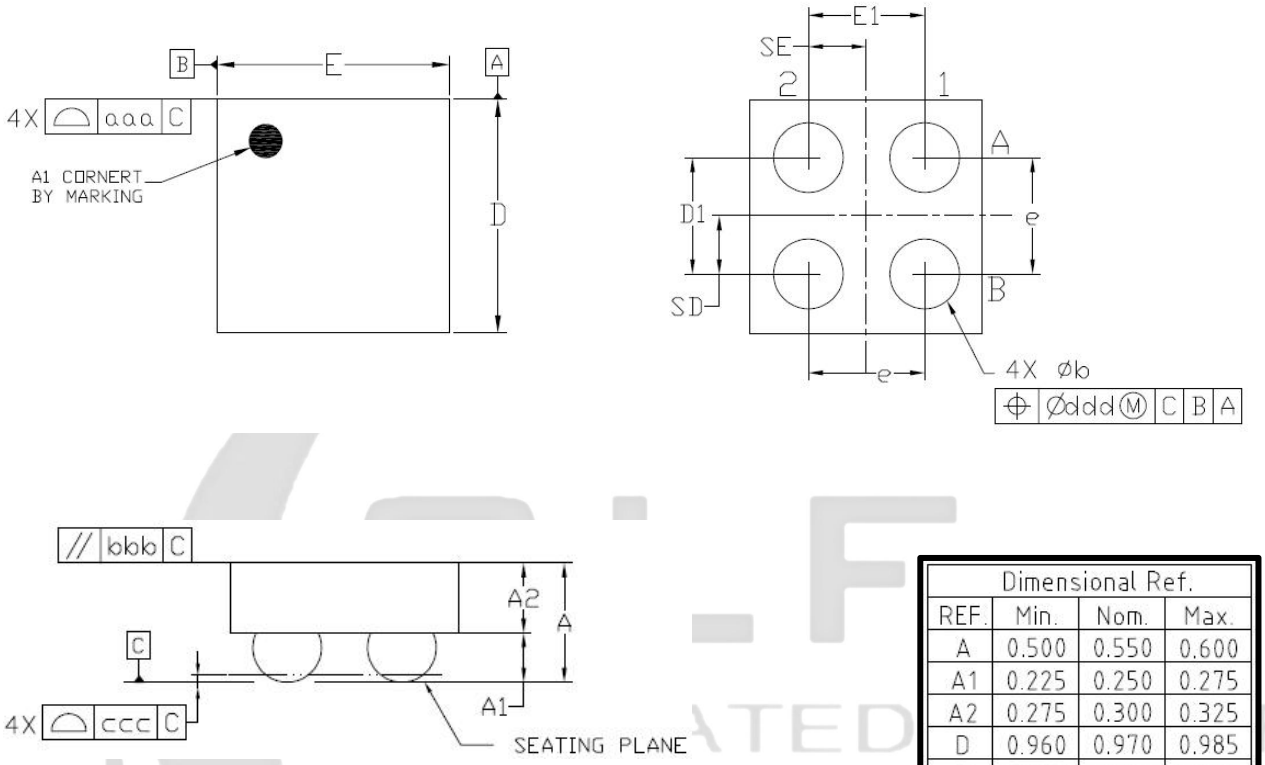
Input and Output Capacitors

Input and output capacitors are not required for GLF73912 operation. However, a 0.1μF capacitor is recommended to be placed close to the VBAT and VOUT pins in order to mitigate any unexpected electrical noise or the transient voltage peak caused by a hot-plugging voltage source.

Board Layout

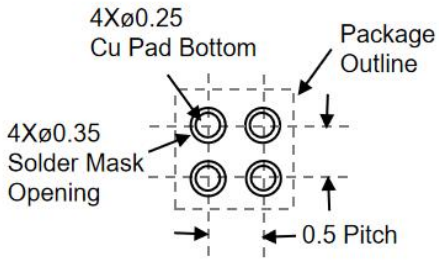
All traces should be as short as possible to minimize parasitic inductance effects. Wide traces for VBAT, VOUT, and GND will help reduce voltage drops, and parasitic effects during dynamic operation as well as improve the thermal performance at high load currents.

PACKAGE OUTLINE



Dimensional Ref.			
REF.	Min.	Nom.	Max.
A	0.500	0.550	0.600
A1	0.225	0.250	0.275
A2	0.275	0.300	0.325
D	0.960	0.970	0.985
E	0.960	0.970	0.985
D1	0.450	0.500	0.550
E1	0.450	0.500	0.550
b	0.260	0.310	0.360
e	0.500 BSC		
SD	0.250 BSC		
SE	0.250 BSC		
Tol. of Form&Position			
aaa	0.10		
bbb	0.10		
ccc	0.05		
ddd	0.05		

Recommended Footprint

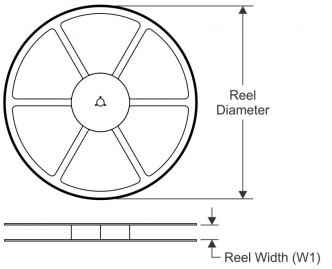


Notes

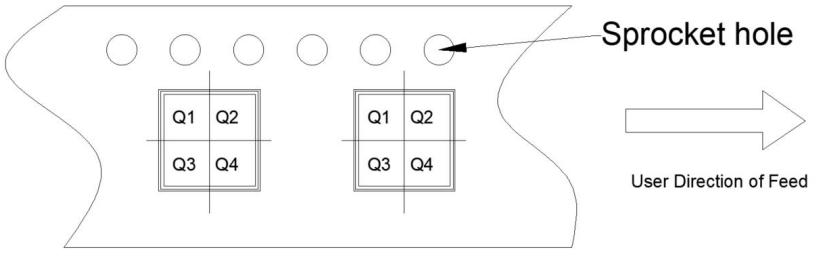
1. All DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
2. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M-1994.

TAPE AND REEL INFORMATION

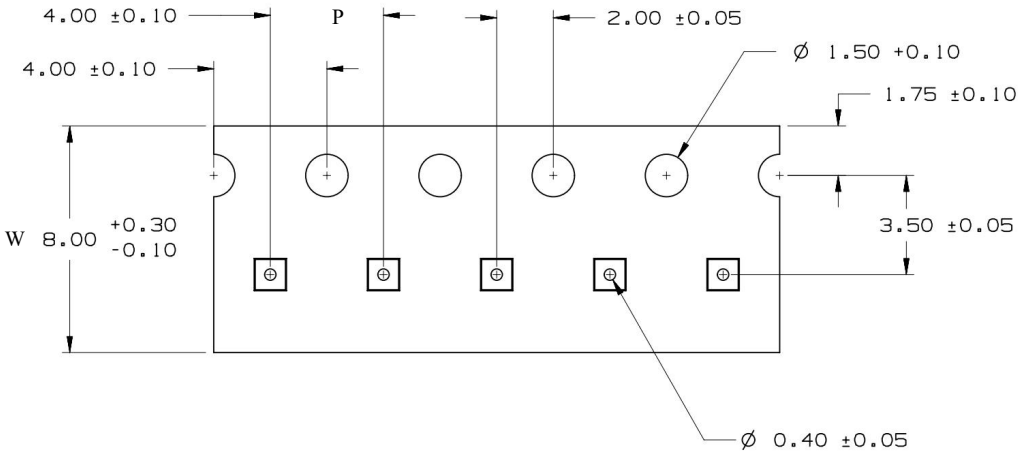
REEL DIMENSIONS

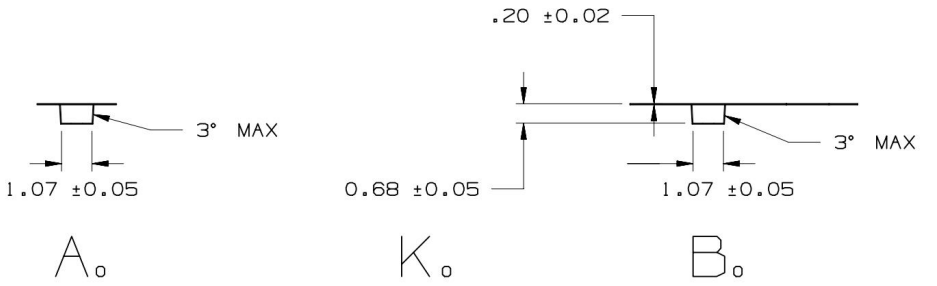


QUADRANT ASSIGNMENTS PIN 1 ORIENTATION TAPE



TAPE DIMENSIONS





Device	Package	Pins	SPQ	Reel Diameter (mm)	Reel Width W1	A0	B0	K0	P	W	Pin1
GLF73912	WLCSP	4	3000	180	9	1.07	1.07	0.68	4	8	Q1

Remark:

A0: Dimension designed to accommodate the component width

B0: Dimension designed to accommodate the component length

C0: Dimension designed to accommodate the component thickness

W: Overall width of the carrier tape

P: Pitch between successive cavity centers

SPECIFICATION DEFINITIONS

Document Type	Meaning	Product Status
Target Specification	This is a target specification intended to support exploration and discussion of critical needs for a proposed or target device. Spec limits including typical, minimum, and maximum values are desired, or target, limits. GLF reserves the right to change limits at any time without warning or notification. A target specification in no way guarantees future production of the device in question.	Design / Development
Preliminary Specification	This is a draft version of a product specification. The specification is still under internal review and subject to change. GLF reserves the right to change the specification at any time without warning or notification. A preliminary specification in no way guarantees future production of the device in question.	Qualification
Product Specification	This document represents the anticipated production performance characteristics of the device.	Production

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