



**THE DATASHEET OF  
74AUP1G38GX,125**



# 74AUP1G38

Low-power 2-input NAND gate (open drain)

Rev. 10 — 18 July 2023

Product data sheet

## 1. General description

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The 74AUP1G38 is a single 2-input NAND gate with open-drain output. Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times. This device ensures very low static and dynamic power consumption across the entire  $V_{CC}$  range from 0.8 V to 3.6 V. This device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

## 2. Features and benefits

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- Wide supply voltage range from 0.8 V to 3.6 V
- CMOS low power dissipation
- High noise immunity
- Complies with JEDEC standards:
  - JESD8-12 (0.8 V to 1.3 V)
  - JESD8-11 (0.9 V to 1.65 V)
  - JESD8-7 (1.65 V to 1.95 V)
  - JESD8-5 (2.3 V to 2.7 V)
  - JESD8C (2.7 V to 3.6 V)
- Low static power consumption;  $I_{CC} = 0.9 \mu\text{A}$  (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Overvoltage tolerant inputs to 3.6 V
- Low noise overshoot and undershoot < 10 % of  $V_{CC}$
- $I_{OFF}$  circuitry provides partial Power-down mode operation
- ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 class 3A exceeds 5000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

### 3. Ordering information

Table 1. Ordering information

Type number	Package			Version
	Temperature range	Name	Description	
<a href="#">74AUP1G38GW</a>	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	<a href="#">SOT353-1</a>
<a href="#">74AUP1G38GM</a>	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	<a href="#">SOT886</a>
<a href="#">74AUP1G38GN</a>	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 × 1.0 × 0.35 mm	<a href="#">SOT1115</a>
<a href="#">74AUP1G38GS</a>	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm	<a href="#">SOT1202</a>
<a href="#">74AUP1G38GX</a>	-40 °C to +125 °C	X2SON5	plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 × 0.8 × 0.32 mm	<a href="#">SOT1226-3</a>

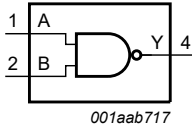
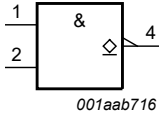
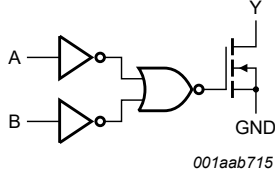
### 4. Marking

Table 2. Marking

Type number	Marking code[1]
74AUP1G38GW	aB
74AUP1G38GM	aB
74AUP1G38GN	aB
74AUP1G38GS	aB
74AUP1G38GX	aB

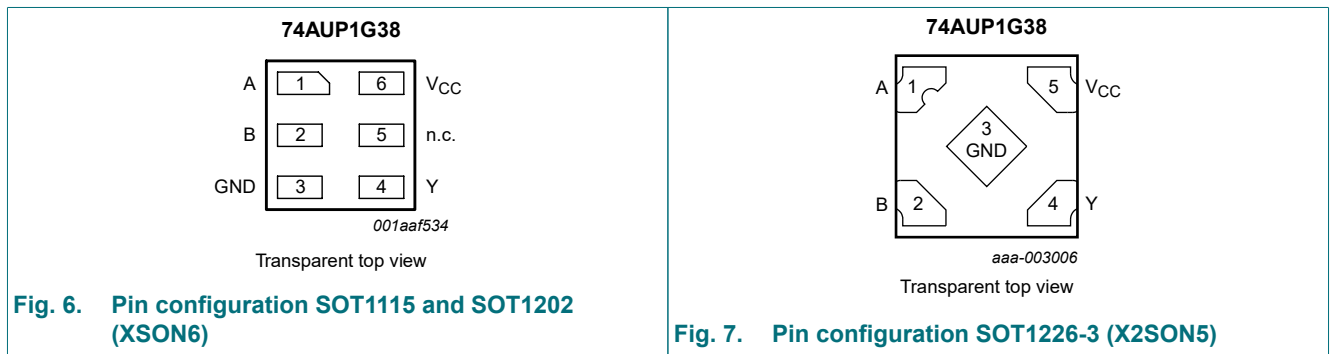
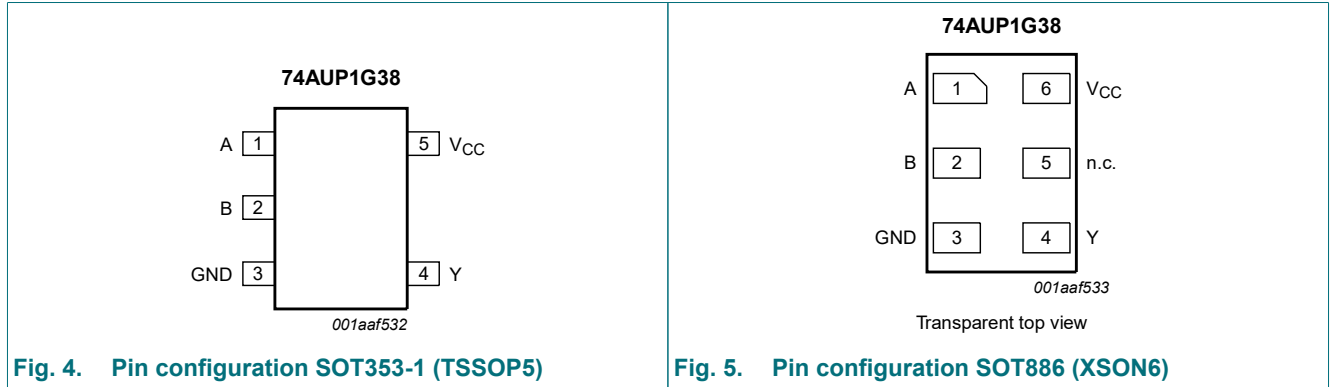
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

### 5. Functional diagram

 <p>001aab717</p> <p><b>Fig. 1. Logic symbol</b></p>	 <p>001aab716</p> <p><b>Fig. 2. IEC logic symbol</b></p>	 <p>001aab715</p> <p><b>Fig. 3. Logic diagram</b></p>
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## 6. Pinning information

### 6.1. Pinning



### 6.2. Pin description

Table 3. Pin description

Symbol	Pin		Description
	TSSOP5 and X2SON5	XSON6	
A	1	1	data input
B	2	2	data input
GND	3	3	ground (0 V)
Y	4	4	data output
n.c.	-	5	not connected
V <sub>CC</sub>	5	6	supply voltage

## 7. Functional description

**Table 4. Function table**

*H = HIGH voltage level; L = LOW voltage level; Z = high-impedance OFF state.*

Input		Output
A	B	Y
L	L	Z
L	H	Z
H	L	Z
H	H	L

## 8. Limiting values

**Table 5. Limiting values**

*In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).*

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CC}$	supply voltage		-0.5	+4.6	V
$I_{IK}$	input clamping current	$V_I < 0\text{ V}$	-50	-	mA
$V_I$	input voltage		-0.5	+4.6	V
$I_{OK}$	output clamping current	$V_O < 0\text{ V}$	-50	-	mA
$V_O$	output voltage	Active mode and Power-down mode	-0.5	+4.6	V
$I_O$	output current	$V_O = 0\text{ V to }V_{CC}$	-	+20	mA
$I_{CC}$	supply current		-	+50	mA
$I_{GND}$	ground current		-50	-	mA
$T_{stg}$	storage temperature		-65	+150	°C
$P_{tot}$	total power dissipation	$T_{amb} = -40\text{ °C to }+125\text{ °C}$	-	250	mW

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT353-1 (TSSOP5) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 74 °C.

For SOT886 (XSON6) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 74 °C.

For SOT1115 (XSON6) package:  $P_{tot}$  derates linearly with 3.2 mW/K above 71 °C.

For SOT1202 (XSON6) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 74 °C.

For SOT1226-3 (X2SON5) package:  $P_{tot}$  derates linearly with 3.0 mW/K above 67 °C.

## 9. Recommended operating conditions

**Table 6. Recommended operating conditions**

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CC}$	supply voltage		0.8	3.6	V
$V_I$	input voltage		0	3.6	V
$V_O$	output voltage	Active mode and Power-down mode	0	3.6	V
$T_{amb}$	ambient temperature		-40	+125	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{CC} = 0.8\text{ V to }3.6\text{ V}$	0	200	ns/V

## 10. Static characteristics

**Table 7. Static characteristics**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>T<sub>amb</sub> = 25 °C</b>						
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.70 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.65 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.30 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.35 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OL</sub>	LOW-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>				
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.31	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.31	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.31	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.44	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.31	V
I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.44	V		
I <sub>I</sub>	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.1	μA
I <sub>OZ</sub>	OFF-state output current	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> (and at least one input LOW); V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.1	μA
I <sub>OFF</sub>	power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V	-	-	±0.2	μA
ΔI <sub>OFF</sub>	additional power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V to 0.2 V	-	-	±0.2	μA
I <sub>CC</sub>	supply current	V <sub>I</sub> = GND or V <sub>CC</sub> ; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.5	μA
ΔI <sub>CC</sub>	additional supply current	V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 3.3 V	-	-	40	μA
C <sub>I</sub>	input capacitance	V <sub>CC</sub> = 0 V to 3.6 V; V <sub>I</sub> = GND or V <sub>CC</sub>	-	0.8	-	pF
C <sub>O</sub>	output capacitance	output enabled; V <sub>O</sub> = GND; V <sub>CC</sub> = 0 V	-	1.7	-	pF
		output disabled; V <sub>O</sub> = GND; V <sub>CC</sub> = 0 V	-	1.1	-	pF

## Low-power 2-input NAND gate (open drain)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>T<sub>amb</sub> = -40 °C to +85 °C</b>						
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.70 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.65 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.30 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.35 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OL</sub>	LOW-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>				
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.37	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.35	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.33	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.33	V
I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.45	V		
I <sub>I</sub>	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.5	μA
I <sub>OZ</sub>	OFF-state output current	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> (and at least one input LOW); V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.5	μA
I <sub>OFF</sub>	power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V	-	-	±0.5	μA
ΔI <sub>OFF</sub>	additional power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V to 0.2 V	-	-	±0.6	μA
I <sub>CC</sub>	supply current	V <sub>I</sub> = GND or V <sub>CC</sub> ; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.9	μA
ΔI <sub>CC</sub>	additional supply current	V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 3.3 V	-	-	50	μA

## Low-power 2-input NAND gate (open drain)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>T<sub>amb</sub> = -40 °C to +125 °C</b>						
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.75 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.70 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.25 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.30 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OL</sub>	LOW-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>				
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.11	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.33 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.41	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.39	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.36	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.50	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.36	V
I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.50	V		
I <sub>I</sub>	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.75	μA
I <sub>OZ</sub>	OFF-state output current	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> (and at least one input LOW); V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.75	μA
I <sub>OFF</sub>	power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V	-	-	±0.75	μA
ΔI <sub>OFF</sub>	additional power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V to 0.2 V	-	-	±0.75	μA
I <sub>CC</sub>	supply current	V <sub>I</sub> = GND or V <sub>CC</sub> ; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	1.4	μA
ΔI <sub>CC</sub>	additional supply current	V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 3.3 V	-	-	75	μA

## 11. Dynamic characteristics

**Table 8. Dynamic characteristics**

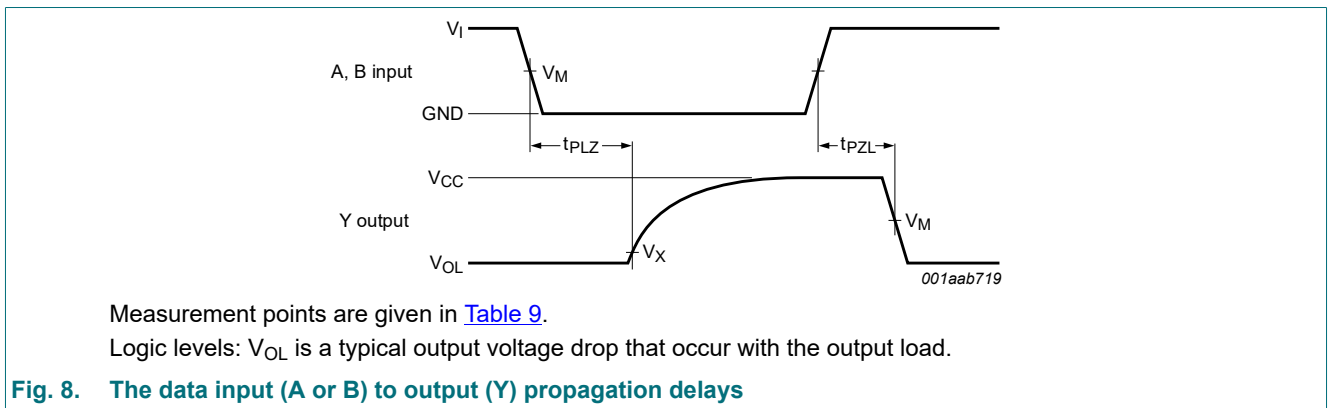
Voltages are referenced to GND (ground = 0 V; for test circuit see Fig. 9).

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
<b>C<sub>L</sub> = 5 pF</b>										
t <sub>pd</sub>	propagation delay	A or B to Y; see Fig. 8 [2]								
		V <sub>CC</sub> = 0.8 V	-	13.5	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	1.9	4.6	10.4	1.8	11.4	1.8	12.6	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.5	3.3	6.5	1.4	7.4	1.4	8.2	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.2	2.9	5.1	1.1	5.9	1.1	6.5	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.0	2.2	3.8	0.9	4.5	0.9	4.9	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	0.9	2.3	4.0	0.8	4.5	0.8	4.9	ns
<b>C<sub>L</sub> = 10 pF</b>										
t <sub>pd</sub>	propagation delay	A or B to Y; see Fig. 8 [2]								
		V <sub>CC</sub> = 0.8 V	-	16.3	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.3	5.6	12.3	2.1	13.7	2.1	15.1	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.8	4.1	7.6	1.7	8.8	1.7	9.7	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.6	3.8	6.1	1.4	7.1	1.4	7.8	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.4	2.9	4.6	1.2	5.4	1.2	5.9	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.3	3.2	5.7	1.1	6.4	1.1	7.0	ns
<b>C<sub>L</sub> = 15 pF</b>										
t <sub>pd</sub>	propagation delay	A or B to Y; see Fig. 8 [2]								
		V <sub>CC</sub> = 0.8 V	-	19.0	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.6	6.6	14.2	2.4	15.8	2.4	17.4	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.1	4.8	8.7	1.9	10.1	1.9	11.1	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.9	4.6	7.6	1.7	8.5	1.7	9.3	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	3.6	5.6	1.5	6.3	1.5	6.9	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.6	4.1	7.5	1.4	8.3	1.4	9.1	ns
<b>C<sub>L</sub> = 30 pF</b>										
t <sub>pd</sub>	propagation delay	A or B to Y; see Fig. 8 [2]								
		V <sub>CC</sub> = 0.8 V	-	27.0	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.6	9.5	19.5	3.2	21.8	3.2	24.0	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.9	7.0	11.5	2.6	13.6	2.6	15.0	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.6	7.0	12.1	2.3	13.3	2.3	14.6	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.4	5.4	8.9	2.1	9.9	2.1	10.9	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.3	6.5	12.7	2.1	13.9	2.1	15.3	ns

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
<b>C<sub>L</sub> = 5 pF, 10 pF, 15 pF and 30 pF</b>										
C <sub>PD</sub>	power dissipation capacitance	f <sub>i</sub> = 1 MHz; V <sub>I</sub> = GND to V <sub>CC</sub> [3]								
		V <sub>CC</sub> = 0.8 V	-	0.6	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	0.7	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	0.8	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	0.9	-	-	-	-	-	pF
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	1.1	-	-	-	-	-	pF
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	1.4	-	-	-	-	pF	

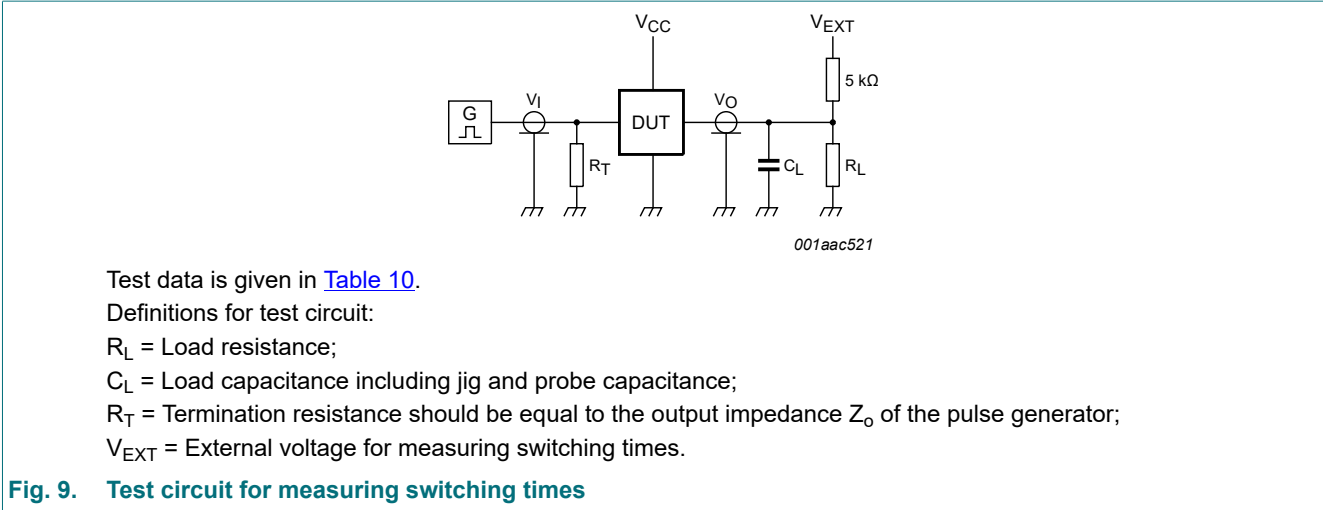
- [1] All typical values are measured at nominal V<sub>CC</sub>.
- [2] t<sub>pd</sub> is the same as t<sub>PZL</sub> and t<sub>PLZ</sub>.
- [3] C<sub>PD</sub> is used to determine the dynamic power dissipation (P<sub>D</sub> in μW).  
 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N$  where:  
 f<sub>i</sub> = input frequency in MHz;  
 V<sub>CC</sub> = supply voltage in V;  
 N = number of inputs switching.

### 11.1. Waveforms and test circuit



**Table 9. Measurement points**

Supply voltage	Input			Output	
V <sub>CC</sub>	V <sub>M</sub>	V <sub>I</sub>	t <sub>r</sub> = t <sub>f</sub>	V <sub>M</sub>	V <sub>X</sub>
0.8 V to 1.6 V	0.5 × V <sub>CC</sub>	V <sub>CC</sub>	≤ 3.0 ns	0.5 × V <sub>CC</sub>	V <sub>OL</sub> + 0.1 V
1.65 V to 2.7 V	0.5 × V <sub>CC</sub>	V <sub>CC</sub>	≤ 3.0 ns	0.5 × V <sub>CC</sub>	V <sub>OL</sub> + 0.15 V
3.0 V to 3.6 V	0.5 × V <sub>CC</sub>	V <sub>CC</sub>	≤ 3.0 ns	0.5 × V <sub>CC</sub>	V <sub>OL</sub> + 0.3 V



**Fig. 9. Test circuit for measuring switching times**

**Table 10. Test data**

Supply voltage		Load		$V_{EXT}$		
$V_{CC}$	$C_L$	$R_L$ [1]	$t_{PLH}, t_{PHL}$	$t_{PZH}, t_{PHZ}$	$t_{PZL}, t_{PLZ}$	
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	$2 \times V_{CC}$	

[1] For measuring enable and disable times  $R_L = 5 \text{ k}\Omega$ .  
 For measuring propagation delays, setup and hold times and pulse width  $R_L = 1 \text{ M}\Omega$ .

## 12. Package outline

TSSOP5: plastic thin shrink small outline package; 5 leads; body width 1.25 mm

SOT353-1



Fig. 10. Package outline SOT353-1 (TSSOP5)

XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm

SOT886



Fig. 11. Package outline SOT886 (XSON6)

XSON6: extremely thin small outline package; no leads;  
6 terminals; body 0.9 x 1.0 x 0.35 mm

SOT1115



Fig. 12. Package outline SOT1115 (XSON6)

XSON6: extremely thin small outline package; no leads;  
6 terminals; body 1.0 x 1.0 x 0.35 mm

SOT1202



Fig. 13. Package outline SOT1202 (XSON6)

X2SON5: plastic thermal enhanced extremely thin small outline package; no leads;  
5 terminals; body 0.8 x 0.8 x 0.32 mm

SOT1226-3



Fig. 14. Package outline SOT1226-3 (X2SON5)

## 13. Abbreviations

Table 11. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model

## 14. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP1G38 v.10	20230718	Product data sheet	-	74AUP1G38 v.9
Modifications:	<ul style="list-style-type: none"> <li><a href="#">Section 2</a>: ESD specification updated according to the latest JEDEC standard.</li> </ul>			
74AUP1G38 v.9	20220816	Product data sheet	-	74AUP1G38 v.8
Modifications:	<ul style="list-style-type: none"> <li>Package SOT1226 (X2SON5) changed to SOT1226-3 (X2SON5).</li> </ul>			
74AUP1G38 v.8	20220207	Product data sheet	-	74AUP1G38 v.7
Modifications:	<ul style="list-style-type: none"> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li><a href="#">Fig. 10</a>: Package outline drawing for SOT353-1 (TSSOP5) has changed.</li> <li>Type number 74AUP1G38GF (SOT891/ XSON6) removed.</li> <li><a href="#">Section 1</a> and <a href="#">Section 2</a> updated.</li> <li><a href="#">Table 5</a>: Derating values for <math>P_{tot}</math> total power dissipation updated.</li> </ul>			
74AUP1G38 v.7	20160404	Product data sheet	-	74AUP1G38 v.6
Modifications:	<ul style="list-style-type: none"> <li><a href="#">Fig. 7</a>: Typo corrected in pin naming (pins A and B swapped)</li> </ul>			
74AUP1G38 v.6	20120628	Product data sheet	-	74AUP1G38 v.5
Modifications:	<ul style="list-style-type: none"> <li>Added type number 74AUP1G38GX (SOT1226)</li> <li>Package outline drawing of SOT886 (<a href="#">Fig. 11</a>) modified.</li> </ul>			
74AUP1G38 v.5	20111129	Product data sheet	-	74AUP1G38 v.4
Modifications:	<ul style="list-style-type: none"> <li>Legal pages updated.</li> </ul>			
74AUP1G38 v.4	20101007	Product data sheet	-	74AUP1G38 v.3
74AUP1G38 v.3	20090622	Product data sheet	-	74AUP1G38 v.2
74AUP1G38 v.2	20070614	Product data sheet	-	74AUP1G38 v.1
74AUP1G38 v.1	20061020	Product data sheet	-	-

## 15. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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## Contents

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<b>1. General description</b> .....	<b>1</b>
<b>2. Features and benefits</b> .....	<b>1</b>
<b>3. Ordering information</b> .....	<b>2</b>
<b>4. Marking</b> .....	<b>2</b>
<b>5. Functional diagram</b> .....	<b>2</b>
<b>6. Pinning information</b> .....	<b>3</b>
6.1. Pinning.....	3
6.2. Pin description.....	3
<b>7. Functional description</b> .....	<b>4</b>
<b>8. Limiting values</b> .....	<b>4</b>
<b>9. Recommended operating conditions</b> .....	<b>4</b>
<b>10. Static characteristics</b> .....	<b>5</b>
<b>11. Dynamic characteristics</b> .....	<b>8</b>
11.1. Waveforms and test circuit.....	9
<b>12. Package outline</b> .....	<b>11</b>
<b>13. Abbreviations</b> .....	<b>16</b>
<b>14. Revision history</b> .....	<b>16</b>
<b>15. Legal information</b> .....	<b>17</b>

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