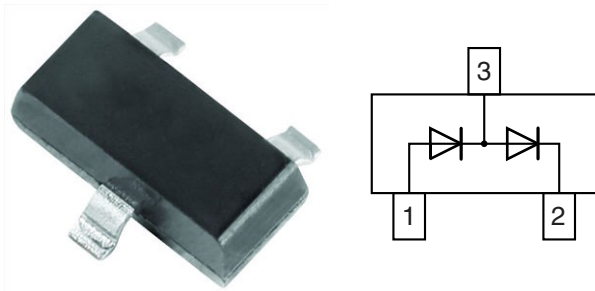




## Small Signal Switching Diode, Dual



### FEATURES

- Silicon epitaxial planar diode
- Fast switching dual diode, especially suited for automatic insertion
- AEC-Q101 qualified available (part number on request)
- Molding compound meets UL 94 V-0 flammability rating
- Moisture sensitivity level (MSL) 1
- Base P/N-G3 - green, commercial grade
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



### LINKS TO ADDITIONAL RESOURCES



### MECHANICAL DATA

**Case:** SOT-23

**Weight:** approx. 9.2 mg

**Packaging codes / options:**

18/10K per 13" reel (8 mm tape), 10K/box

08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE						
PART	ORDERING CODE	AEC-Q101 QUALIFIED	TYPE MARKING	CIRCUIT CONFIGURATION	TAPED UNITS PER REEL	MINIMUM ORDER QUANTITY
MMBD7000-G	MMBD7000-G3-08	no	M5G	Dual serial	3 000 (8 mm tape on 7" reel)	15 000
	MMBD7000-G3-18	no			10 000 (8 mm tape on 13" reel)	10 000

ABSOLUTE MAXIMUM RATINGS ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		$V_R$	100	V
Forward current (continuous) <sup>(1)</sup>		$I_F$	350	mA
Non-repetitive peak forward current <sup>(1)</sup>	$t = 1\text{ s}$	$I_{FSM}$	500	mA
Power dissipation	on FR-4 board with recommended soldering footprint	$P_{tot}$	270	mW
	Infinite heatsink		390	mW

**Note**

<sup>(1)</sup> Infinite heatsink

THERMAL CHARACTERISTICS ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Thermal resistance junction to ambient air	according to JEDEC <sup>®</sup> 51-3 on FR-4 board with recommended soldering footprint	$R_{thJA}$	460	K/W
Thermal resistance junction to lead	Infinite heatsink	$R_{thJL}$	320	K/W
Maximum junction temperature		$T_j$	150	$^{\circ}\text{C}$
Storage temperature range		$T_{stg}$	-65 to +150	$^{\circ}\text{C}$
Operating temperature range		$T_{op}$	-55 to +150	$^{\circ}\text{C}$



ELECTRICAL CHARACTERISTICS ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	MIN.	MAX.	UNIT
Reverse breakdown voltage	$I_R = 100\text{ }\mu\text{A}$	$V_{(BR)}$	100		V
Leakage current	$V_R = 50\text{ V}$	$I_R$		400	nA
	$V_R = 100\text{ V}$	$I_R$		3	$\mu\text{A}$
	$V_R = 50\text{ V}, T_j = 125\text{ }^{\circ}\text{C}$	$I_R$		100	$\mu\text{A}$
Forward voltage	$I_F = 1\text{ mA}$	$V_F$	0.55	0.70	V
	$I_F = 10\text{ mA}$	$V_F$	0.67	0.82	V
	$I_F = 100\text{ mA}$	$V_F$	0.75	1.10	V
Diode capacitance	$V_R = 0, f = 1\text{ MHz}$	$C_D$		1.5	pF
Reverse recovery time	$I_F = I_R = 10\text{ mA}, i_R = 1\text{ mA}, R_L = 100\text{ }\Omega$	$t_{rr}$		4	ns

**TYPICAL CHARACTERISTICS** ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)

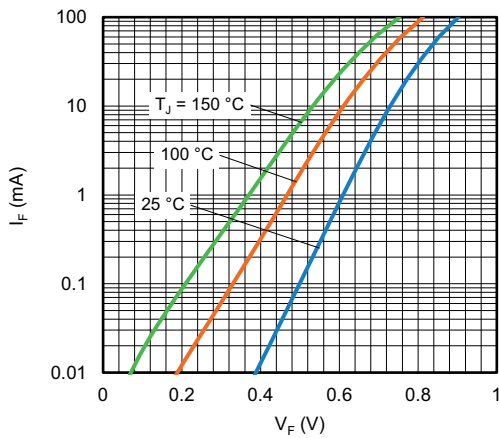


Fig. 1 - Forward Current vs. Forward Voltage

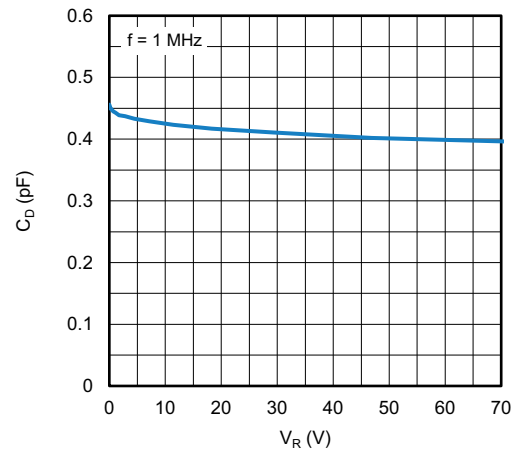


Fig. 3 - Typical Capacitance vs. Reverse Voltage

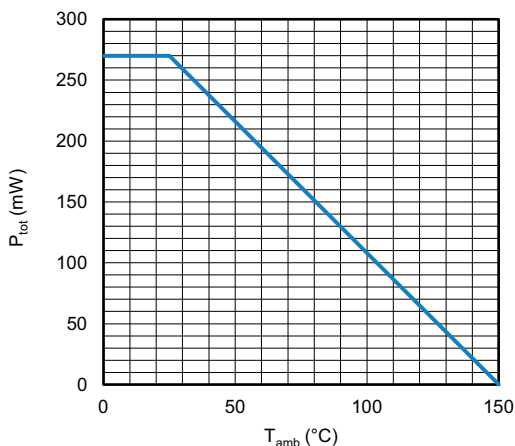


Fig. 2 - Admissible Power Dissipation vs. Ambient Temperature

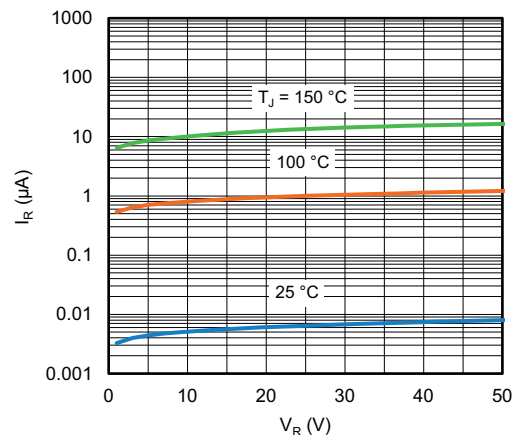
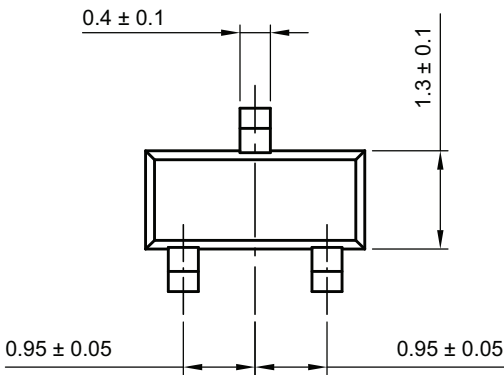
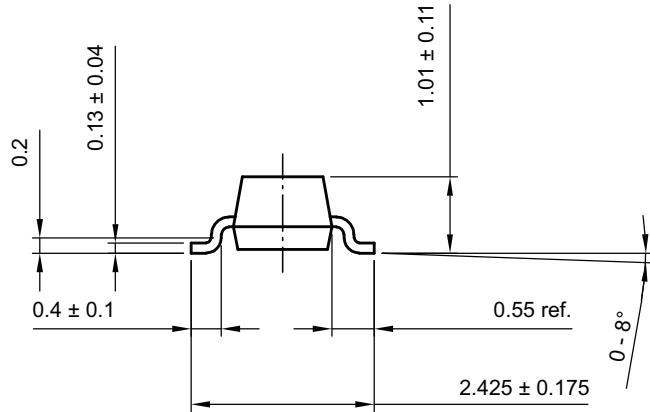
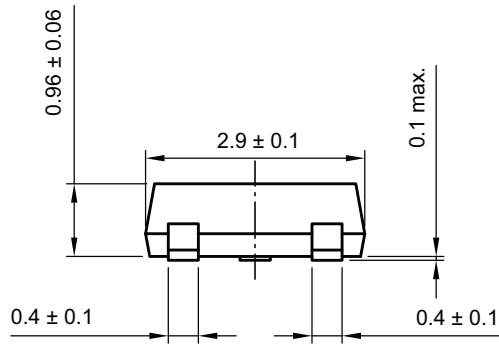


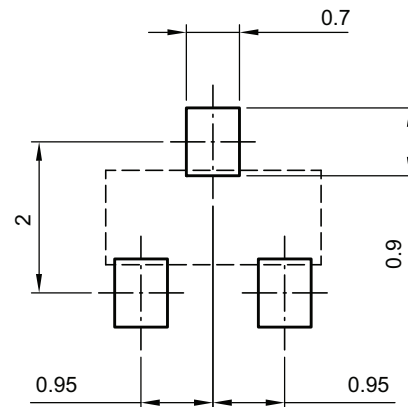
Fig. 4 - Typical Reverse Leakage Current vs. Reverse Voltage



PACKAGE DIMENSIONS in millimeters: SOT-23



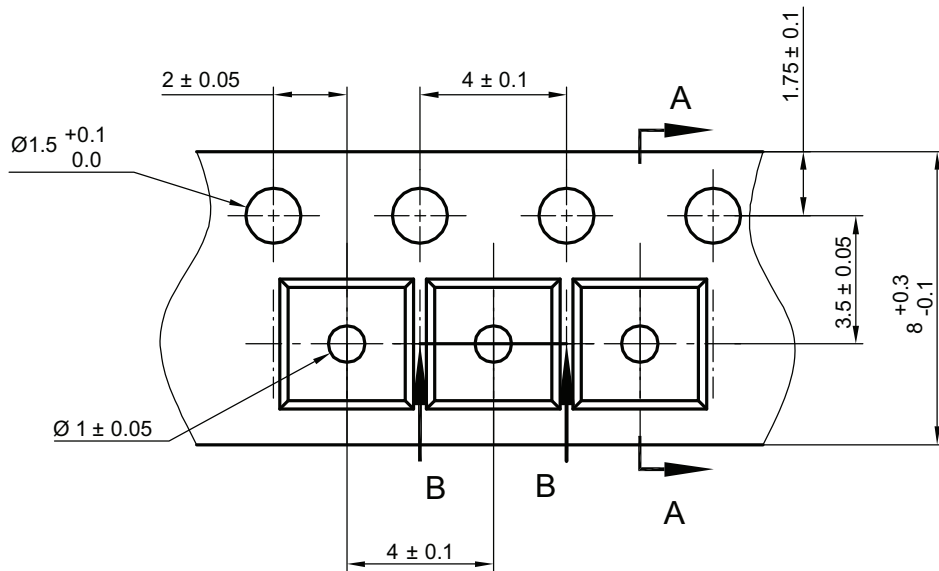
footprint recommendation:



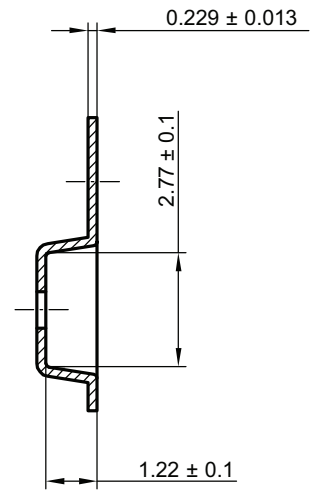
Created - Date: 18-Oct-2021  
 Rev. 01 - Date: 18-Jan-2022  
 S8-V-3929.01-009 (4)



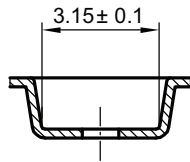
CARRIER TAPE SOT-23



A-A Section



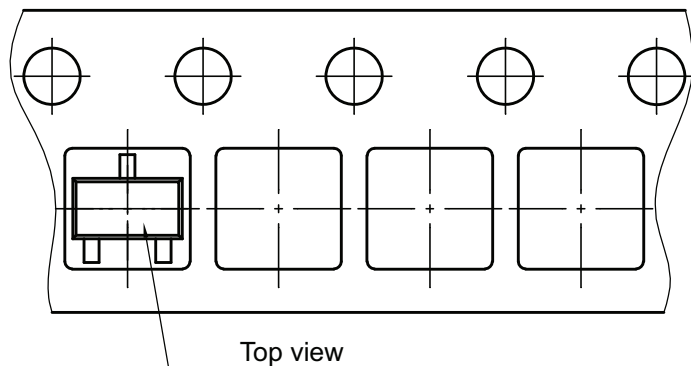
B-B Section



Created Date: 04-Feb-2010  
Rev. Date: 07-Feb-2022  
S8-V-3929.01-005 (4)

ORIENTATION IN CARRIER TAPE SOT-23

Unreeling direction



SOT-23

Created Date: 04-Feb-2010  
Rev. Date: 07-Nov-2022  
S8-V-3929.01-005 (4)



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