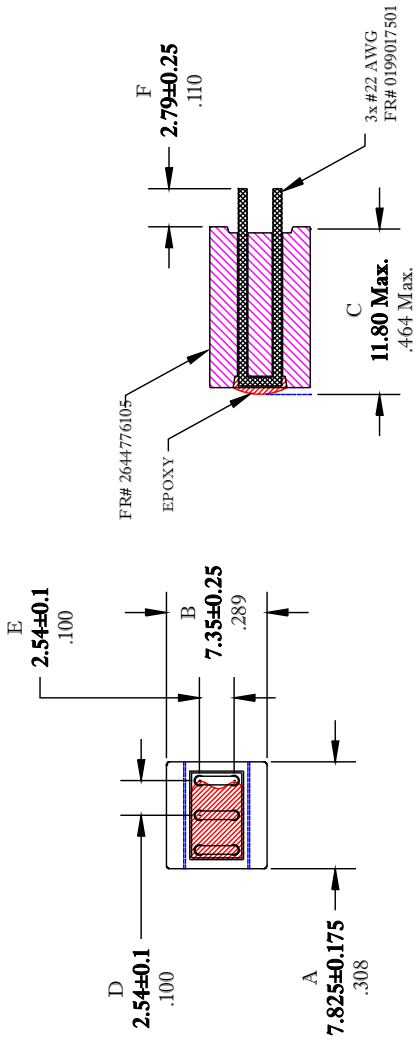


** Not all features are shown in this drawing. For more detailed information, please contact your Fair-Rite representative.

UNCONTROLLED ~CATALOG~ DRAWING

SIZE
B



ELECTRICAL

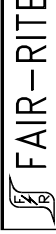
IMPEDANCE	25 MHz
MINIMUM	150 Min.

DIMENSIONS
BOLD NUMBERS ARE IN MILLIMETERS
 LIGHT NUMBERS ARE IN INCHES

NOTES:

- 1 - CHIPS, CRACKS, AND APPEARANCE TO MEET FAIR-RITE PRODUCTS VISUAL INSPECTION CRITERIA FOR GENERIC "PC" BEADS.
- 2 - PART MEETS FAIR-RITE PRODUCTS CORP. SOP# ELC-55 FOR THERMAL SHOCK RESISTANCE.
- 3 - WIRE MEETS SOLDERABILITY SPECIFICATION WHEN TESTED PER FAIR-RITE PRODUCTS CORP. SOP# ELC-57.
- 4 - IMPEDANCE MEASURED ON A HP4191A USING A 16092A TEST FIXTURE AT AN END WINDING.
- 5 - EPOXY TO BE INSULCAST LN 15-16 WITH A MIX RATIO OF 1:1, OR APPROVED EQUIVALENT.

FR# 44



TITLE		.100 T	
DRAWN BY	DATE	SIZE	
JSERGENICS	06-29-2018	B	
CHKD BY			
APPRVD BY			SCA

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