



**THE DATASHEET OF
ICS953401CFLFT**



[Home](#) > [Products](#) > [Clock / Timing Devices](#) > [PC-Notebook-Server Clocks](#) > [Clock Synthesizer by Chipset Vendor](#) > [Desktop Chipsets](#) > 953401 > 953401CFLFT

[Add to myIDT \[?\]](#)

You may also like...

953401CFLFT

Category: [Desktop Chipsets](#)

Generic Part: 953401

Market Group: PC CLOCK

Description: PC MAIN CLOCK

The ICS953401 is a two chip clock solution for desktop designs using SIS 656/662 style chipsets. When used with a zero delay buffer such as the ICS9P931 for DDR applications it provides all the necessary clocks signals for such a system.

Output Features:

- 2 - Pairs of differential CPUCLKs (differential current mode)
- 1 - Pair of differential SRCCLK (differential current mode)
- 5 - Pairs of differential PCI-Express clocks
- 8 - PCI @ 3.3V, 2 free-running
- 2 - ZCLKs @ 3.3V
- 1- 12/48MHZ @ 3.3V selectable by I2C
- 1- 24/48MHz, @ 3.3V selectable by I2C
- 3 - REF @ 3.3V, 14.318MHz.



Parameters

Package	SSOP 56 (PVG56)
Voltage	3.3 V
Package	SSOP 56
Speed	NA
Temperature	C
Status	Active
Sample	No
Minimum Order Quantity	1000
Factory Order Increment	1000

Distributor Inventory

No Pricing information is available from our Distributors at this time.

Documents

Type	Title	Size	Revision Date
Misc	PC Clocks Contact Info	61 KB	05/29/2007

Package

Description	SSOP 300 MIL, 56LD
Class	PLASTIC
Moisture Sensitivity Level (MSL)	1
Category	Green
Moisture Exposure Floor Life	Unlimited @ <30°C/85% RH
Peak Reflow Temperature	260°C
Rebake Conditions	NA
Length	18.4
Mark	G
Width	7.5
Pitch	0.64
Thickness	2.3
Status	Active

Looking for pricing, stock, or lifecycle information?

Click below to explore more details on WIN SOURCE:

-  [View ICS953401CFLFT on WIN SOURCE](#)
-  [IDT, Integrated Device Technology Inc Information](#)

Optimize Your Supply Chain with WIN SOURCE Solutions

-  Global Sourcing Solution
-  Obsolete Management
-  Cost Control Management
-  Shortage Management
-  Alternative Solution
-  Excess Inventory Management