

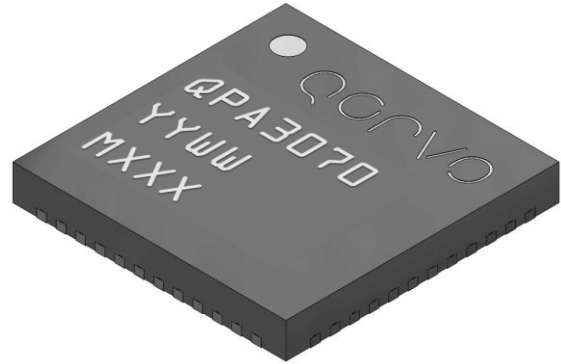
## Product Overview

Qorvo’s QPA3070 is a packaged, high-power S-band amplifier fabricated on Qorvo’s production 0.25  $\mu\text{m}$  GaN on SiC process (QGaN25HV). Covering 2.9–3.5 GHz, the QPA3070 provides 52 dBm of saturated output power and 28 dB of large-signal gain while achieving 58% power-added efficiency.

The QPA3070 is packaged in a 7 mm x 7 mm 48-pin plastic overmolded package. It can support a variety of operating conditions to best support system requirements. With good thermal properties, it can support a range of bias voltages.

The QPA3070 MMIC has DC blocking capacitors on both RF ports, which are matched to 50 ohms. The QPA3070 is ideal for military radar systems.

Lead-free and RoHS compliant.

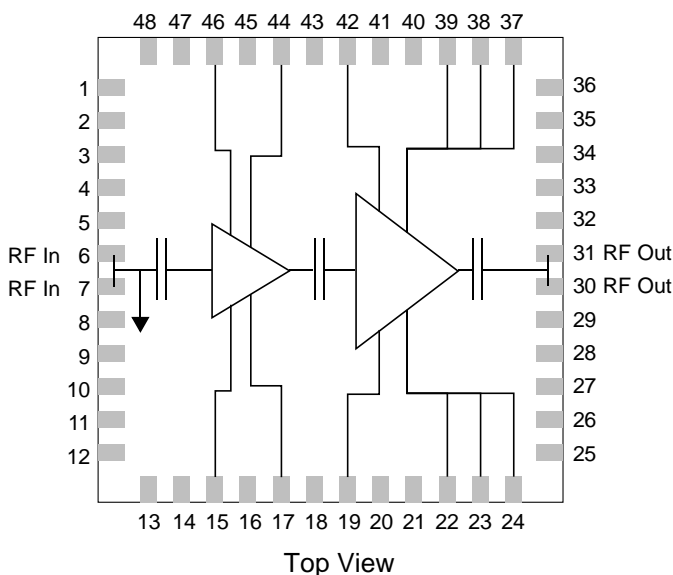


## Key Features

- Frequency Range: 2.9–3.5 GHz
- $P_{SAT}$  ( $P_{IN}=24$  dBm): 52 dBm
- PAE ( $P_{IN}=24$  dBm): 58 %
- Power Gain ( $P_{IN}=24$  dBm): 28 dB
- Bias:  $V_D = 50$  V,  $I_{DQ} = 422$  mA
- Package Dimensions: 7.00 x 7.00 x 0.85 mm

*Performance is typical across frequency. Please reference electrical specification table and data plots for more details.*

## Functional Block Diagram



## Applications

- Radar

## Ordering Information

Part No.	Description
QPA3070	2.9 – 3.5 GHz 150 W GaN Power Amplifier (10 Pcs.)
QPA3070TR7	250 piece 7" reel
QPA3070EVB02	Evaluation Board for QPA3070

### Absolute Maximum Ratings

Parameter	Value / Rang
Drain Voltage ( $V_D$ )	65 V
Gate Voltage Range ( $V_G$ )	-5 to 0 V
Drain Current ( $I_{D\_TOTAL}$ )	14.3 A
Gate Current ( $I_G$ )	See plot on page 12
Power Dissipation ( $P_{DISS}$ ), 85 °C	160 W
Input Power ( $P_{IN}$ ), Pulsed, 50 $\Omega$ , $V_D=50$ V, $I_{DQ}=422$ mA, 85 °C	30 dBm
Input Power ( $P_{IN}$ ), Pulsed, 3:1 VSWR, $V_D=50$ V, $I_{DQ}=422$ mA, 85 °C	28 dBm
Soldering Temperature (30 seconds)	260 °C
Storage Temperature	-55 to 150 °C

Operation of this device outside the parameter ranges given above may cause permanent damage. These are stress ratings only, and functional operation of the device at these conditions is not implied.

### Recommended Operating Conditions

Parameter	Value / Range
Drain Voltage ( $V_D$ )	50 V
Drain Current ( $I_{DQ\_TOTAL}$ )	422 mA
Gate Voltage Range ( $V_G$ )	-2.8 to -2.0 V
Operating Temperature	-40 to +85 °C

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all recommended operating conditions.

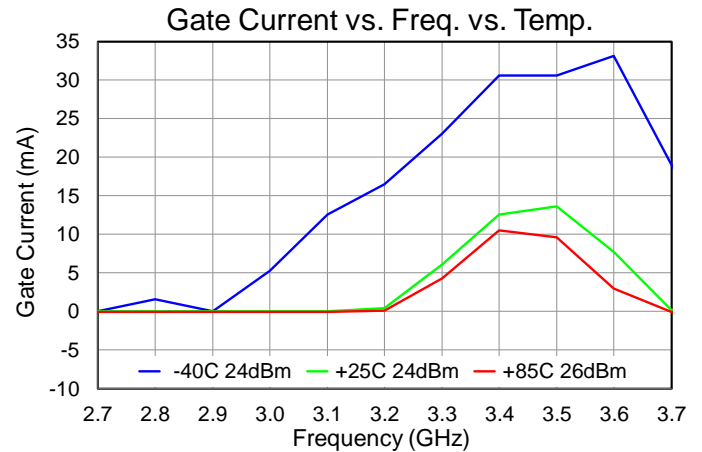
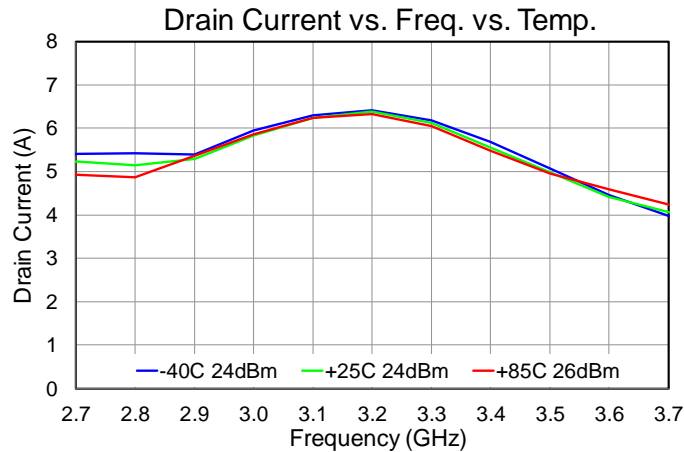
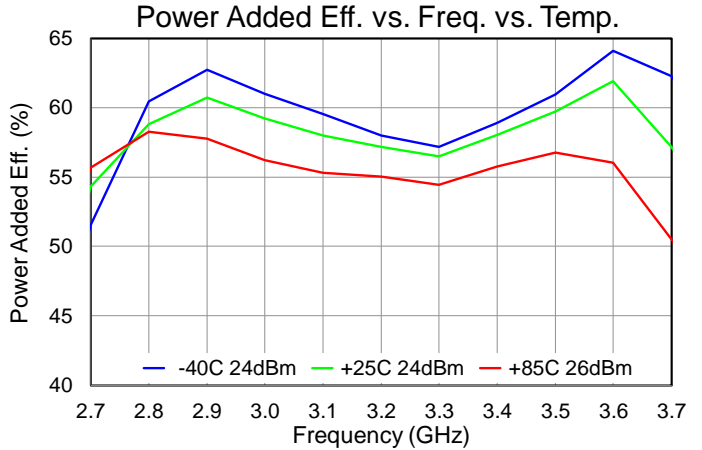
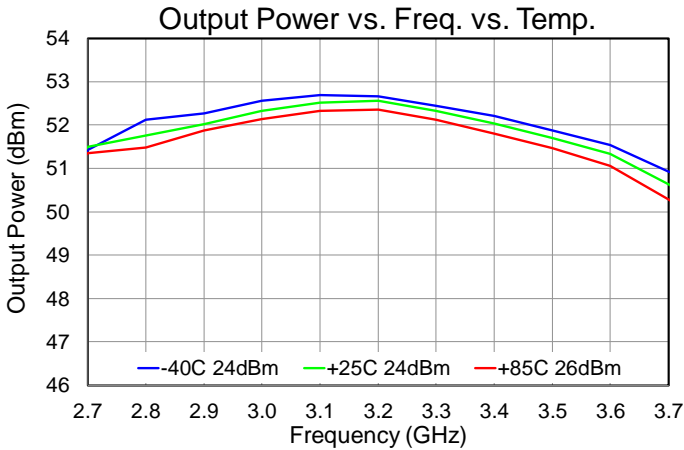
### Electrical Specifications

Parameter		Min	Typ	Max	Units
Operational Frequency Range		2.9		3.5	GHz
Output Power ( $P_{IN} = 24$ dBm)	2.9 GHz		52.0		dBm
	3.2 GHz		52.6		dBm
	3.5 GHz		51.7		dBm
Power Added Efficiency ( $P_{IN} = 24$ dBm)	2.9 GHz		60.7		%
	3.2 GHz		59.2		%
	3.5 GHz		59.7		%
Small Signal Gain (CW)	2.9 GHz		33.3		dB
	3.2 GHz		32.5		dB
	3.5 GHz		35.4		dB
Input Return Loss (CW)	2.9 GHz		12		dB
	3.2 GHz		17		dB
	3.5 GHz		16		dB
Output Return Loss (CW)	2.9 GHz		7		dB
	3.2 GHz		5		dB
	3.5 GHz		8		dB
Second Harmonic ( $P_{IN} = 24$ dBm)			-23		dBc
Third Harmonic ( $P_{IN} = 24$ dBm)			-22		dBc
Gate Leakage ( $V_D = 10$ V, $V_G = -3.7$ V)		-33.75			mA
$P_{OUT}$ Temp. Coeff. (85–25 °C, $P_{IN} = 24$ dBm)			-0.008		dB/°C
Sm. Sig. Gain Temp. Coefficient (85 to -40 °C, CW)			-0.043		dB/°C

Test conditions, unless otherwise noted: T = 25 °C,  $V_D = 50$  V,  $I_{DQ} = 422$  mA, PW = 300 us, Duty Cycle = 20%

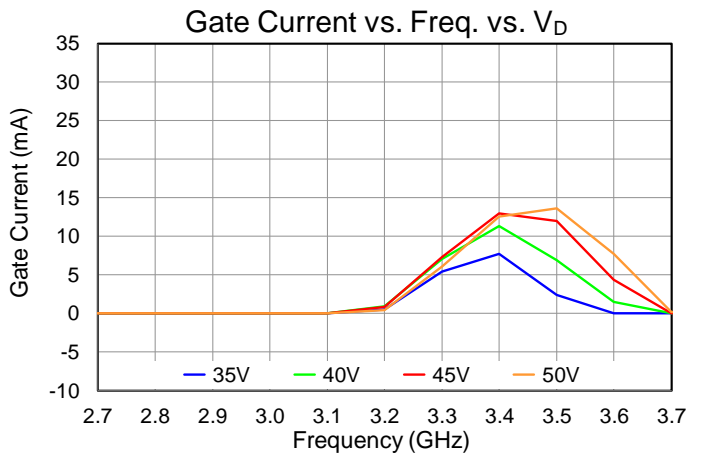
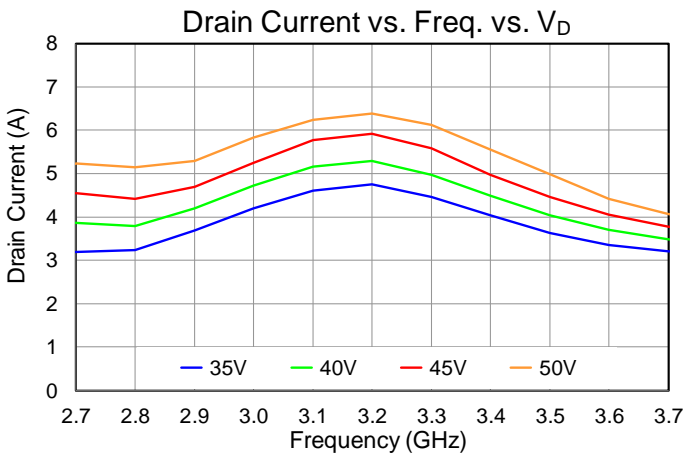
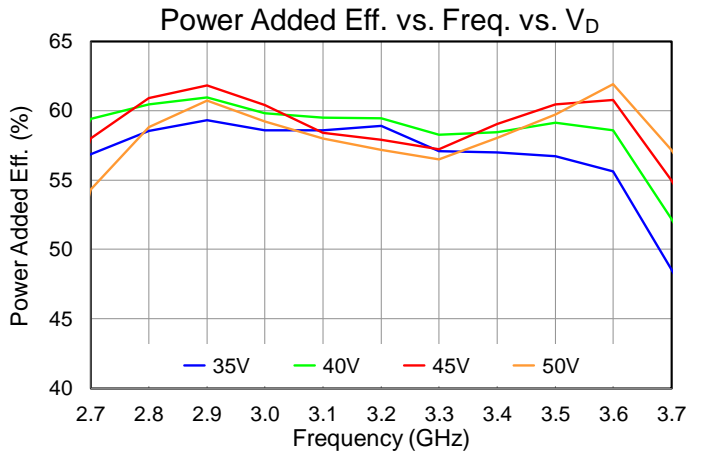
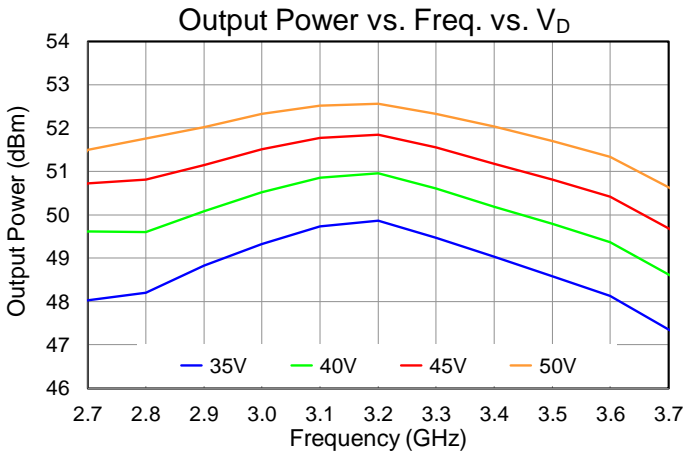
**Performance Plots – Large Signal**

Test conditions, unless otherwise noted: T = 25 °C, V<sub>D</sub> = 50 V, I<sub>DQ</sub> = 422 mA, PW = 300 us, Duty Cycle = 20%



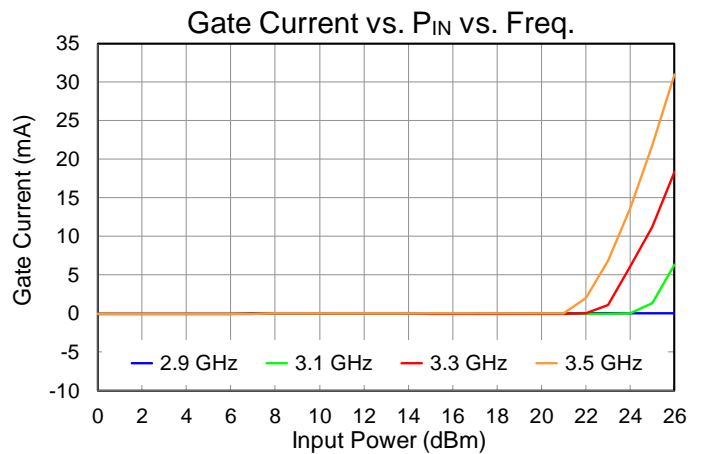
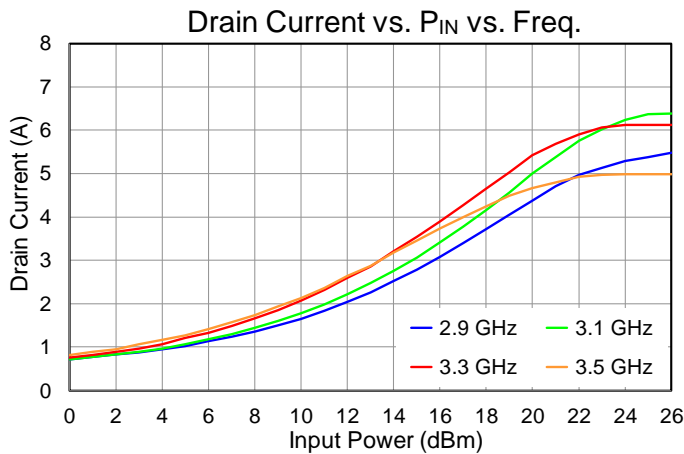
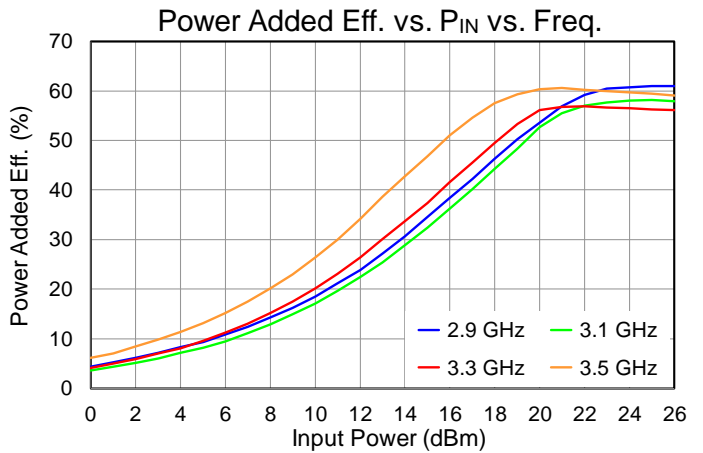
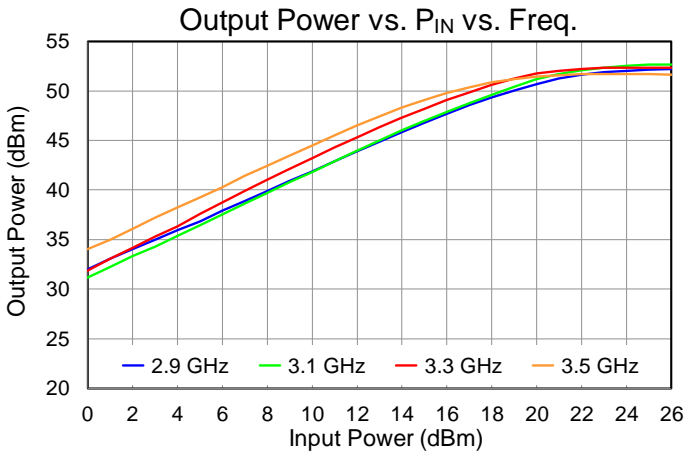
Performance Plots – Large Signal

Test conditions, unless otherwise noted: T = 25 °C, V<sub>D</sub> = 50 V, I<sub>DQ</sub> = 422 mA, PW = 300 us, Duty Cycle = 20%



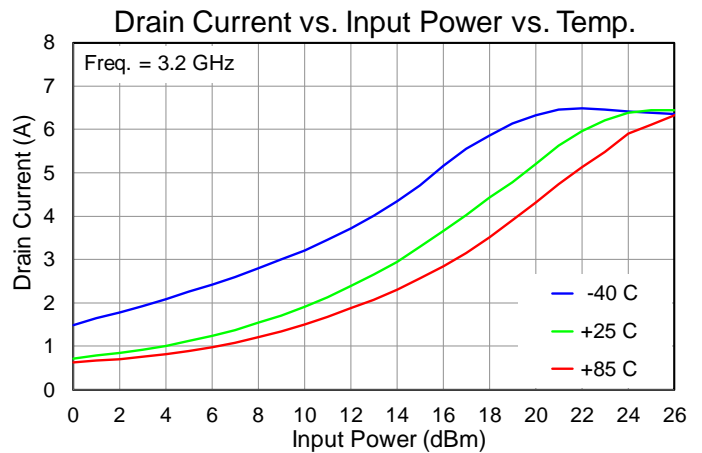
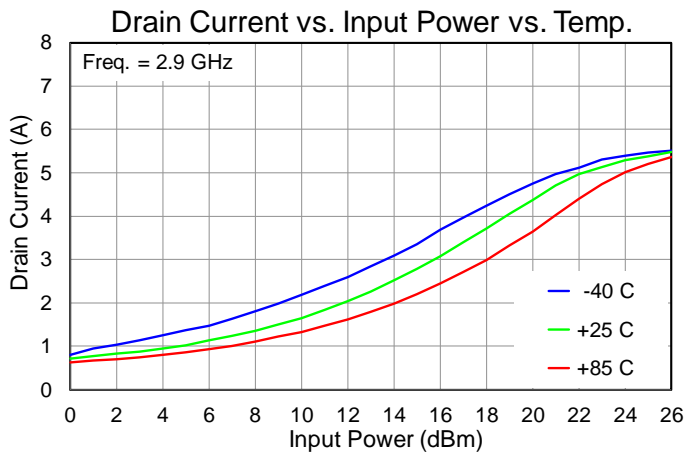
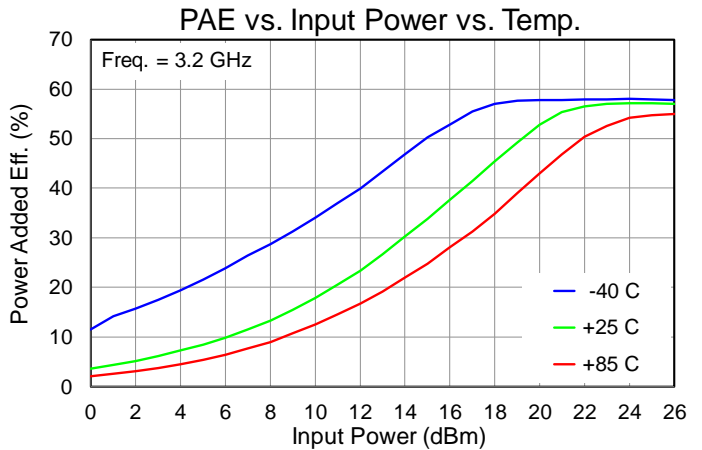
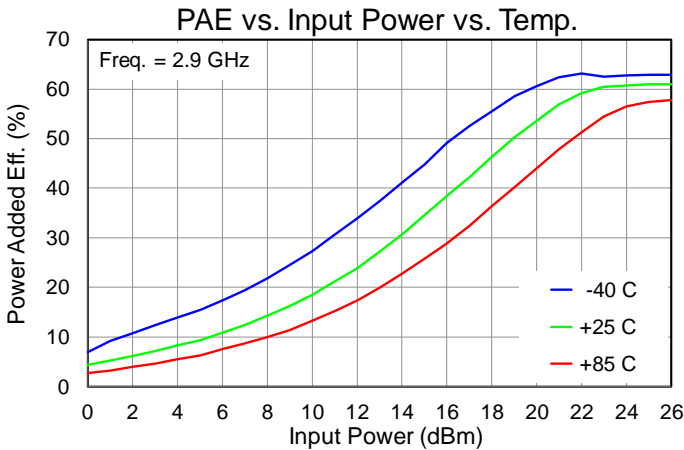
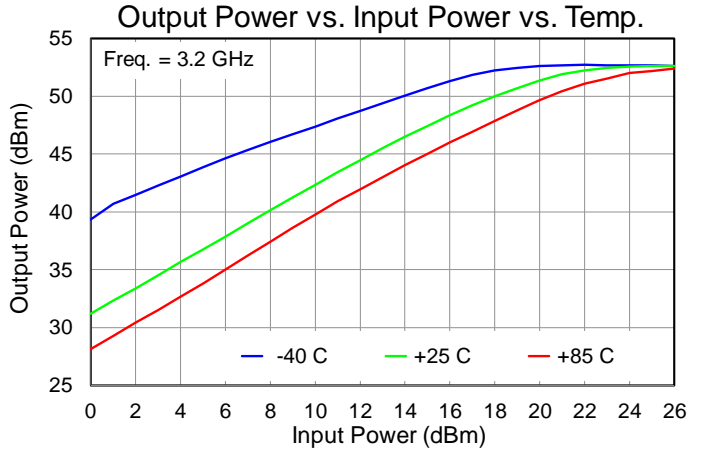
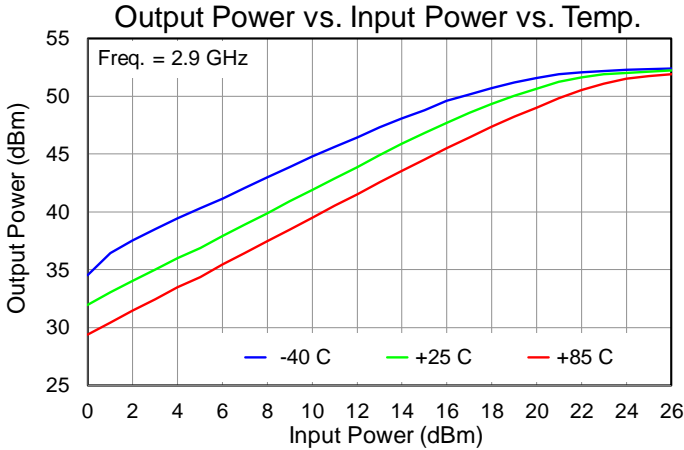
Performance Plots – Large Signal

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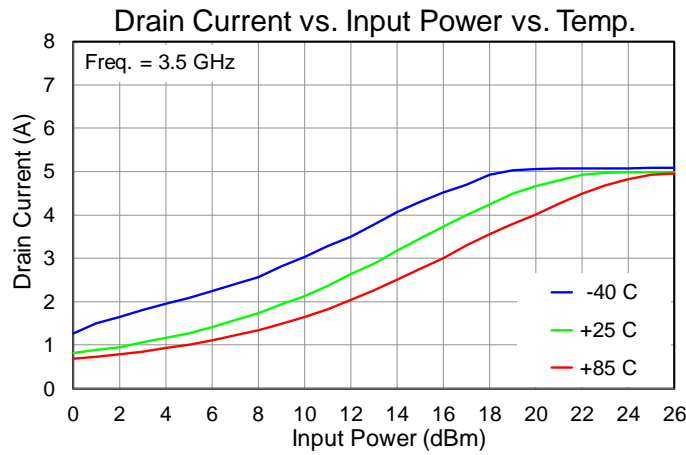
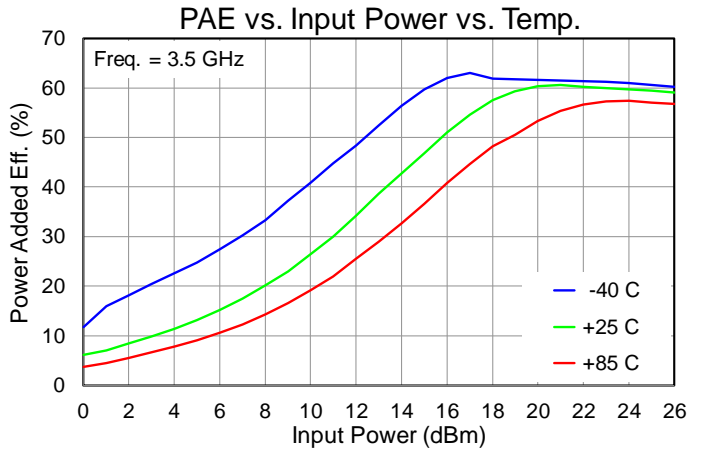
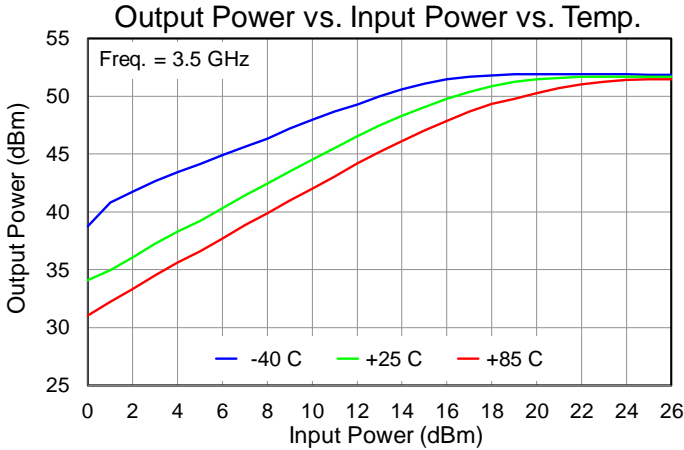
Performance Plots – Large Signal

Test conditions, unless otherwise noted: T = 25 °C, V<sub>D</sub> = 50 V, I<sub>DQ</sub> = 422 mA, PW = 300 us, Duty Cycle = 20%



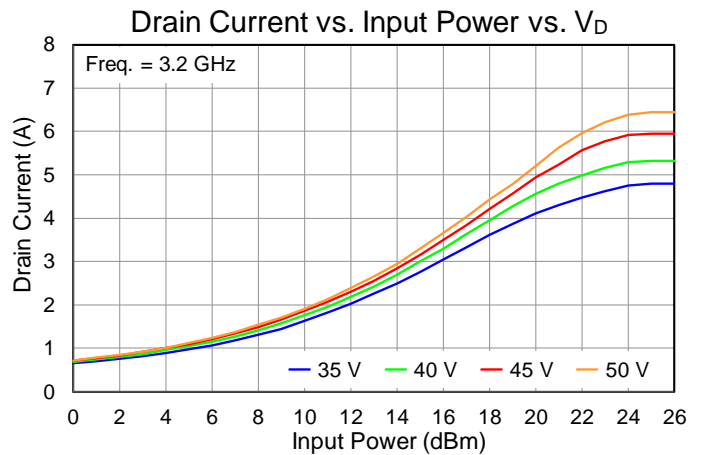
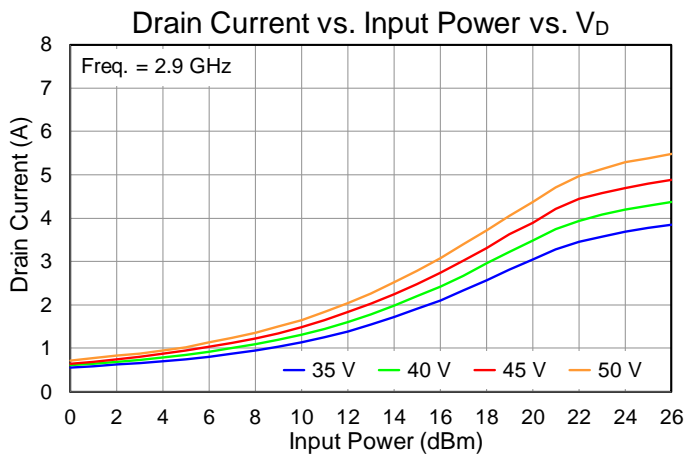
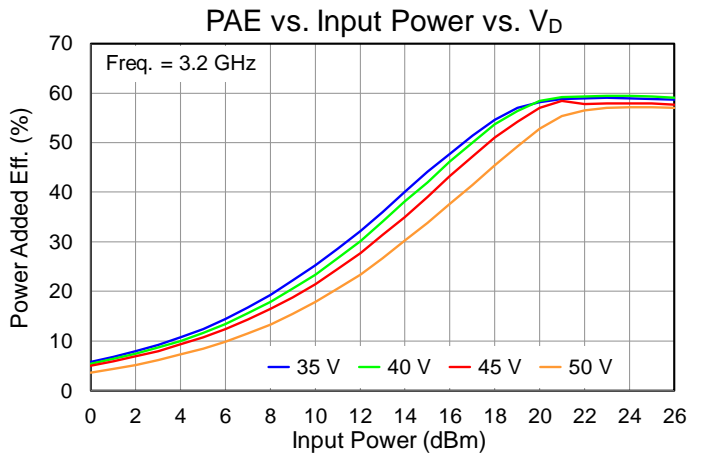
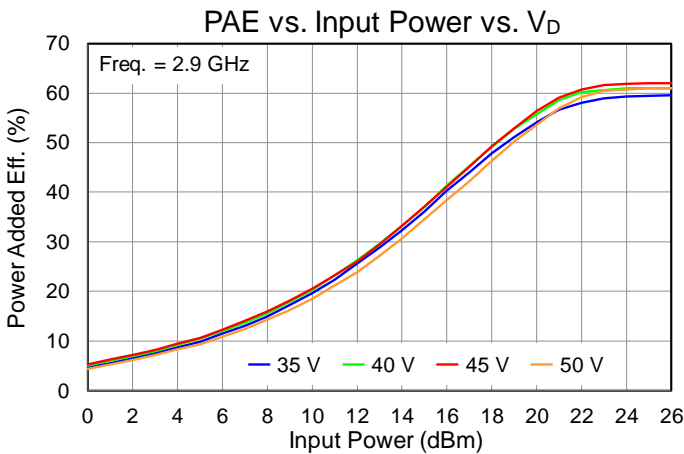
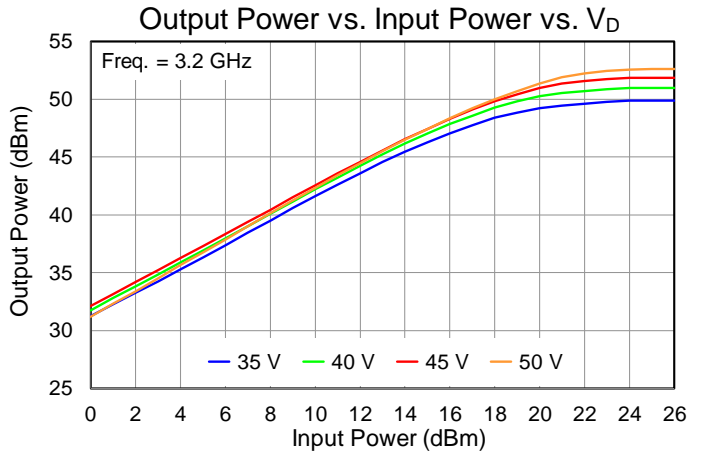
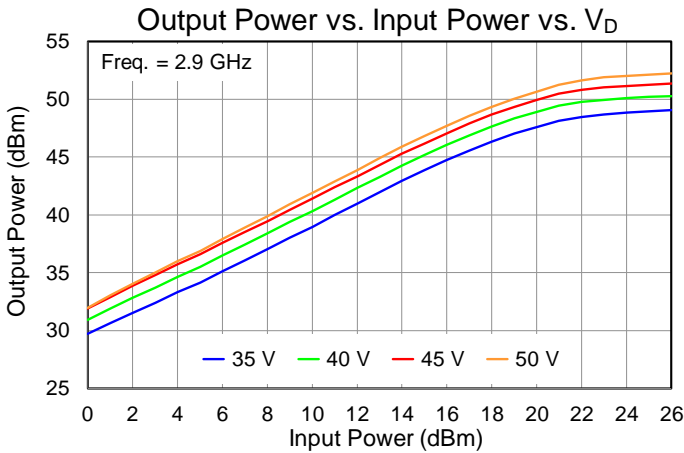
**Performance Plots – Large Signal**

Test conditions, unless otherwise noted: T = 25 °C, V<sub>D</sub> = 50 V, I<sub>DQ</sub> = 422 mA, PW = 300 us, Duty Cycle = 20%



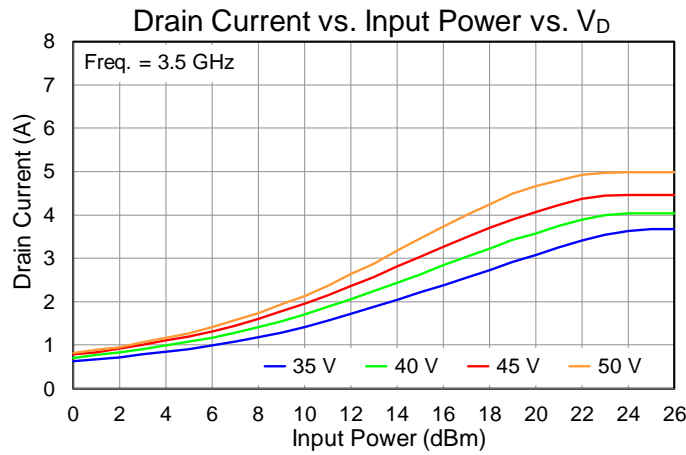
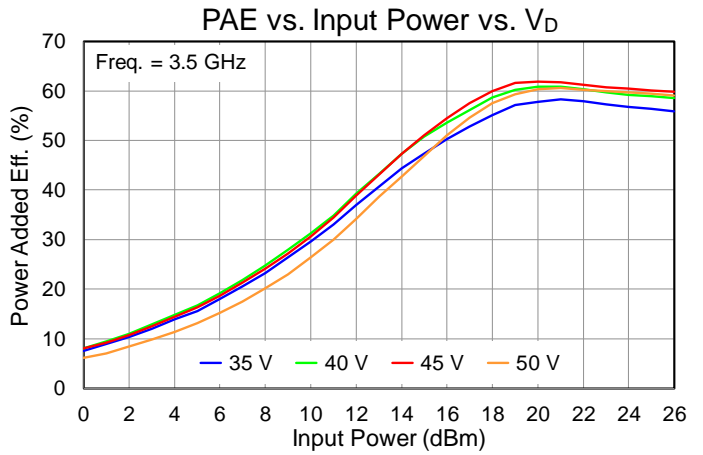
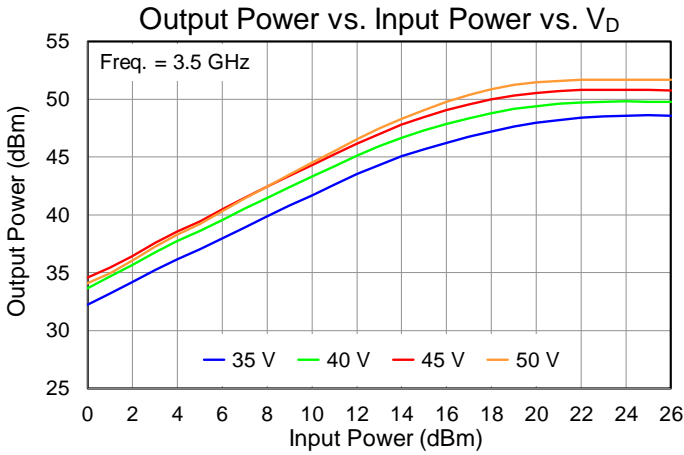
Performance Plots – Large Signal

Test conditions, unless otherwise noted:  $T = 25\text{ }^{\circ}\text{C}$ ,  $V_D = 50\text{ V}$ ,  $I_{DQ} = 422\text{ mA}$ ,  $PW = 300\text{ }\mu\text{s}$ , Duty Cycle = 20%



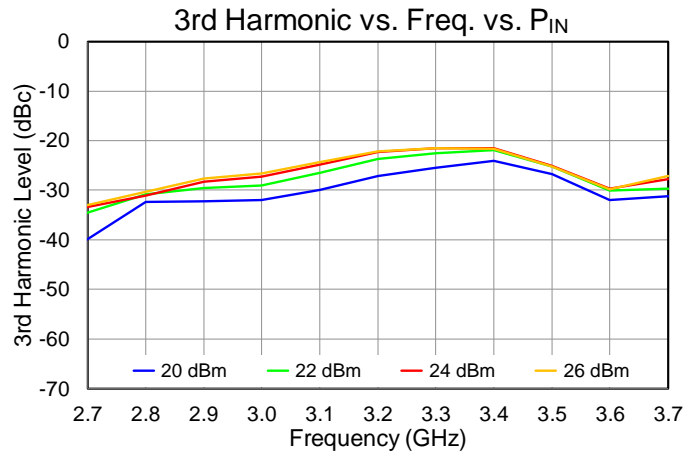
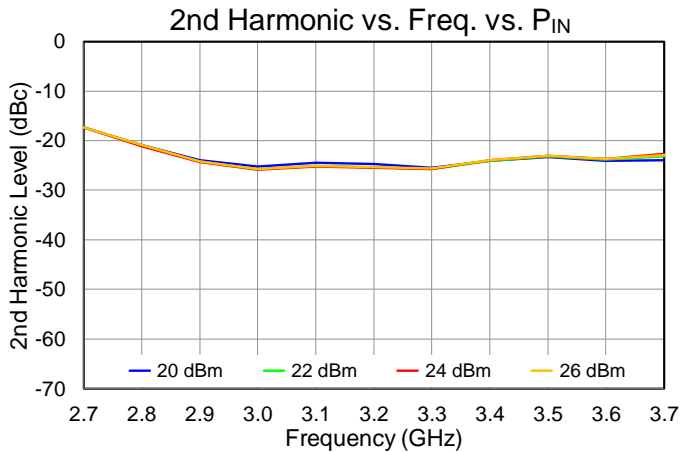
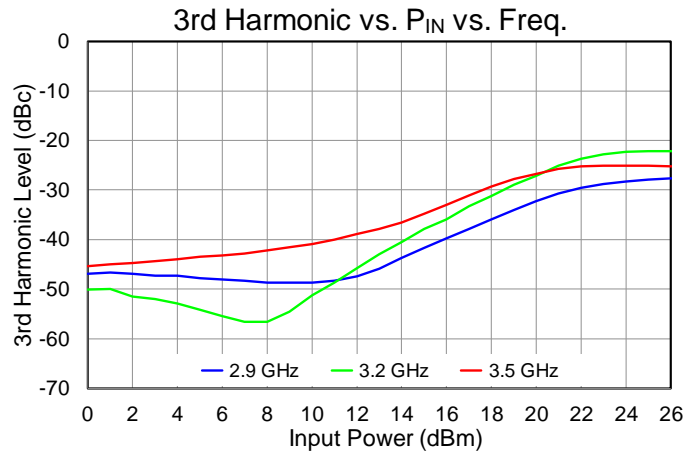
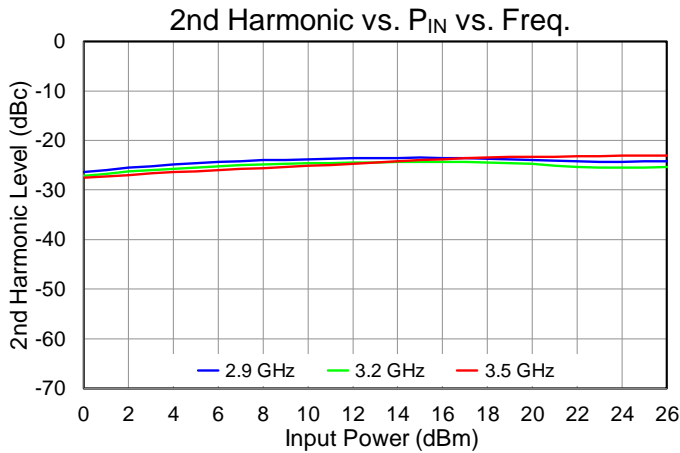
Performance Plots – Large Signal

Test conditions, unless otherwise noted: T = 25 °C, V<sub>D</sub> = 50 V, I<sub>DQ</sub> = 422 mA, PW = 300 us, Duty Cycle = 20%



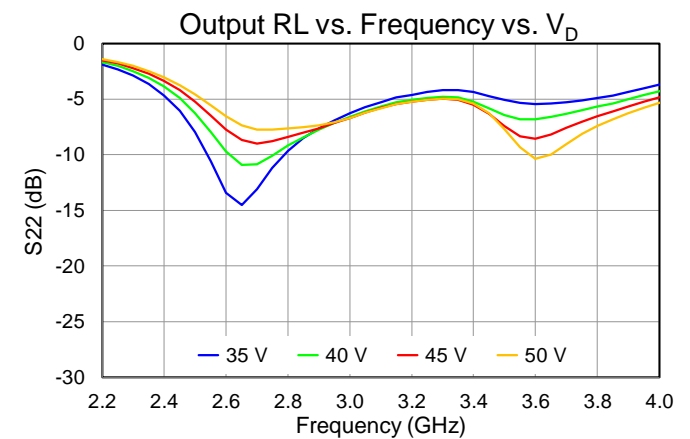
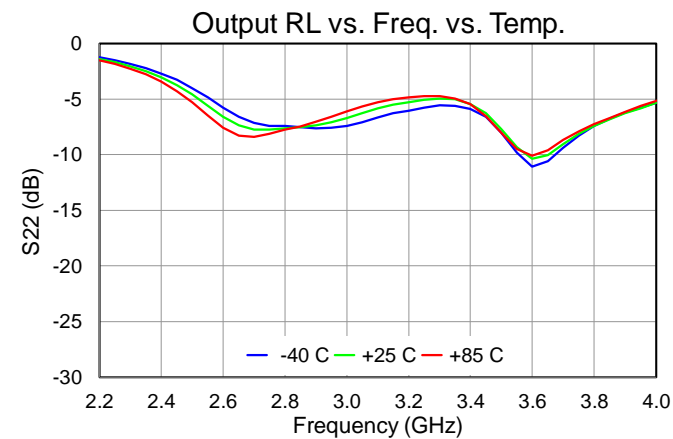
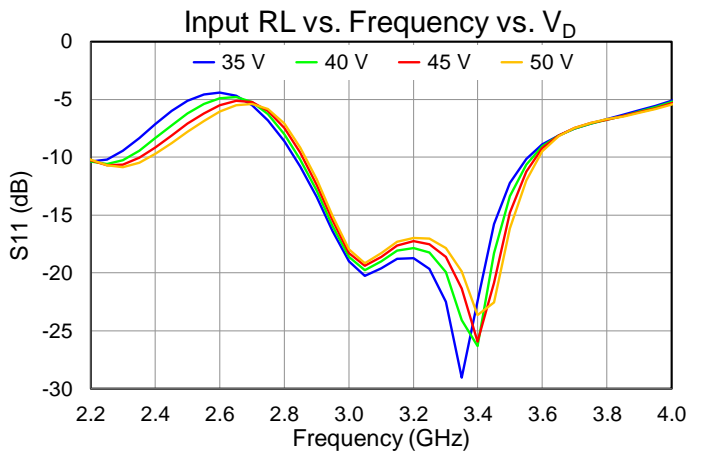
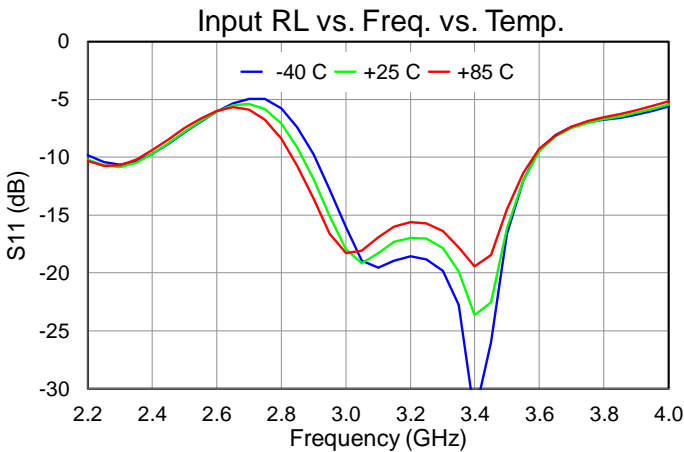
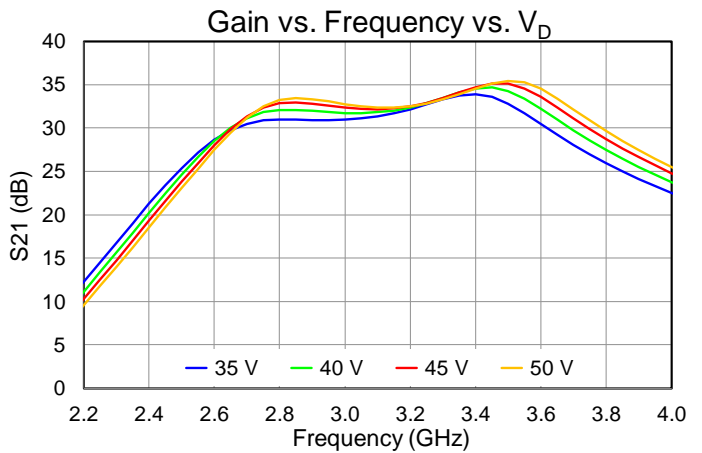
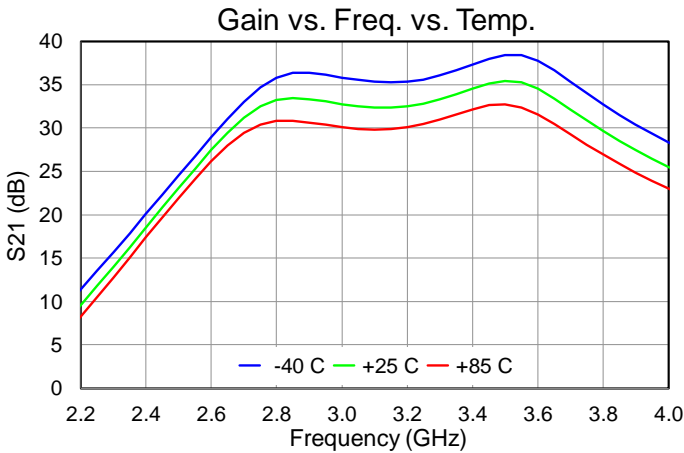
Performance Plots – Harmonics

Test conditions, unless otherwise noted: T = 25 °C, V<sub>D</sub> = 50 V, I<sub>DQ</sub> = 422 mA, PW = 300 us, Duty Cycle = 20%



**Performance Plots – Small Signal**

Test conditions, unless otherwise noted: T = 25 °C, V<sub>D</sub> = 50 V, I<sub>DQ</sub> = 422 mA, CW



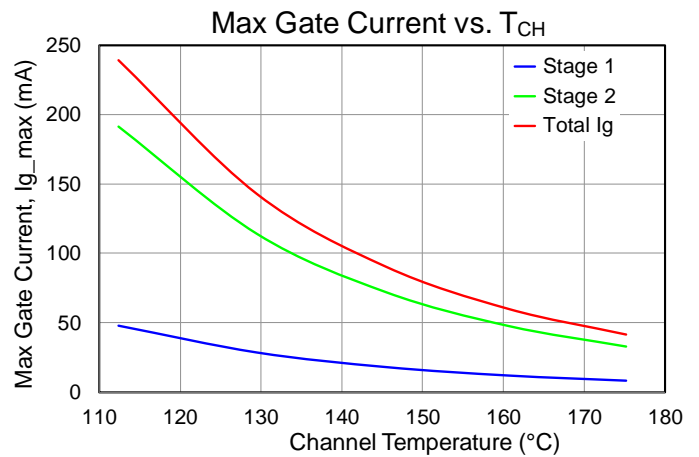
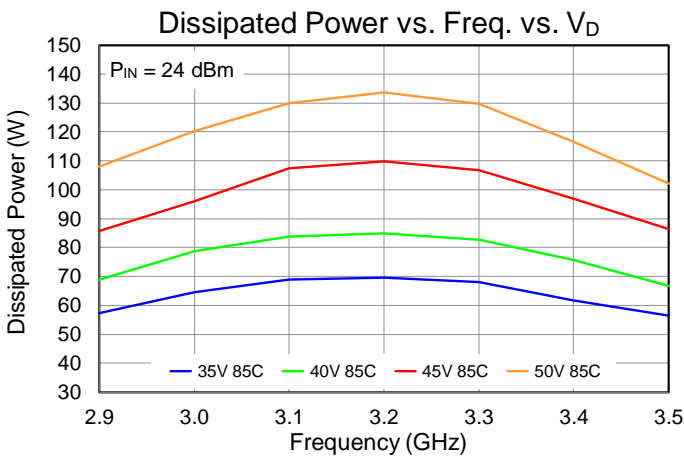
## Thermal and Reliability Information

Parameter	Test Conditions	Value	Units
Thermal Resistance ( $\theta_{JC}$ ) <sup>(1)</sup>	$T_{base} = 85\text{ }^{\circ}\text{C}$ , $V_D = 50\text{ V}$ , $I_{DQ} = 422\text{ mA}$ , $P_{DISS} = 21.1\text{ W}$	0.528	$^{\circ}\text{C/W}$
Channel Temperature, $T_{CH}$ (No RF drive) <sup>(2)</sup>		96.1	$^{\circ}\text{C}$
Thermal Resistance ( $\theta_{JC}$ ) <sup>(1)</sup>	$T_{base} = 85\text{ }^{\circ}\text{C}$ , $V_D = 50\text{ V}$ , $I_{DQ} = 422\text{ mA}$ , Freq = 3.2 GHz, $I_{D\_Drive} = 5.91\text{ A}$ , $P_{IN} = 24\text{ dBm}$ , $P_{OUT} = 52.0\text{ dBm}$ , $P_{DISS} = 133.5\text{ W}$ , $PW = 300\text{ }\mu\text{s}$ , DC = 20%	0.571	$^{\circ}\text{C/W}$
Channel Temperature, $T_{CH}$ (w/RF drive) <sup>(2)</sup>		161.2	$^{\circ}\text{C}$

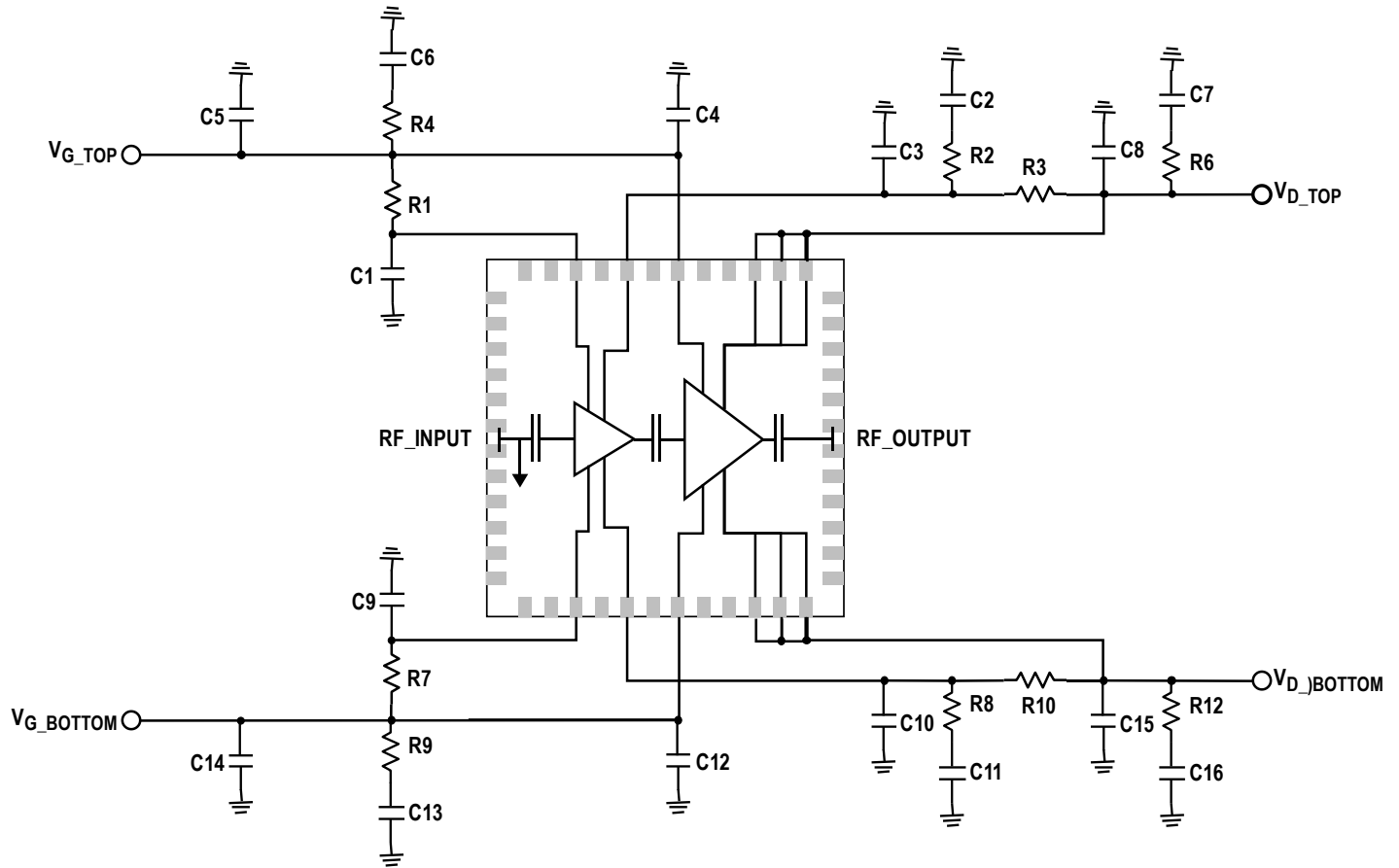
Notes:

1. Thermal resistance determined to the back of the package (fixed at 85 °C)
2. IR scan equivalent. Refer to the following document: [GaN Device Channel Temperature, Thermal Resistance, and Reliability Estimates](#)

## Dissipated Power and Maximum Gate Current



Applications Information



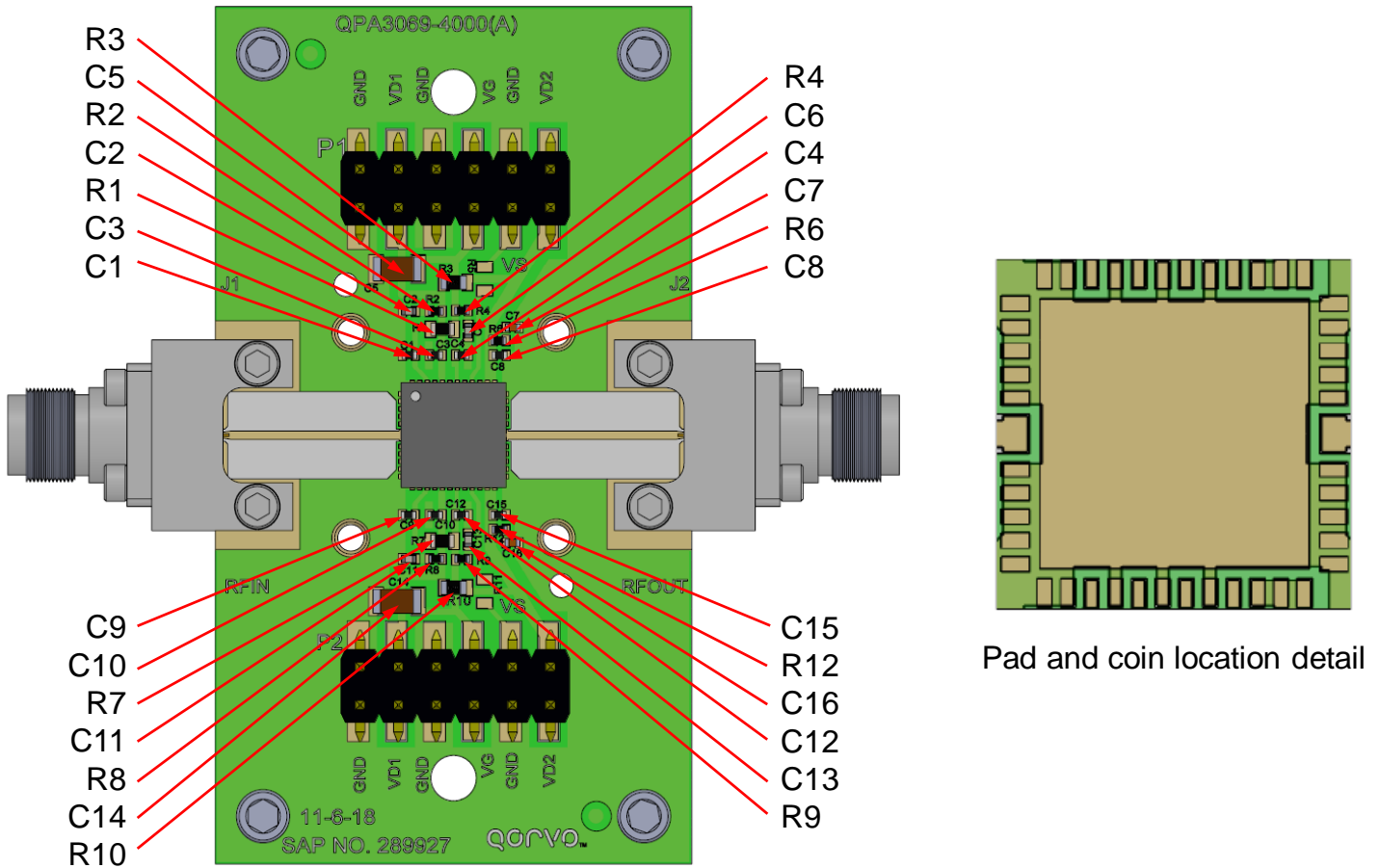
Notes:

1.  $V_G$  &  $V_D$  must be biased from both sides.

Bill of Materials

Reference Des.	Value	Description	Manuf.	Part Number
C1,C3,C4,C8,C9,C10,C12,C15	1000 pF	CAP, 1000pF, 10%, 100V, X7R, 0402	Various	
C2,C6,C11,C13	0.01 uF	CAP, 0.01uF, ±10%, 50V, X7R, 0402	Various	
C5,C14	10 uF	CAP, 10uF, 20%, 50V, 20%, X5R, 1206	Various	
C7,C16	0.1 uF	CAP, 0.1uF, 10%, 50V, X7R, 0402	Various	
R3,R10	0 Ω	RES, 0 OHM, 1/10W, 0603	Various	
R2,R4,R6,R8,R9,R12	10 Ω	RES, 10 OHM, 5%, 0.1W, 0402	Various	
R1,R7	10 Ω	RES, 10 OHM, 5%, 1/10W, 0603	Various	
J1, J2	2.92 mm	Female End Launch Connector	Southwest Microwave	1092-02A-5

Evaluation Board (EVB) Layout Assembly



RF PCB is Rogers 6035HTC, 0.010" thick, 0.5 ounce copper both sides  
 Overall PCB is a multi-layer board with a coined center pad for improved thermal conductivity

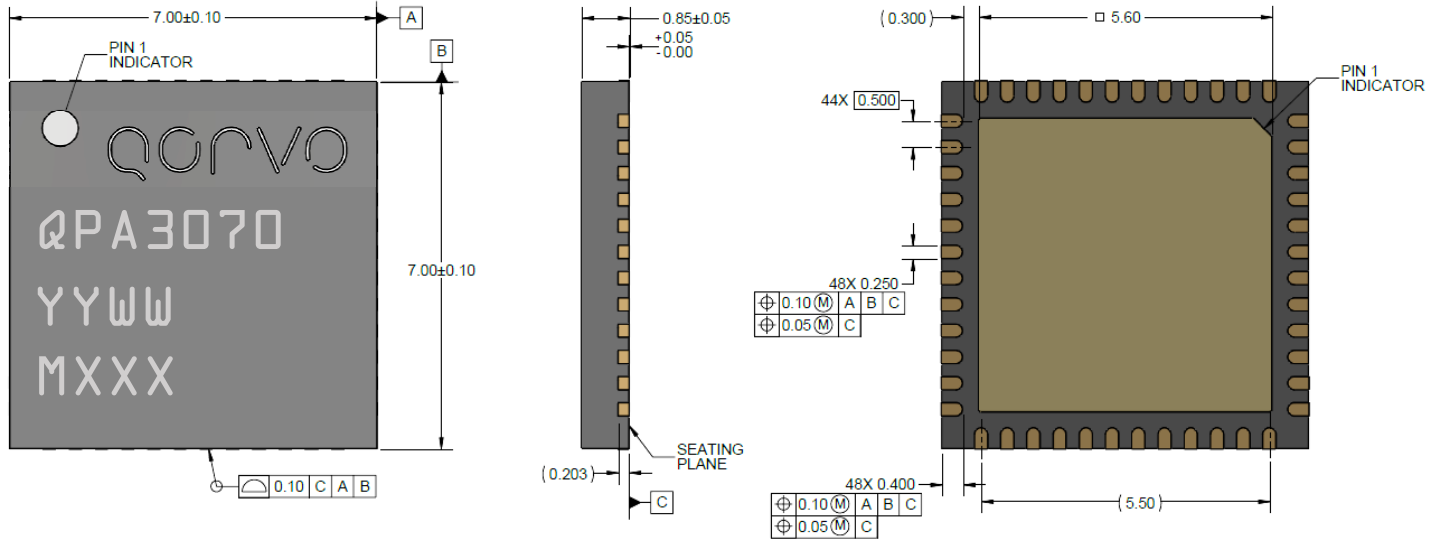
**Bias-Up Procedure**

1. Set  $I_D$  (peak) supply limit to 7500 mA,  $I_G$  supply limit to 60 mA
2. Set  $V_G$  to -5.0 V
3. Set  $V_D$  +50 V
4. Adjust  $V_G$  more positive until  $I_{DQ} \approx 422$  mA (peak)
5. Apply RF signal

**Bias-Down Procedure**

1. Turn off RF signal
2. Reduce  $V_G$  to -5.0 V. Ensure  $I_{DQ} \sim 0$ mA
4. Set  $V_D$  to 0 V
5. Turn off  $V_D$  supply
6. Turn off  $V_G$  supply

**Mechanical Information and Bond Pad Description**



Unless otherwise specified, all dimensions are in mm.

Package leads are gold plated.

Part is mold encapsulated.

Tolerances:

.XX =  $\pm 0.25$

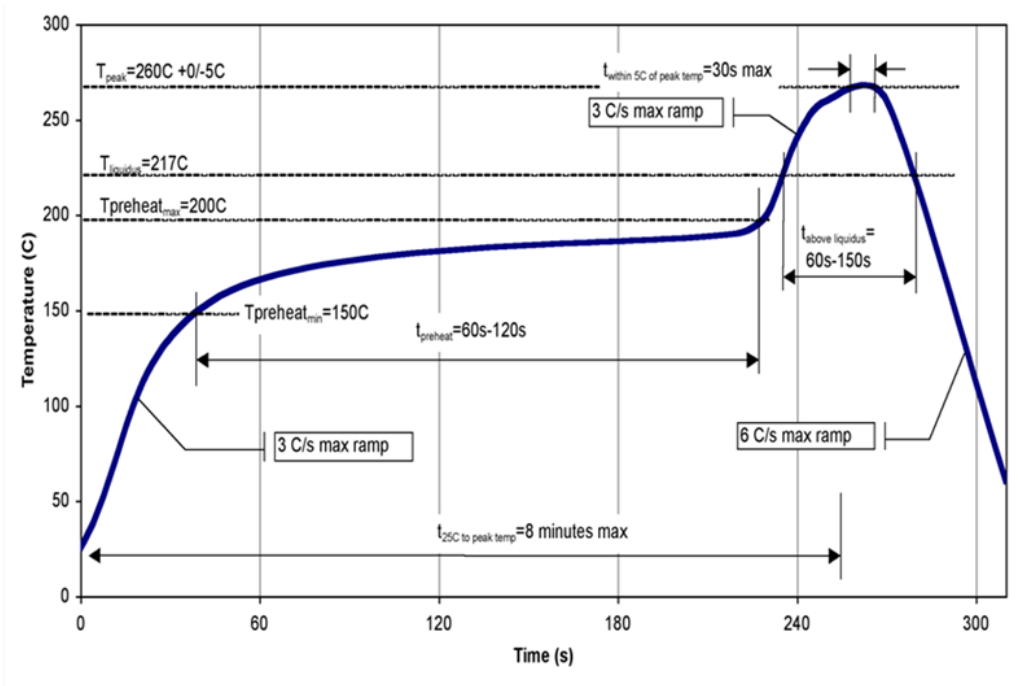
.XXX =  $\pm 0.100$

.XXXX =  $\pm 0.0254$

**Bond Pad Description**

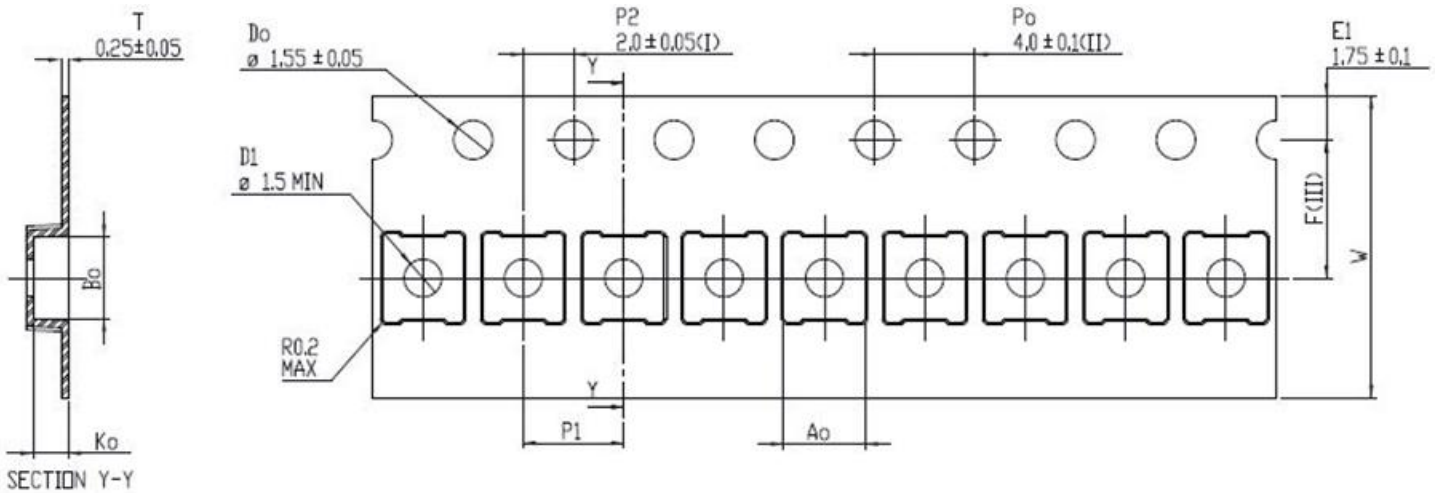
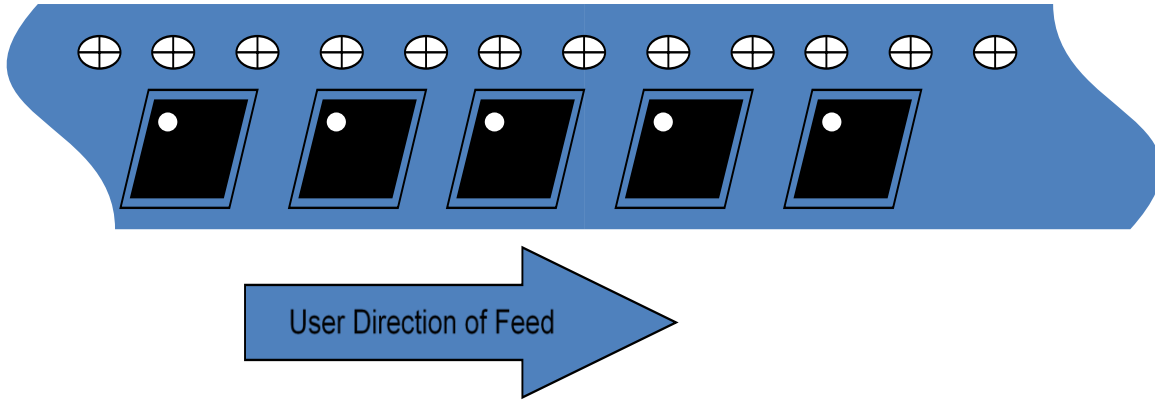
Pad No.	Symbol	Description
1-5, 8-14, 16, 18, 20-21, 25-29, 32-36, 40-41, 43, 45, 47-48	NC	No connection. Grounding these pins is recommended, but not required
6, 7	RF INPUT	RF Input; matched to 50 $\Omega$ ; DC grounded
15, 46	VG1	Gate voltage, stage 1. Bias network is required; see Application Circuit as an example (must be biased from both sides)
17, 44	VD1	Drain voltage, stage 1. Bias network is required; see Application Circuit as an example (must be biased from both sides)
19, 42	VG2	Gate voltage, stage 2. Bias network is required; see Application Circuit as an example (must be biased from both sides)
22-24, 37-39	VD2	Drain voltage, stage 2. Bias network is required; see Application Circuit as an example (must be biased from both sides)
30, 31	RF OUTPUT	RF Output; matched to 50 $\Omega$ ; DC blocked
Center Pad	GND	Center pad must be grounded to PCB

Recommended Soldering Temperature Profile



**Tape and Reel Information – Carrier and Cover Tape Dimensions**

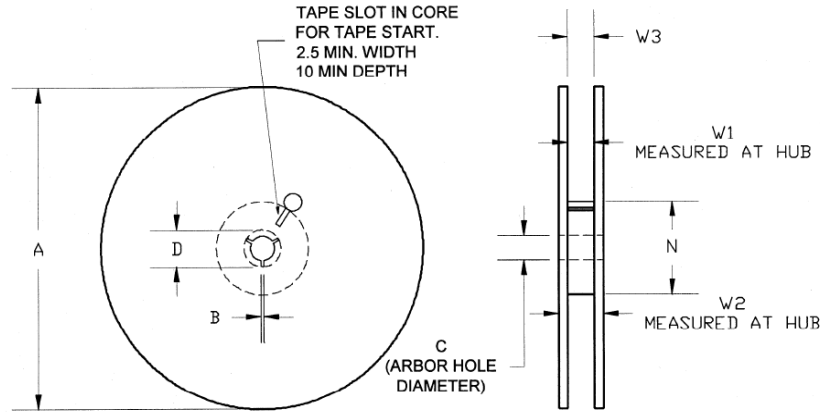
Tape and reel specifications for this part are also available on the Qorvo website.  
 Standard T/R size = 250 pieces on a 7" reel.



Feature	Measure	Symbol	Size (in)	Size (mm)
Cavity	Length	A0	0.128	3.25
	Width	B0	0.128	3.25
	Depth	K0	0.055	1.40
	Pitch	P1	0.157	4.00
Centerline Distance	Cavity to Perforation - Length Direction	P2	0.079	2.00
	Cavity to Perforation - Width Direction	F	0.217	5.50
Cover Tape	Width	C	0.362	9.20
Carrier Tape	Width	W	0.472	12.00

**Tape and Reel Information – Reel Dimensions**

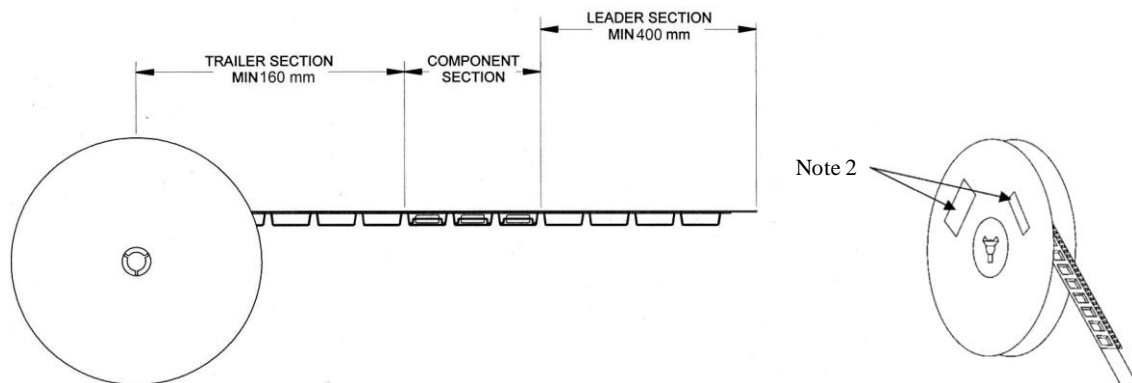
Packaging reels are used to prevent damage to devices during shipping and storage, loaded carrier tape is typically wound onto a plastic take-up reel. The reel size is 7" diameter. The reels are made from high-impact injection-molded polystyrene (HIPS), which offers mechanical and ESD protection to packaged devices.



Feature	Measure	Symbol	Size (in)	Size (mm)
Flange	Diameter	A	6.969	177.0
	Thickness	W2	0.717	18.2
	Space Between Flange	W1	0.504	12.8
Hub	Outer Diameter	N	2.283	58.0
	Arbor Hole Diameter	C	0.512	13.0
	Key Slit Width	B	0.079	2.0
	Key Slit Diameter	D	0.787	20.0

**Tape and Reel Information – Tape Length and Label Placement**

Tape and reel specifications for this part are also available on the Qorvo website. Standard T/R size = 250 pieces on a 7" reel.



Notes:

1. Empty part cavities at the trailing and leading ends are sealed with cover tape. See EIA 481.
2. Labels are placed on the flange opposite the sprockets in the carrier tape.

## Handling Precautions

Parameter	Rating	Standard
ESD – Human Body Model (HBM)	1B	ANSI/ESDA/JEDEC JS-001
ESD – Charge Device Model (CDM)	C3	ANSI/ESDA/JEDEC JS-002
MSL – Moisture Sensitivity Level	MSL3	IPC/JEDEC J-STD-020



Caution!  
ESD-Sensitive Device

## Solderability

Compatible with the latest version of J-STD-020 Lead-free solder, 260 °C.

## RoHS Compliance

This part is compliant with 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment) as amended by Directive 2015/863/EU.

This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C<sub>15</sub>H<sub>12</sub>Br<sub>4</sub>O<sub>2</sub>) Free
- PFOS Free
- SVHC Free

## Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations:

Web: [www.qorvo.com](http://www.qorvo.com)

Tel: 1-844-890-8163

Email: [customer.support@qorvo.com](mailto:customer.support@qorvo.com)

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