



**THE DATASHEET OF
WG50N65DHWQ**



1. General description

High speed IGBT with anti-parallel diode in TO247 package.



2. Features and benefits

- High speed with low switching losses
- Fast and soft recovery anti-parallel diode
- Positive $V_{CE(sat)}$ temperature coefficient
- Trench gate field-stop technology
- Halogen Free package and Pb-free lead finish, RoHS compliant
- Low thermal resistance
- Qualified according to JEDEC, Meets UL94V0 Flammability requirement

3. Applications

- Power Factor Correction
- Welding Converter
- Solar Inverter
- Industrial Inverter
- UPS

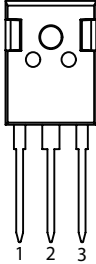
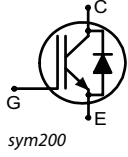
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Value			Unit	
V_{CE}	Collector-emitter voltage, $T_j \geq 25\text{ °C}$	650			V	
I_C	DC collector current, limited by $T_{j(max)}$ $T_C = 100\text{ °C}$	50			A	
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
$V_{CE(sat)}$	Collector-emitter saturation voltage	$V_{GE} = 15\text{ V}; I_C = 50\text{ A}; T_j = 25\text{ °C}$	-	1.65	2	V

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		
2	C	collector		
3	E	emitter		
mb	C	mounting base; connected to collector		

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WG50N65DHW	TO247	WG50N65DHWQ	Tube	30	SOT429	25-Mar-2013

7. Marking

Table 4. Marking codes

Type number	Marking codes
WG50N65DHW	WG50N 65DHW

8. Limiting values

Table 5. Limiting values

Symbol	Parameter	Value	Unit
V_{CE}	Collector-emitter voltage, $T_j \geq 25\text{ °C}$	650	V
I_C	DC collector current, limited by $T_{j(max)}$ $T_C = 25\text{ °C}$ $T_C = 100\text{ °C}$	91 50	A
$I_{C(puls)}$	Pulsed collector current, t_p limited by $T_{j(max)}$	200	A
-	Turn off safe operating area $V_{CE} \leq 600\text{ V}$, $T_j \leq 150\text{ °C}$, $t_p = 1\text{ }\mu\text{s}$	200	A
I_F	Diode forward current, limited by $T_{j(max)}$ $T_C = 25\text{ °C}$ $T_C = 100\text{ °C}$	100 50	A
V_{GE}	Gate-emitter voltage Transient Gate-emitter voltage ($t_p \leq 10\text{ }\mu\text{s}$, $D < 0.010$)	± 20	V
P_{tot}	Power dissipation $T_C = 25\text{ °C}$ Power dissipation $T_C = 100\text{ °C}$	278 111	W
T_{stg}	Storage temperature	-55 to 150	°C
T_j	Operating junction temperature	-55 to 150	°C
-	Peak soldering temperature	260	°C
M	Mounting Torque with washer	0.55	Nm

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	IGBT thermal resistance from junction to mounting base		-	-	0.45	K/W
$R_{th(j-mb)}$	Diode thermal resistance from junction to mounting base		-	-	1.9	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient		-	40	-	K/W

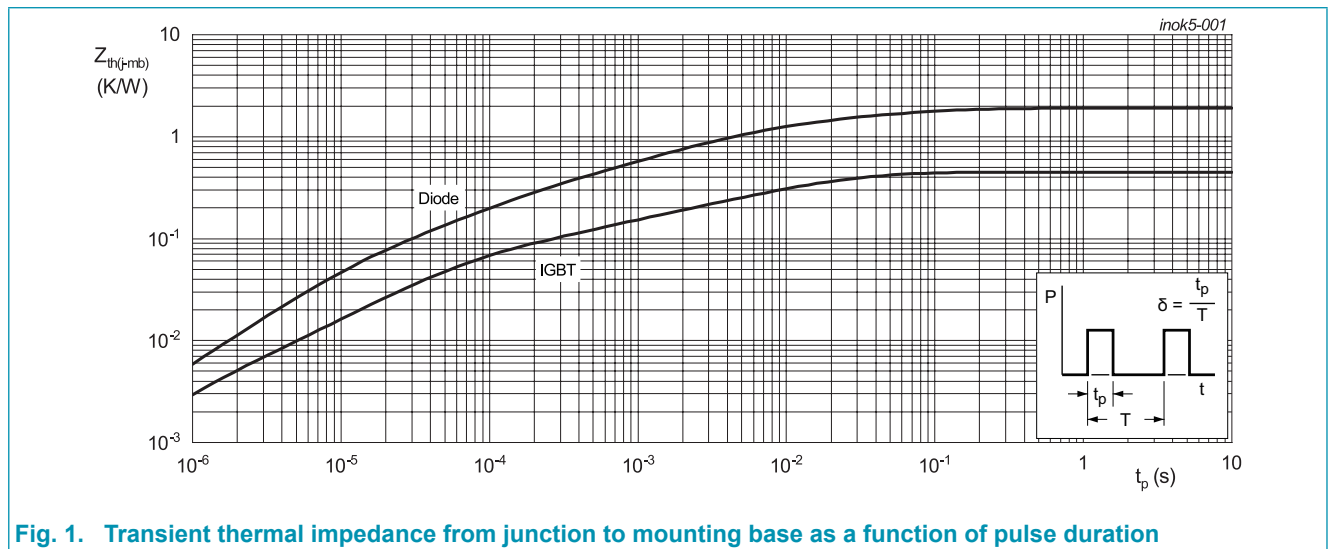


Fig. 1. Transient thermal impedance from junction to mounting base as a function of pulse duration

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
BV_{CES}	Collector-emitter breakdown voltage	$V_{GE} = 0\text{ V}; I_C = 0.2\text{ mA}$	650	-	-	V
$V_{CE(sat)}$	Collector-emitter saturation voltage	$V_{GE} = 15\text{ V}; I_C = 50\text{ A}; T_j = 25\text{ °C}$	-	1.65	2	V
		$V_{GE} = 15\text{ V}; I_C = 50\text{ A}; T_j = 150\text{ °C}$	-	2.05	-	V
V_F	Diode forward voltage	$V_{GE} = 0\text{ V}; I_F = 30\text{ A}; T_j = 25\text{ °C}$	-	1.7	2.4	V
		$V_{GE} = 0\text{ V}; I_F = 30\text{ A}; T_j = 150\text{ °C}$	-	1.55	-	V
$V_{GE(th)}$	Gate-emitter threshold voltage	$I_C = 0.25\text{ mA}; V_{CE} = V_{GE}$	4	5	6	V
I_{CES}	Zero gate voltage collector current	$V_{CE} = 650\text{ V}; V_{GE} = 0\text{ V}; T_j = 25\text{ °C}$	-	-	10	uA
		$V_{CE} = 650\text{ V}; V_{GE} = 0\text{ V}; T_j = 150\text{ °C}$	-	-	2	mA
g_{fs}	Transconductance	$V_{CE} = 20\text{ V}; I_C = 50\text{ A}$	-	50	-	S
Dynamic characteristics						
C_{ies}	Input capacitance	$V_{CE} = 25\text{ V}; V_{GE} = 0\text{ V}; f = 1\text{ MHz}; T_j = 25\text{ °C}$	-	3800	-	pF
C_{oes}	Output capacitance		-	130	-	pF
C_{res}	Reverse transfer capacitance		-	70	-	pF
Q_G	Gate charge	$V_{CC} = 520\text{ V}; I_C = 50\text{ A}; V_{GE} = 15\text{ V}; T_j = 25\text{ °C}$	-	160	-	nC

11. Switching Characteristics

Table 8. Switching Characteristics, Inductive Load

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
IGBT characteristics						
$t_{d(on)}$	Turn-on delay time	$T_J = 25\text{ }^\circ\text{C};$ $V_{CC} = 400\text{ V}; I_C = 50\text{ A}; V_{GE} = 15\text{ V} / 0\text{ V};$ $R_G = 10\text{ } \Omega;$ Energy losses include "tail" and diode reverse recovery	-	66	-	nS
t_r	Rise time		-	61	-	nS
$t_{d(off)}$	Turn-off delay time		-	163	-	nS
t_f	Fall time		-	76	-	nS
E_{on}	Turn-on energy		-	1.7	-	mJ
E_{off}	Turn-off energy		-	0.6	-	mJ
E_{ts}	Total switching energy		-	2.3	-	mJ
$t_{d(on)}$	Turn-on delay time	$T_J = 150\text{ }^\circ\text{C};$ $V_{CC} = 400\text{ V}; I_C = 50\text{ A}; V_{GE} = 15\text{ V} / 0\text{ V};$ $R_G = 10\text{ } \Omega;$ Energy losses include "tail" and diode reverse recovery	-	62	-	nS
t_r	Rise time		-	61	-	nS
$t_{d(off)}$	Turn-off delay time		-	170	-	nS
t_f	Fall time		-	95	-	nS
E_{on}	Turn-on energy		-	1.9	-	mJ
E_{off}	Turn-off energy		-	0.8	-	mJ
E_{ts}	Total switching energy		-	2.7	-	mJ
Diode characteristics						
t_{rr}	Reverse recovery time	$T_J = 25\text{ }^\circ\text{C};$ $V_R = 400\text{ V}; I_F = 30\text{ A}; dI_F/dt = 500\text{ A/us}$	-	105	-	nS
Q_r	Reverse recovery charge		-	570	-	nC
I_{RM}	Reverse recovery peak current		-	11	-	A
t_{rr}	Reverse recovery time	$T_J = 150\text{ }^\circ\text{C};$ $V_R = 400\text{ V}; I_F = 30\text{ A}; dI_F/dt = 500\text{ A/us}$	-	127	-	nS
Q_r	Reverse recovery charge		-	1265	-	nC
I_{RM}	Reverse recovery peak current		-	17	-	A

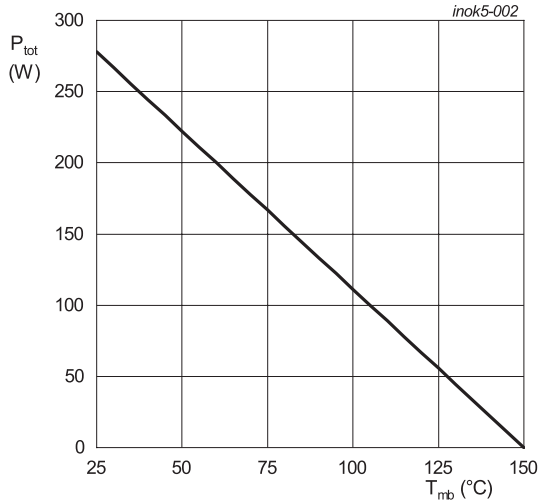


Fig. 2. Power dissipation as a function of case temperature

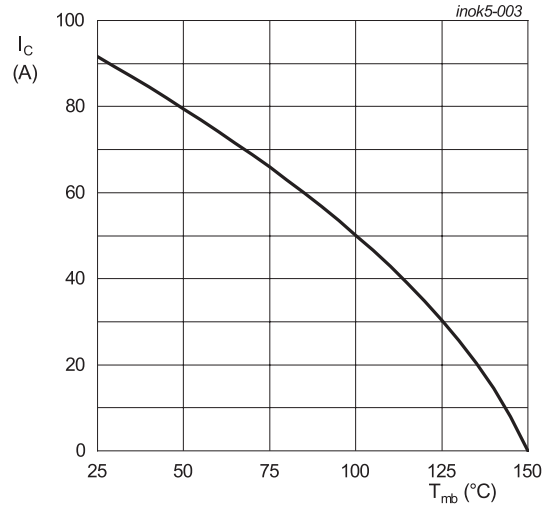


Fig. 3. Collector current as a function of case temperature
 $V_{GE} = 15 \text{ V}; T_j = 150 \text{ °C}$

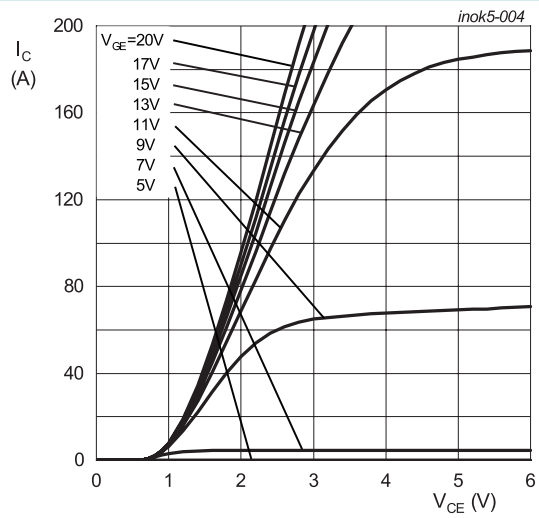


Fig. 4. Typical output characteristic
 $T_j = 25 \text{ °C}$

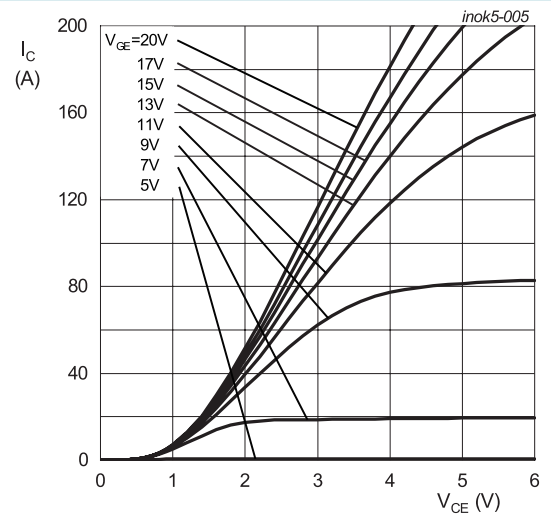
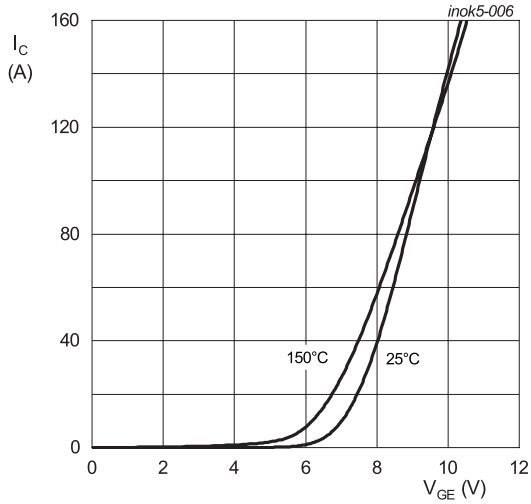
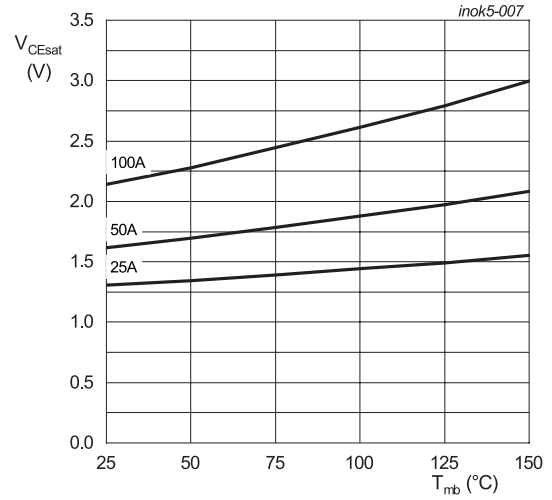


Fig. 5. Typical output characteristic
 $T_j = 150 \text{ °C}$



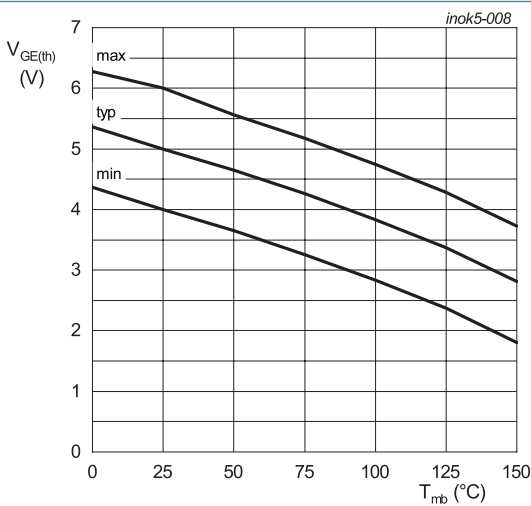
$V_{CE} = 20\text{ V}$

Fig. 6. Typical transfer characteristic



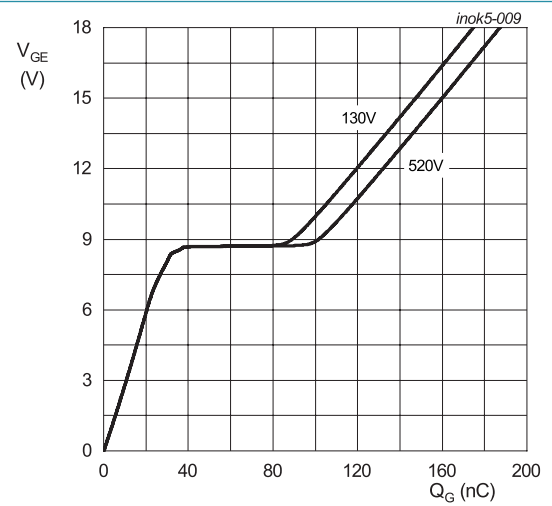
$V_{GE} = 15\text{ V}$

Fig. 7. Typical collector-emitter saturation voltage as a function of junction temperature



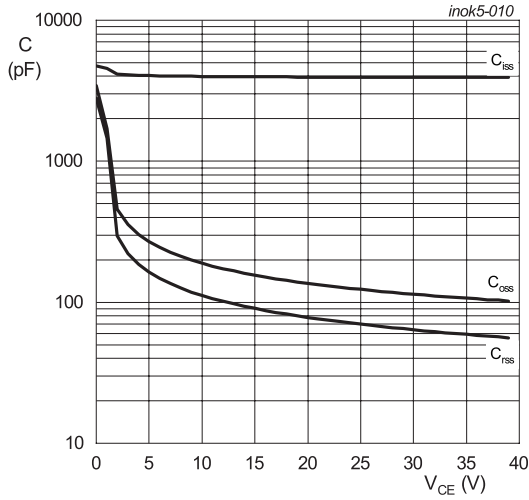
$I_C = 0.5\text{ mA}$

Fig. 8. Gate-emitter threshold voltage as a function of junction temperature



$I_C = 50\text{ A}$

Fig. 9. Typical gate charge



$V_{GE} = 0 \text{ V}; f = 1 \text{ MHz}$

Fig. 10. Typical capacitance as a function of collector-emitter voltage

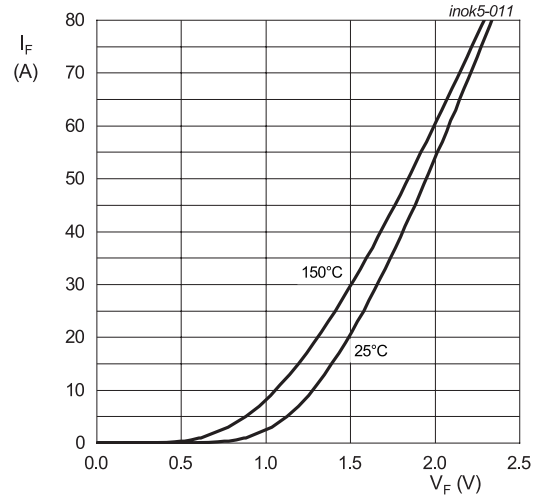
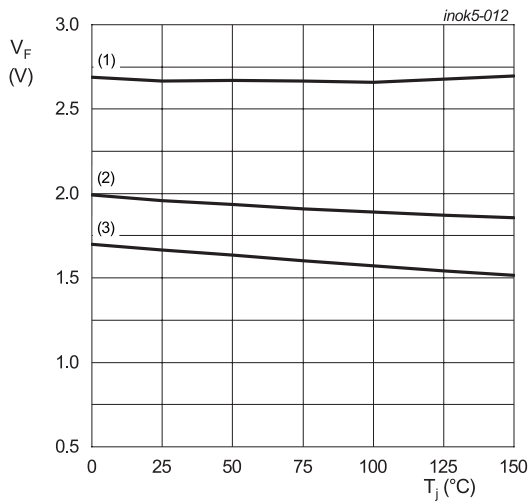
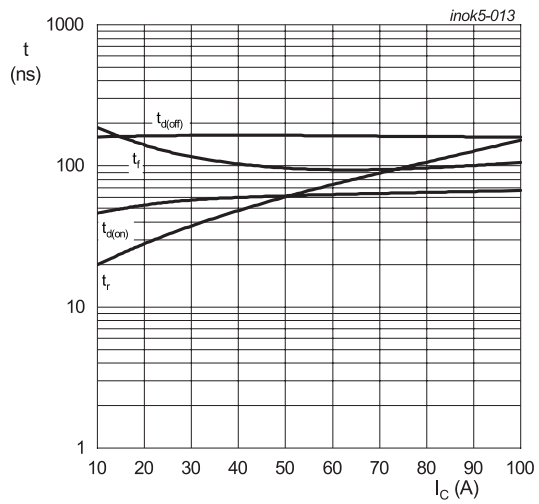


Fig. 11. Typical diode forward current as a function of forward voltage



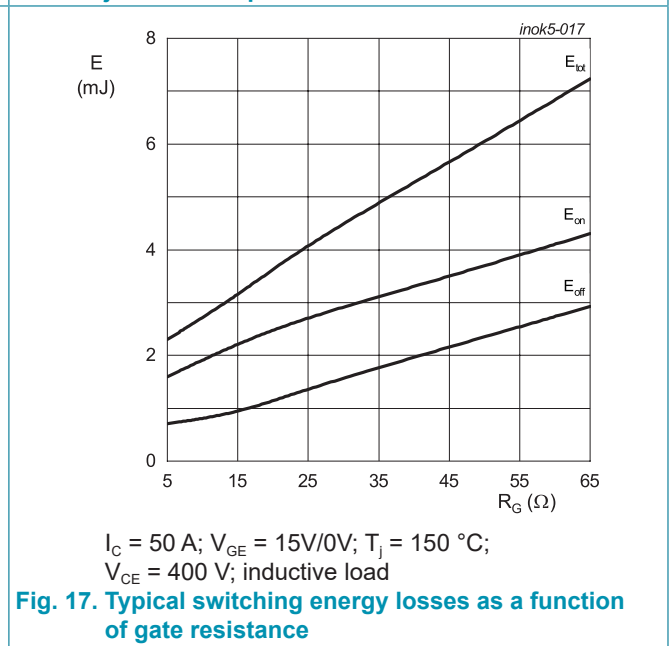
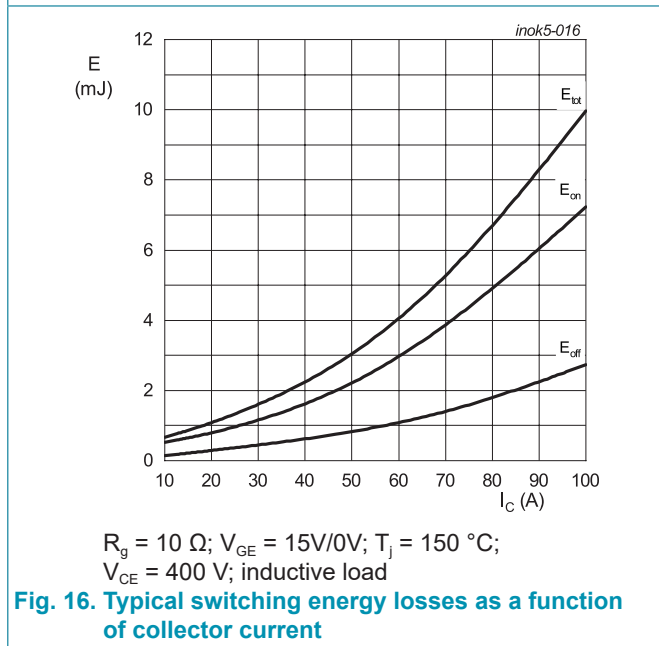
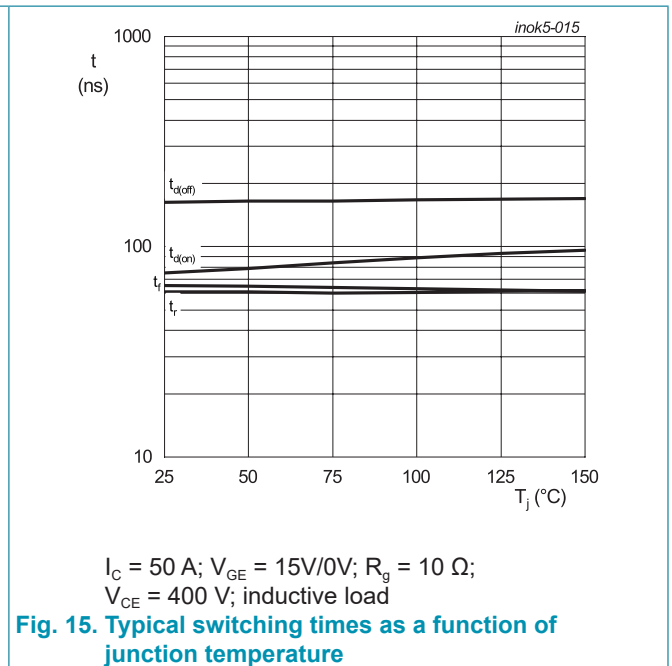
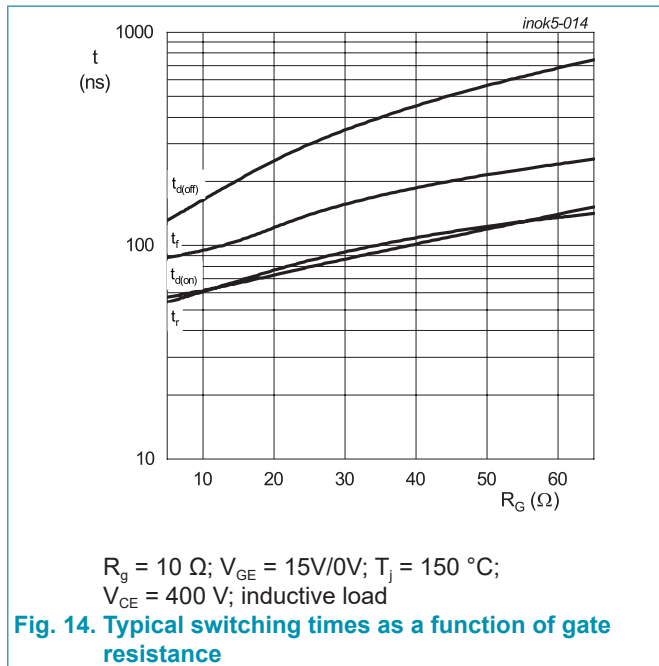
- (1) $I_F = 100 \text{ A}$
- (2) $I_F = 50 \text{ A}$
- (3) $I_F = 30 \text{ A}$

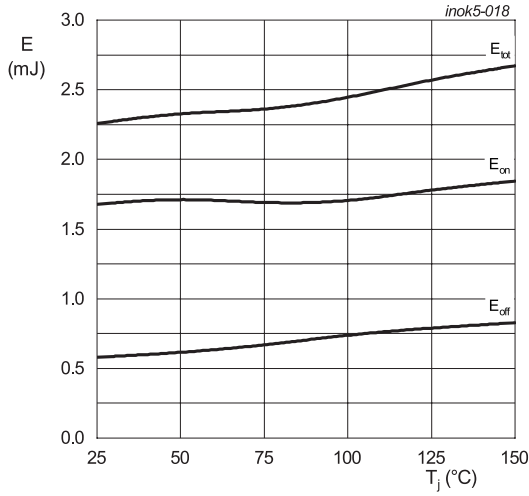
Fig. 12. Typical diode forward voltage as a function of junction temperature



$R_g = 10 \Omega; V_{GE} = 15\text{V}/0\text{V}; T_j = 150 \text{ }^\circ\text{C};$
 $V_{CE} = 400 \text{ V};$ inductive load

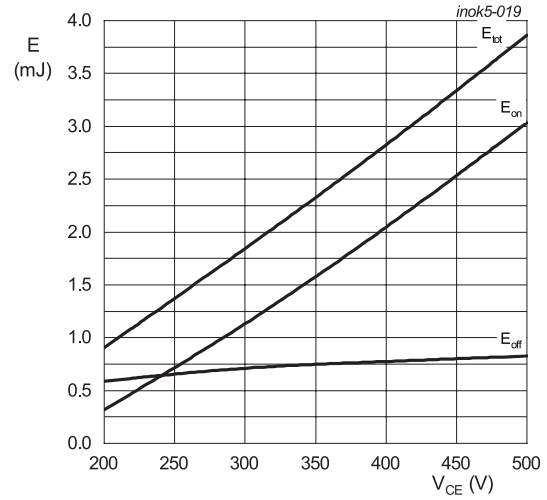
Fig. 13. Typical switching times as a function of collector current





$I_C = 50 \text{ A}$; $V_{CE} = 15\text{V}/0\text{V}$; $R_g = 10 \text{ }\Omega$;
 $V_{CE} = 400 \text{ V}$; inductive load

Fig. 18. Typical switching energy losses as a function of junction temperature



$I_C = 50 \text{ A}$; $V_{CE} = 15\text{V}/0\text{V}$; $T_j = 150 \text{ }^\circ\text{C}$;
 $R_g = 10 \text{ }\Omega$; inductive load

Fig. 19. Typical switching energy losses as a function of collector emitter voltage

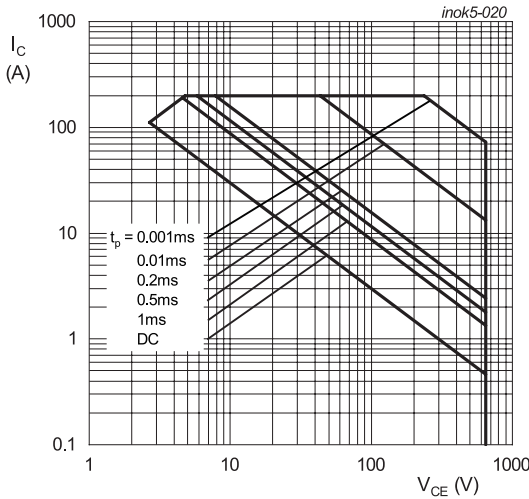


Fig. 20. Forward bias safe operating area

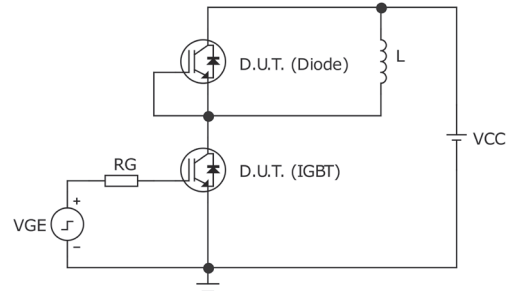


Fig. 21. Test circuit for inductive load switching

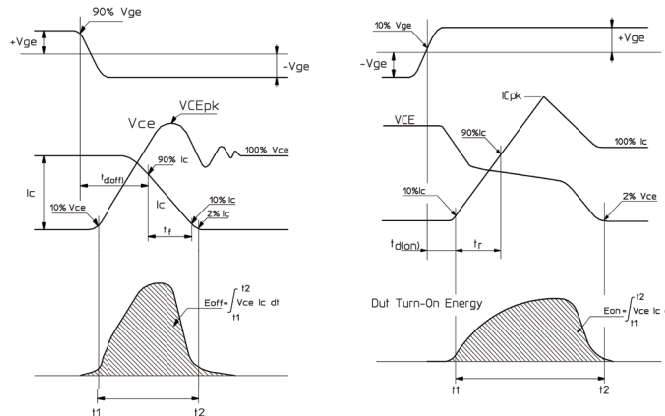
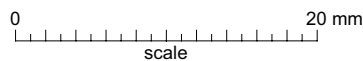
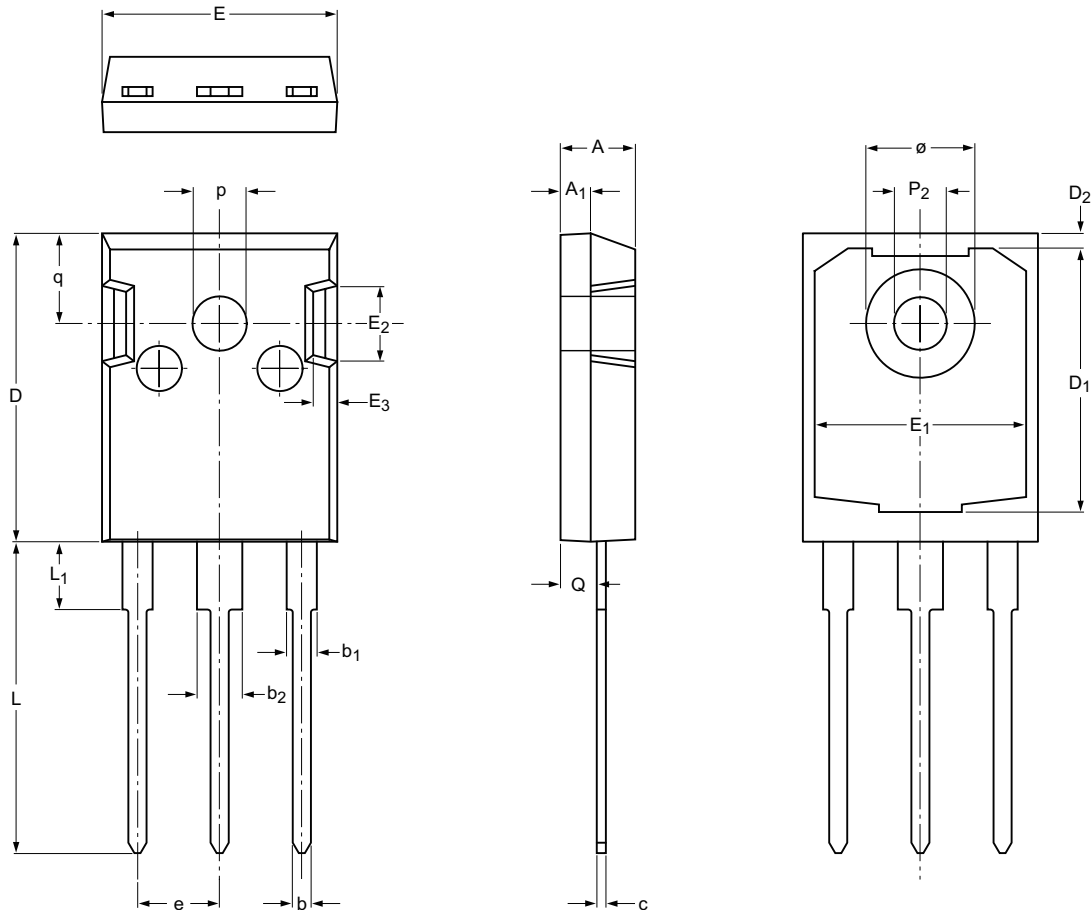


Fig. 22. Definition of switching times and losses

12. Package outline

Plastic single-ended through-hole package; heatsink mounted; 1 mounting hole; 3-lead TO-247

SOT429



Dimensions (mm are the original dimensions)

Unit ⁽¹⁾	A	A ₁	b	b ₁	b ₂	c	D	D ₁	D ₂	E	E ₁	E ₂	E ₃	e ⁽¹⁾	L	L ₁	P ₂	p	Q	q	ø	
max	5.20	2.10	1.40	2.20	3.20	0.70	20.6	17.68	1.20	15.75	14.22	5.20	1.80		20.90	4.75	3.60	3.70	2.60	6.18	7.30	
nom														5.45								
min	4.70	1.90	1.00	1.80	2.80	0.50	20.3	17.28	0.80	15.45	13.82	4.80	1.40		20.40	4.25	3.40	3.50	2.20	5.78	7.10	

Note

1. Basic spacing between centers.

sot429_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT429		TO-247				04-09-14 13-03-25

13. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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14. Contents

1. General description.....	1
2. Features and benefits	1
3. Applications	1
4. Quick reference data	1
5. Pinning information.....	2
6. Ordering information.....	2
7. Marking.....	2
8. Limiting values	3
9. Thermal characteristics	4
10. Characteristics.....	5
11. Switching Characteristics.....	6
12. Package outline	12
13. Legal information	13
14. Contents	14

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