



**THE DATASHEET OF
DMT10H009LK3-13**



Product Summary

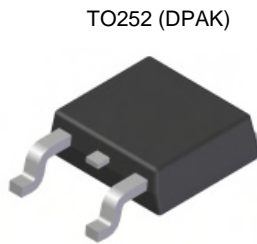
BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C
100V	9mΩ @ V _{GS} = 10V	90A
	13mΩ @ V _{GS} = 4.5V	76A

Description

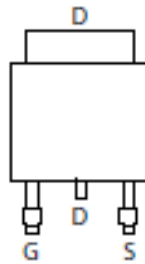
This new generation MOSFET features low on-resistance and fast switching, making it ideal for high efficiency power management applications.

Applications

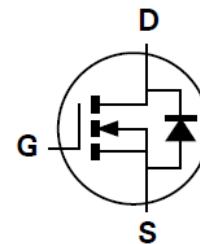
- Power Management Functions
- DC-DC Converters
- Backlighting



Top View



Pin Out
Top View



Equivalent Circuit

Features

- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- Low R_{DS(ON)} – Minimizes Power Losses
- Low Q_g – Minimizes Switching Losses
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. “Green” Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

Mechanical Data

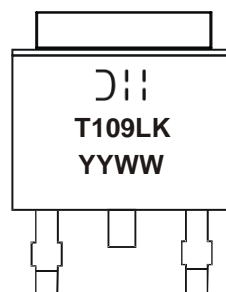
- Case: TO252
- Case Material: Molded Plastic, “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (B3)
- Weight: 0.33 grams (Approximate)

Ordering Information (Note 4)

Part Number	Case	Packaging
DMT10H009LK3-13	TO252 (DPAK)	2,500/Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, “Green” and Lead-free.
 3. Halogen- and Antimony-free “Green” products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



D: = Manufacturer's Marking
 T109LK = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Last Two Digits of Year (ex: 19 = 2019)
 WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	100	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current, V _{GS} = 10V	I _D	T _C = +25°C	90
		T _C = +70°C	73
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)	I _{DM}	360	A
Maximum Continuous Body Diode Forward Current (Note 6)	I _S	91	A
Pulsed Body Diode Forward Current (10μs Pulse, Duty Cycle = 1%)	I _{SM}	360	A
Avalanche Current, L = 0.3mH	I _{AS}	21	A
Avalanche Energy, L = 0.3mH	E _{AS}	66.2	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	1.7	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	71	°C/W
Total Power Dissipation (Note 6)	P _D	3	W
Thermal Resistance, Junction to Ambient (Note 6)	R _{θJA}	40	°C/W
Thermal Resistance, Junction to Case	R _{θJC}	1.1	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	100	—	—	V	V _{GS} = 0V, I _D = 1mA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 80V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	1.2	—	2.5	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	6.7	9	mΩ	V _{GS} = 10V, I _D = 20A
		—	9.1	13		V _{GS} = 4.5V, I _D = 5A
Diode Forward Voltage	V _{SD}	—	0.8	1.3	V	V _{GS} = 0V, I _S = 20A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iSS}	—	2309	—	pF	V _{DS} = 50V, V _{GS} = 0V f = 1MHz
Output Capacitance	C _{oss}	—	536	—		
Reverse Transfer Capacitance	C _{rSS}	—	15.7	—		
Gate Resistance	R _g	—	1.9	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge	Q _g	—	20	—	nC	V _{DD} = 50V, I _D = 20A, V _{GS} = 4.5V
Gate-Source Charge	Q _{gs}	—	7.0	—		
Gate-Drain Charge	Q _{gd}	—	8.5	—		
Turn-On Delay Time	t _{D(ON)}	—	5.4	—	ns	V _{DD} = 50V, V _{GS} = 10V, I _D = 20A, R _g = 3Ω
Turn-On Rise Time	t _r	—	10.6	—		
Turn-Off Delay Time	t _{D(OFF)}	—	28.3	—		
Turn-Off Fall Time	t _f	—	14.9	—		
Reverse Recovery Time	t _{RR}	—	44.3	—	ns	I _F = 20A, di/dt = 100A/μs
Reverse Recovery Charge	Q _{RR}	—	65.5	—	nC	

- Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

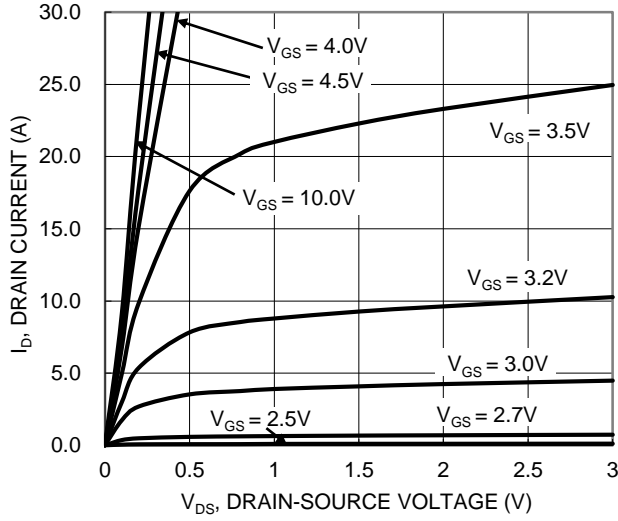


Figure 1. Typical Output Characteristic

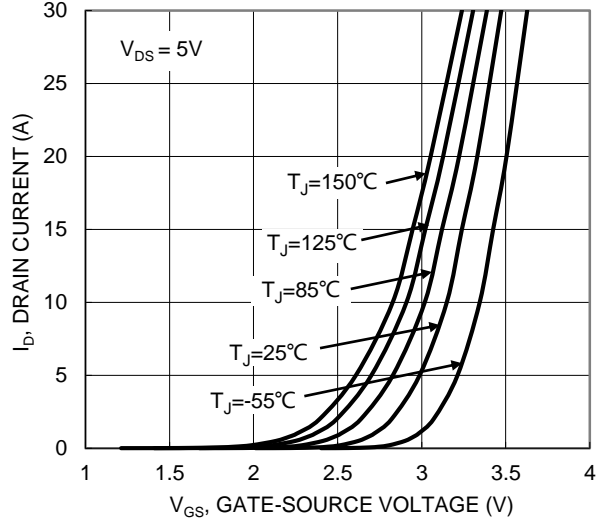


Figure 2. Typical Transfer Characteristic

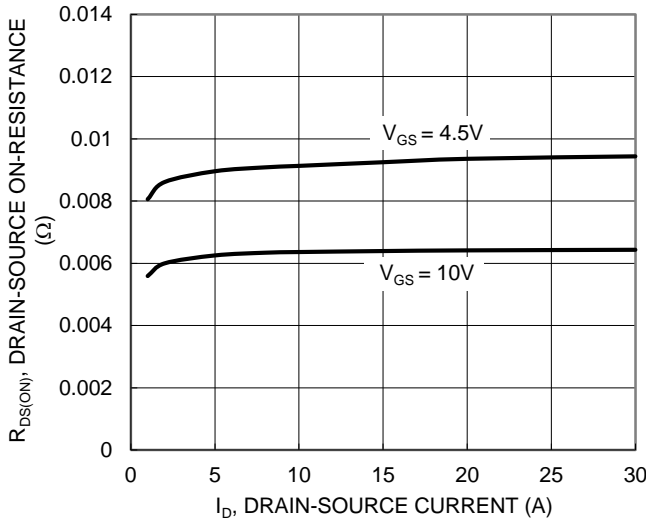


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

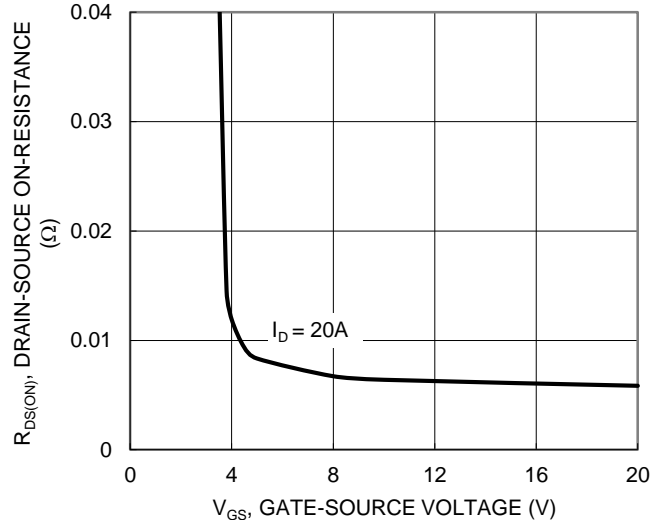


Figure 4. Typical Transfer Characteristic

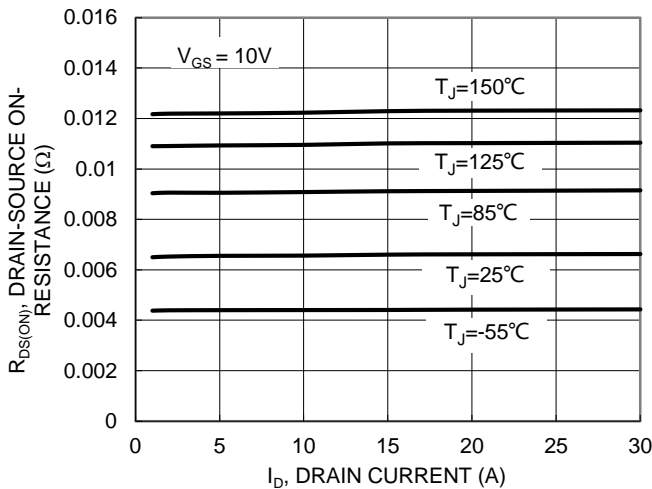


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

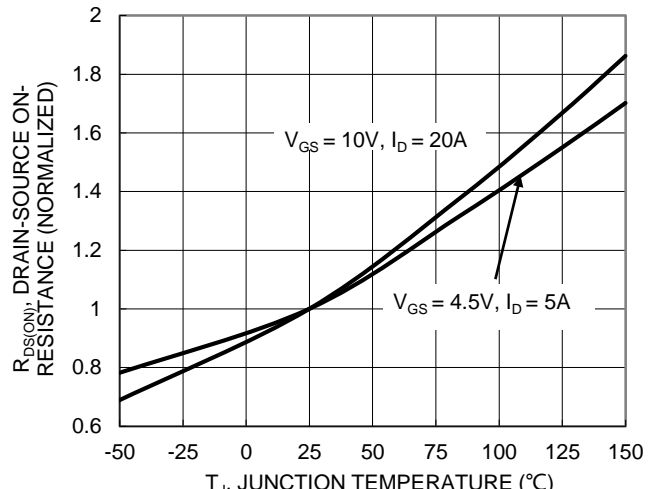


Figure 6. On-Resistance Variation with Junction Temperature

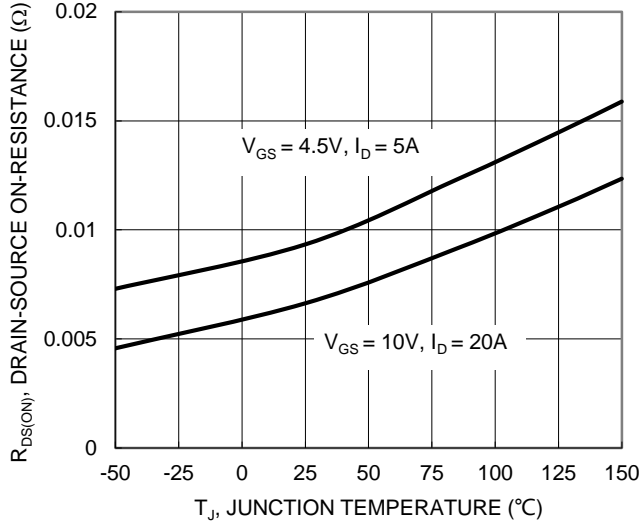


Figure 7. On-Resistance Variation with Junction Temperature

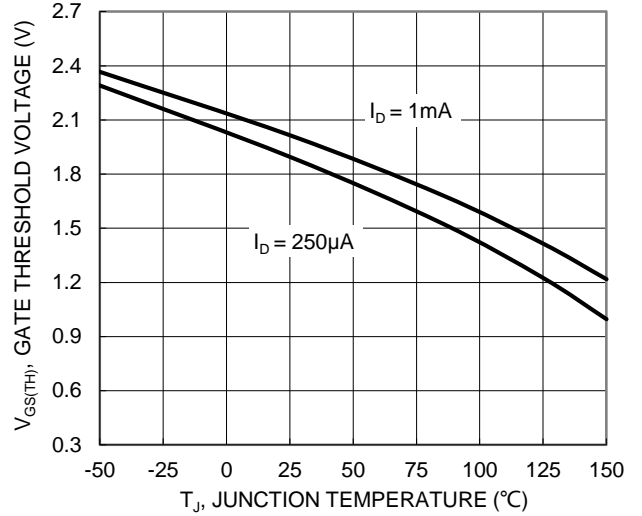


Figure 8. Gate Threshold Variation vs. Junction Temperature

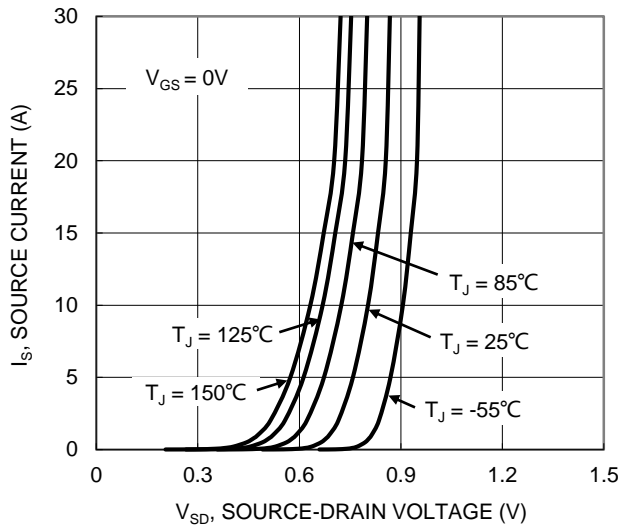


Figure 9. Diode Forward Voltage vs. Current

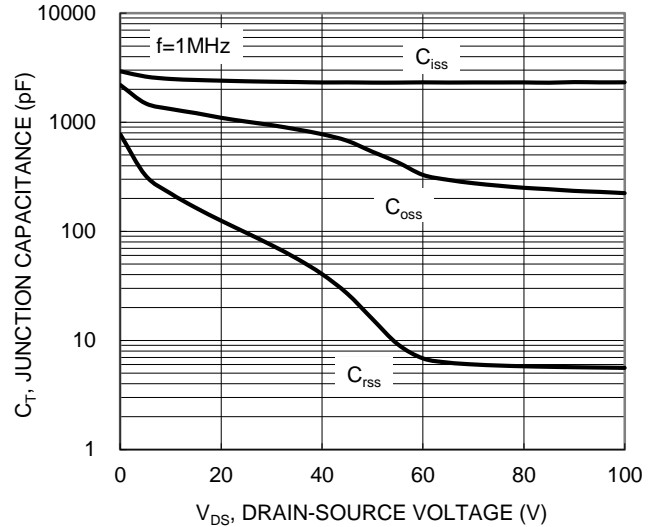


Figure 10. Typical Junction Capacitance

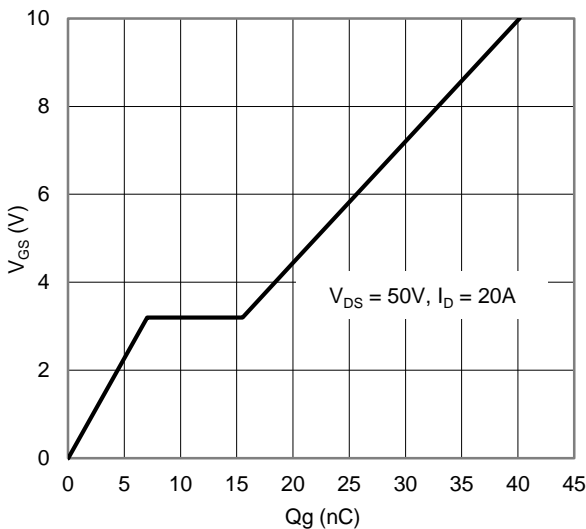


Figure 11. Gate Charge

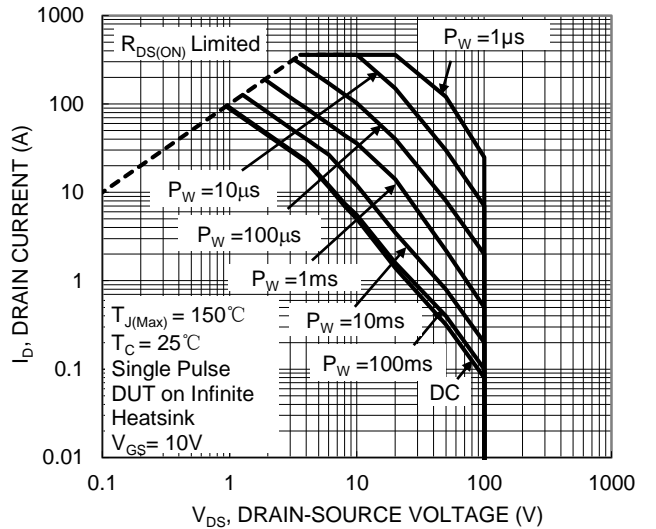


Figure 12. SOA, Safe Operation Area

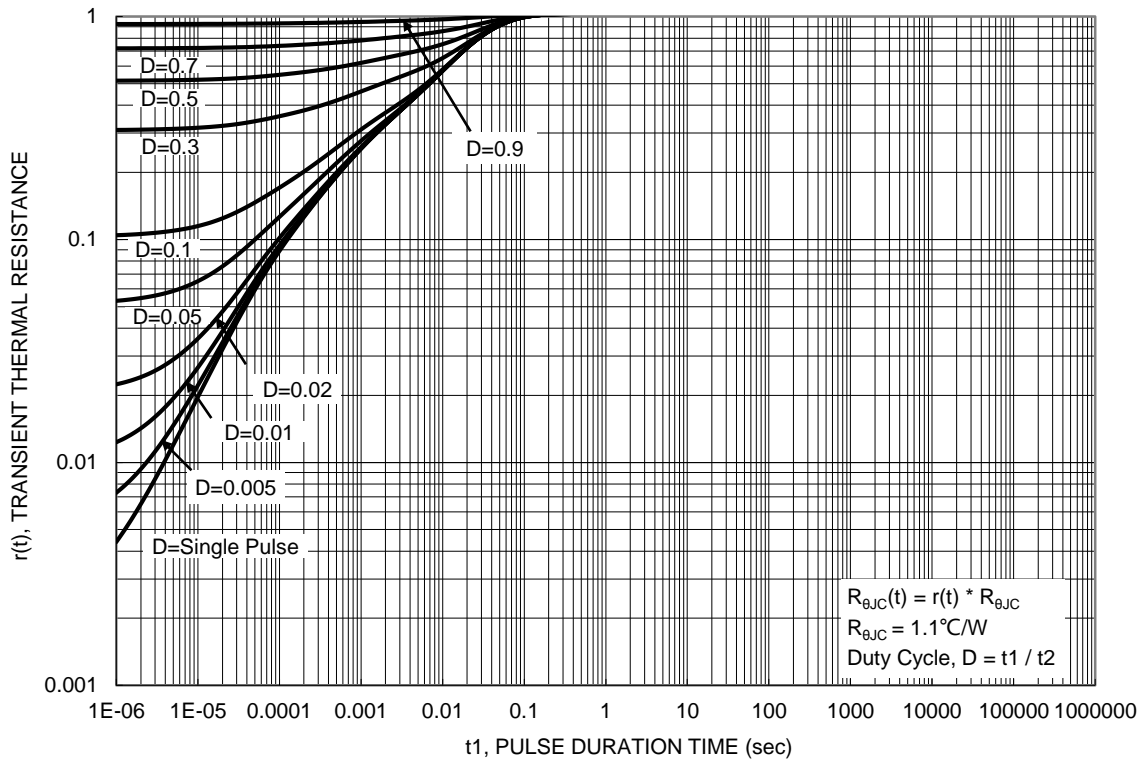
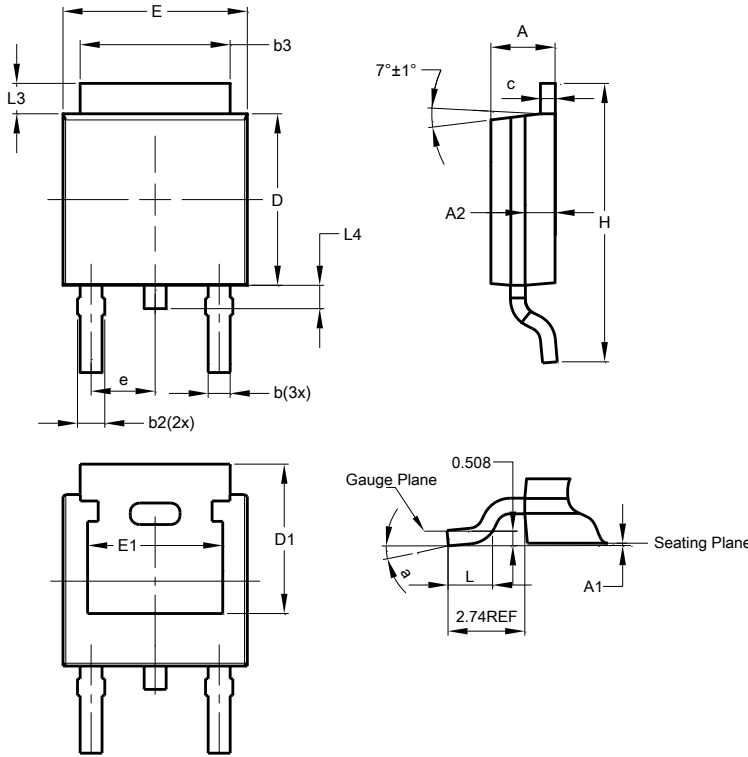


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

TO252 (DPAK)

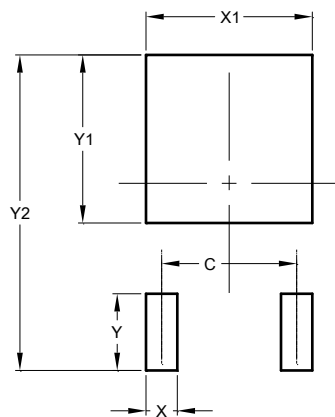


TO252 (DPAK)			
Dim	Min	Max	Typ
A	2.19	2.39	2.29
A1	0.00	0.13	0.08
A2	0.97	1.17	1.07
b	0.64	0.88	0.783
b2	0.76	1.14	0.95
b3	5.21	5.46	5.33
c	0.45	0.58	0.531
D	6.00	6.20	6.10
D1	5.21	-	-
e	-	-	2.286
E	6.45	6.70	6.58
E1	4.32	-	-
H	9.40	10.41	9.91
L	1.40	1.78	1.59
L3	0.88	1.27	1.08
L4	0.64	1.02	0.83
a	0°	10°	-
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

TO252 (DPAK)



Dimensions	Value (in mm)
C	4.572
X	1.060
X1	5.632
Y	2.600
Y1	5.700
Y2	10.700

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