



# THE DATASHEET OF ERA-8SM+



# Monolithic Amplifier

DC-2 GHz

## Product Features

- DC-2 GHz
- Single Voltage Supply
- Internally Matched to 50 Ohms
- Unconditionally Stable
- Low Performance Variation Over Temperature
- Transient Protected
- Aqueous washable
- Protected By US Patent 6,943,629
- Low Additive Phase Noise



Generic photo used for illustration purposes only

## ERA-8SM+

CASE STYLE: WW107

**+RoHS Compliant**

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

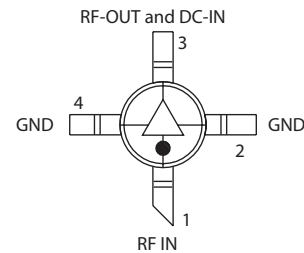
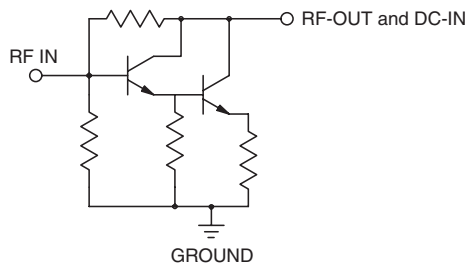
## Typical Applications

- Cellular/ PCS/ 3G Base Station
- CATV, Cable Modem & DBS
- Fixed Wireless & WLAN
- Microwave Radio & Test Equipment

## General Description

ERA-8SM+ (RoHS compliant) is a wideband amplifier offering high dynamic range. It has repeatable performance from lot to lot. It is enclosed in a Micro-X package. ERA-8SM+ uses Darlington configuration and is fabricated using InGaP HBT technology. Expected MTTF is 13,000 years at 85°C case temperature.

## simplified schematic and pin description



Function	Pin Number	Description
RF IN	1	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
RF-OUT and DC-IN	3	RF output and bias pin. DC voltage is present on this pin; therefore a DC blocking capacitor is necessary for proper operation. An RF choke is needed to feed DC bias without loss of RF signal due to the bias connection, as shown in "Recommended Application Circuit".
GND	2,4	Connections to ground. Use via holes as shown in "Suggested Layout for PCB Design" to reduce ground path inductance for best performance.

### Notes

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.  
 B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.  
 C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at [www.minicircuits.com/MCLStore/terms.jsp](http://www.minicircuits.com/MCLStore/terms.jsp)



Electrical Specifications at 25°C and 36mA, unless noted

Parameter		Min.	Typ.	Max.	Units	Cpk
Frequency Range*		DC		2	GHz	
Gain	f=0.1 GHz	29.3	31.5	32.3	dB	≥ 1.5
	f=1 GHz	22.9	24.4	25.9		
	f=2 GHz	—	19	—		
	f=3 GHz	—	15	—		
	f=4 GHz	—	12	—		
Magnitude of Gain Variation versus Temperature (values are negative)	f=0.1 GHz	—	.0079	.016	dB/°C	
	f=1 GHz	—	.0071	.016		
	f=2 GHz	—	.0076	.016		
	f=3 GHz	—	.0089			
	f=4 GHz	—	.0095			
Input Return Loss	f=0.1 GHz		14.5		dB	
	f=1 GHz		16			
	f=2 GHz		15			
	f=3 GHz		13			
	f=4 GHz		10			
Output Return Loss	f=0.1 GHz		12		dB	
	f=1 GHz		13.5			
	f=2 GHz		12			
	f=3 GHz		10			
	f=4 GHz		8			
Reverse Isolation	f=2 GHz	20	23	—	dB	
Output Power @ 1 dB compression	f=0.1 GHz	—	12.5	—	dBm	≥ 1.33
	f=1 GHz	10	12.5	—		
	f=2 GHz	—	10.5	—		
	f=3 GHz	—	7.5	—		
Saturated Output Power (at 3dB compression)	f=0.1 GHz		14.9		dBm	
	f=1 GHz		13.7			
	f=2 GHz		11.9			
	f=3 GHz		8.7			
Output IP3	f=0.1 GHz	24	27	—	dBm	≥ 1.33
	f=1 GHz	22	25	—		
	f=2 GHz	18	21.5	—		
	f=3 GHz	—	18	—		
Noise Figure	f=0.1 GHz	—	2.2	3	dB	
	f=1 GHz	—	2.8	3.8		
	f=2 GHz	—	3	4		
Group Delay	f=1 GHz		130		psec	
Recommended Device Operating Current			36		mA	
Device Operating Voltage		3.5	3.7	3.9	V	≥ 1.5
Device Voltage Variation vs. Temperature at 36mA			-0.5		mV/°C	
Device Voltage Variation vs. Current at 25°C			6.4		mV/mA	
Thermal Resistance, junction-to-case <sup>1</sup>			140		°C/W	

\*Guaranteed specification DC-2 GHz. Low frequency cut off determined by external coupling capacitors.

Absolute Maximum Ratings

Parameter	Ratings
Operating Temperature*	-45°C to 85°C
Storage Temperature	-65°C to 150°C
Operating Current	65mA
Power Dissipation	250mW
Input Power	13dBm

Note: Permanent damage may occur if any of these limits are exceeded. These ratings are not intended for continuous normal operation.

<sup>1</sup>Case is defined as ground leads.

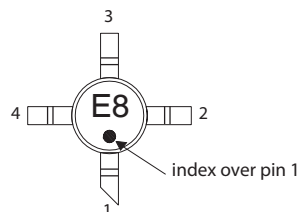
\*Based on typical case temperature rise 5°C above ambient.

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Product Marking



Markings in addition to model number designation may appear for internal quality control purposes.

Additional Detailed Technical Information

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Performance data, graphs, s-parameter data set (.zip file)

Case Style: WW107

Plastic micro-x, .085 body diameter, lead finish: matte-tin

Tape & Reel: F4

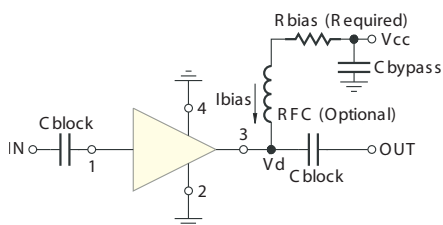
7" Reels with 20, 50, 100, 200, 500, 1K devices

Suggested Layout for PCB Design: PL-075

Evaluation Board: TB-408-8+

Environmental Ratings: ENV08T2

Recommended Application Circuit



Test Board includes case, connectors, and components (in bold) soldered to PCB

R BIAS	
Vcc	"1%" Res. Values (ohms) for Optimum Biasing
7	88.7
8	118
9	143
10	174
11	200
12	232
13	255
14	280
15	309
16	340
17	365
18	392
19	422
20	453

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**ESD Rating**

Human Body Model (HBM): Class 1B (500 v to < 1,000 v) in accordance with ANSI/ESD STM 5.1 - 2001

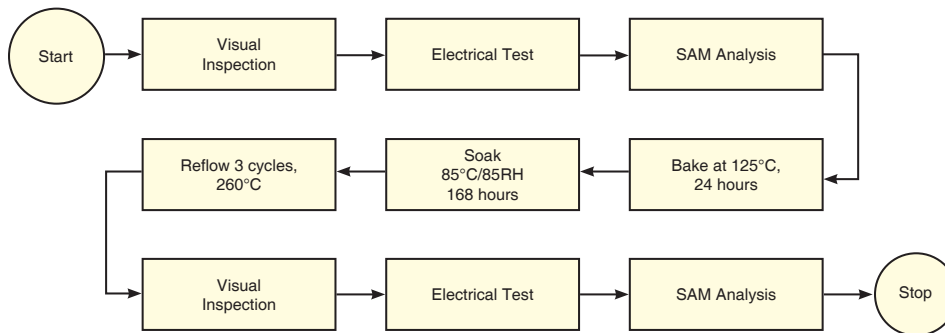
Machine Model (MM): Class M1 (< 100 v) in accordance with ANSI/ESD STM 5.2 - 1999

**MSL Rating**

Moisture Sensitivity: MSL1 in accordance with IPC/JEDECJ-STD-020C

No.	Test Required	Condition	Standard	Quantity
1	Visual Inspection	Low Power Microscope Magnification 40x	MIP-IN-0003 (MCT spec)	45 units
2	Electrical Test	Room Temperature	SCD (MCL spec)	45 units
3	SAM Analysis	Less than 10% growth in term of delamination	J-Std-020C (Jedec Standard)	45 units
4	Moisture Sensitivity Level 1	Bake at 125°C for 24 hours Soak at 85°C/85%RH for 168 hours Reflow 3 cycles at 260°C peak	J-Std-020C (Jedec Standard)	45 units

**MSL Test Flow Chart**



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