

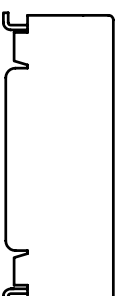
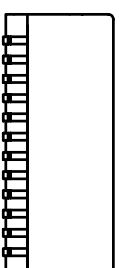
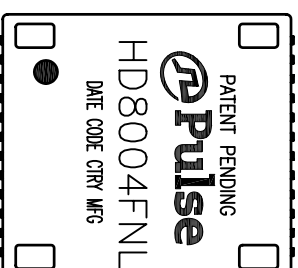


THE DATASHEET OF HD8004FNLT



) WITH FLAMMABILITY
) +125°C

ITY AT 245°C REFLOW PROFILE
 O MOISTURE SENSITIVE
 MAXIMUM THROUGH REFLOW SOLDER
 NG ADD A "T" SUFFIX
 XNL BECOMES HXXXXXXXXNLT).



FINAL OUTLINE

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TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
HD8004FNL-10	PS-2746.002-A	1 OF 3	HD8004FNL	A

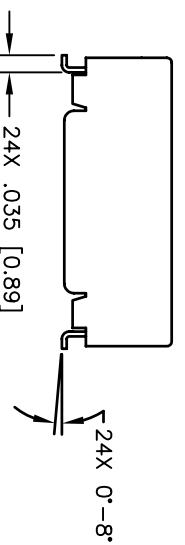
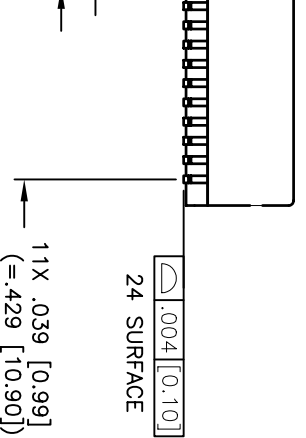
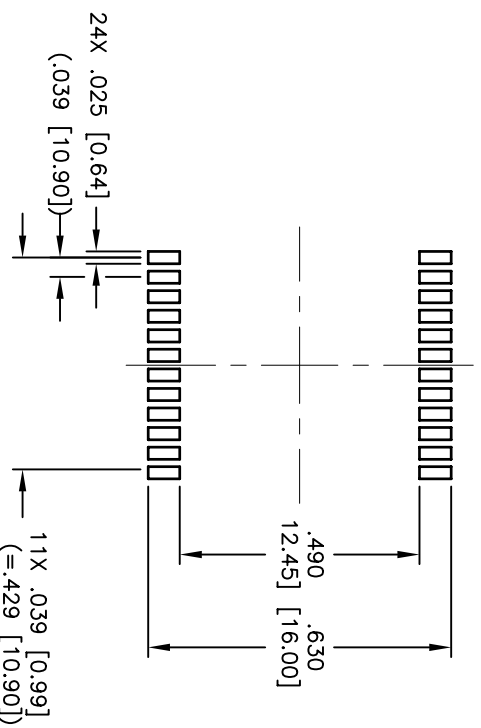
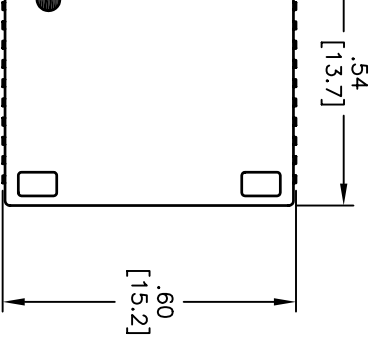
ELECTRICAL CHARACTERISTICS AT +25°C UNLESS OTHER SPECIFIED
MEETS IEEE 802.3 SPECIFICATION

PARAMETER	SPECIFICATIONS	
OPERATING TEMPERATURE	0°C TO 70°C	
TURNS RATIO	1.00 ± 2%	
POLARITY	PER SCHEMATIC	
INSERTION LOSS (SDD21)	100 KHz	1-400 MHz
	-3.0 dB MAX	-1.5 dB MAX (-0.8 dB TYP)
PHY-SIDE RETURN LOSS (SDD11 - Zref = 100 OHMS ± 1%)	1 MHz-40 MHz	100 MHz-400 MHz
	-20 dB MIN	-20+10*LOG ₁₀ (f/40) dB MIN
LINE-SIDE RETURN LOSS (SDD22 - Zref = 100 OHMS ± 1%)	1 MHz - 40 MHz	40 MHz - 400 MHz
	-20 dB MIN	-20+10*LOG ₁₀ (f/40) dB MIN
INDUCTANCE (OCL)	180 uH MIN @ 100 KHz, 100 mV	
CROSSSTALK, ADJACENT CHANNELS (SDD21 - ADJ.)	1-100 MHz	100-400 MHz
	-40 dB MIN	-30 dB MIN
COMMON MODE REJECTION RATIO (SCC21)	1 MHz-100 MHz	100-300 MHz
	-37 dB MIN	300-500 MHz
COMM. TO DIFF. REJECTION RATIO (SDC12)	1 MHz-100 MHz	100-300 MHz
	-40 dB MIN	300-500 MHz
DC CURRENT CARRING CAPABILITY	600 mA MAX AT 57 VDC	
INPUT - OUTPUT ISOLATION	1500 VRMS MIN @ 60 SECONDS	

○ 24	MCT1
○ 23	MX1+
○ 22	MX1-
○ 21	MCT2
○ 20	MX2+
○ 19	MX2-
○ 18	MCT3
○ 17	MX3+
○ 16	MX3-
○ 15	MCT4
○ 14	MX4+
○ 13	MX4-

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TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
ID8004FNL-10	PS-2746.002-A	2 OF 3	HD8004FNL	A



ES WITH THE FOLLOWING

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TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
HD8004FNL-10	PS-2746.002-A	3 OF 3	HD8004FNL	A

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