



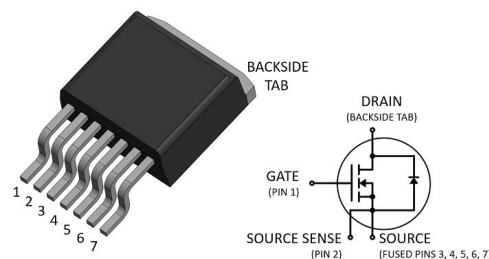
**THE DATASHEET OF  
MSC750SMA170SA**



## 1700 V 750 mΩ SiC N-Channel Power MOSFET

### Product Overview

The silicon carbide (SiC) power MOSFET product line from Microchip increases the performance over silicon MOSFET and silicon IGBT solutions while lowering the total cost of ownership for high-voltage applications. The MSC750SMA170SA device is a 1700 V, 750 mΩ SiC MOSFET in a TO-263 7-lead package with a source sense.



### Features

The following are key features of the MSC750SMA170SA device:

- Low capacitances and low gate charge
- Fast switching speed due to low internal gate resistance (ESR)
- Stable operation at high junction temperature,  $T_{J(max)} = 175\text{ °C}$
- Fast and reliable body diode
- Superior avalanche ruggedness
- RoHS compliant

### Benefits

The following are benefits of the MSC750SMA170SA device:

- High efficiency to enable lighter, more compact system
- Simple to drive and easy to parallel
- Improved thermal capabilities and lower switching losses
- Eliminates the need for external freewheeling diode
- Lower system cost of ownership

### Applications

The MSC750SMA170SA device is designed for the following applications:

- PV inverter, converter, and industrial motor drives
- Smart grid transmission and distribution
- Induction heating and welding
- H/EV powertrain and EV charger
- Power supply and distribution

## 1. Device Specifications

This section shows the specifications of the MSC750SMA170SA device.

### 1.1 Absolute Maximum Ratings

The following table shows the absolute maximum ratings of the MSC750SMA170SA device.

**Table 1-1. Absolute Maximum Ratings**

Symbol	Parameter	Ratings	Unit
$V_{DSS}$	Drain source voltage	1700	V
$I_D$	Continuous drain current at $T_C = 25\text{ }^\circ\text{C}$	6	A
	Continuous drain current at $T_C = 100\text{ }^\circ\text{C}$	5	
$I_{DM}$	Pulsed drain current <sup>1</sup>	12	
$V_{GS}$	Gate-source voltage	23 to -10	V
$P_D$	Total power dissipation at $T_C = 25\text{ }^\circ\text{C}$	64	W
	Linear derating factor	0.425	W/ $^\circ\text{C}$

**Note:**

1. Repetitive rating: pulse width and case temperature limited by maximum junction temperature.

The following table shows the thermal and mechanical characteristics of the MSC750SMA170SA device.

**Table 1-2. Thermal and Mechanical Characteristics**

Symbol	Characteristic/Test Conditions	Min	Typ	Max	Unit
$R_{\theta JC}$	Junction-to-case thermal resistance		1.57	2.35	$^\circ\text{C}/\text{W}$
$T_J$	Operating junction temperature	-55		175	$^\circ\text{C}$
$T_{STG}$	Storage temperature	-55		150	$^\circ\text{C}$
$T_L$	Soldering temperature for 10 seconds (1.6 mm from case)			260	$^\circ\text{C}$
Wt	Package weight		0.05		oz
			1.42		g

### 1.2 Electrical Performance

The following table shows the static characteristics of the MSC750SMA170SA device.  $T_J = 25\text{ }^\circ\text{C}$  unless otherwise specified.

**Table 1-3. Static Characteristics**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$V_{(BR)DSS}$	Drain-source breakdown voltage	$V_{GS} = 0\text{ V}, I_D = 100\text{ }\mu\text{A}$	1700			V
$R_{DS(on)}$	Drain-source on resistance <sup>1</sup>	$V_{GS} = 20\text{ V}, I_D = 2.5\text{ A}$		750	940	m $\Omega$
$V_{GS(th)}$	Gate-source threshold voltage	$V_{GS} = V_{DS}, I_D = 100\text{ }\mu\text{A}$	1.9	3.25		V

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## Device Specifications

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Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$\Delta V_{GS(th)}/\Delta T_J$	Threshold voltage coefficient	$V_{GS} = V_{DS}, I_D = 100 \mu A$		-5.7		mV/°C
$I_{DSS}$	Zero gate voltage drain current	$V_{DS} = 1700 V, V_{GS} = 0 V$			100	$\mu A$
		$V_{DS} = 1700 V, V_{GS} = 0 V, T_J = 125^\circ C$			500	
$I_{GSS}$	Gate-source leakage current	$V_{GS} = 20 V/-10 V$			$\pm 100$	nA

**Note:**

1. Pulse test: pulse width < 380  $\mu s$ , duty cycle < 2%.

The following table shows the dynamic characteristics of the MSC750SMA170SA device.  $T_J = 25^\circ C$  unless otherwise specified.

**Table 1-4. Dynamic Characteristics**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit	
$C_{iss}$	Input capacitance	$V_{GS} = 0 V, V_{DD} = 1360 V, V_{AC} = 25 mV$		184		$\mu F$	
$C_{rSS}$	Reverse transfer capacitance			2			
$C_{oss}$	Output capacitance			14			
$Q_g$	Total gate charge	$V_{GS} = -5 V/20 V, V_{DD} = 1200 V, I_D = 2.5 A$		11		nC	
$Q_{gs}$	Gate-source charge			2.9			
$Q_{gd}$	Gate-drain charge			2.1			
$t_{d(on)}$	Turn-on delay time	$V_{DD} = 1200 V, V_{GS} = -5 V/20 V, I_D = 5 A, R_{g(ext)} = 8 \Omega, \text{Freewheeling diode} = \text{MSC750SMA170SA} (V_{GS} = -5 V); \text{reference Fig. 1-20}$		13		ns	
$t_r$	Voltage rise time			12			
$t_{d(off)}$	Turn-off delay time			7			
$t_f$	Voltage fall time			8			
$E_{on}$	Turn-on switching energy			140			$\mu J$
$E_{off}$	Turn-off switching energy			17			
ESR	Equivalent series resistance	$f = 1 MHz, 25 mV, \text{drain short}$		2.89		$\Omega$	
SCWT	Short circuit withstand time	$V_{DS} = 1200 V, V_{GS} = 20 V$		2.5		$\mu s$	
$E_{AS}$	Avalanche energy, single pulse	$V_{DS} = 150 V, V_{GS} = 20 V, I_D = 2.5 A$		360		mJ	

The following table shows the body diode characteristics of the MSC750SMA170SA device.  $T_J = 25^\circ C$  unless otherwise specified.

**Table 1-5. Body Diode Characteristics**

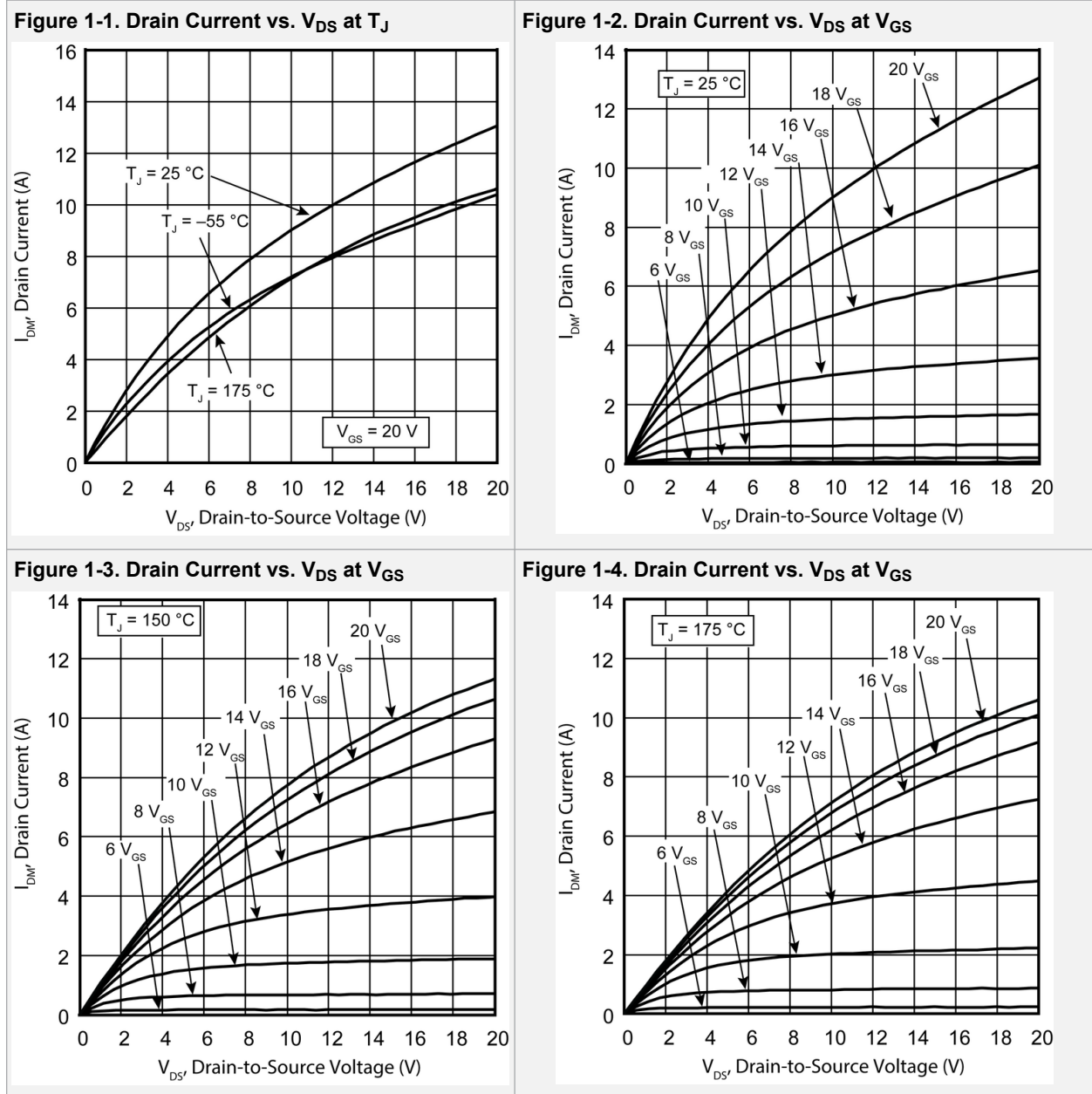
Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$V_{SD}$	Diode forward voltage	$I_{SD} = 2.5 A, V_{GS} = 0 V$		3.8		V
		$I_{SD} = 2.5 A, V_{GS} = -5 V$		3.9		

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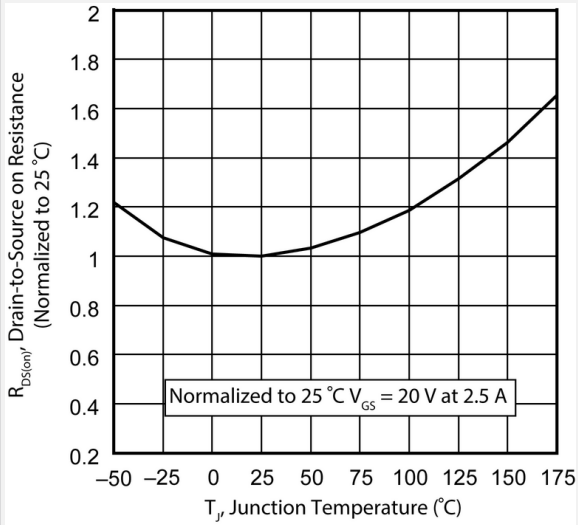
Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$t_{rr}$	Reverse recovery time	$I_{SD} = 5 \text{ A}$ , $V_{GS} = -5 \text{ V}$ , $V_{DD} = 1200 \text{ V}$ , $dI/dt = -4000 \text{ A}/\mu\text{s}$ , Drive $R_g = 8 \Omega$		8.6		ns
$Q_{rr}$	Reverse recovery charge			128		nC
$I_{RRM}$	Reverse recovery current			22		A

### 1.3 Typical Performance Curves

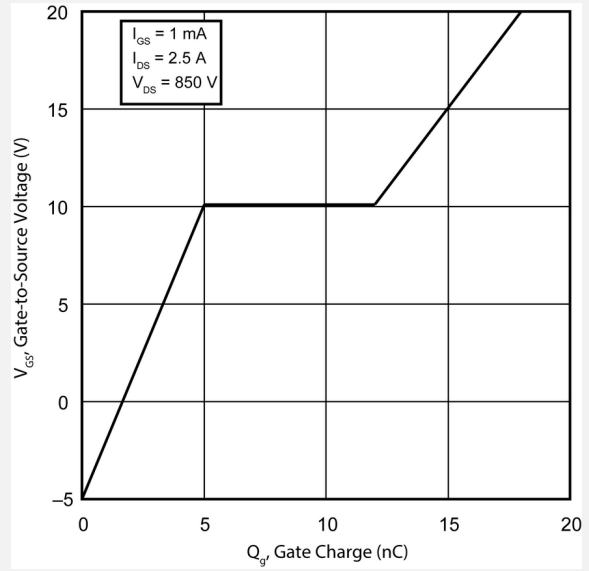
This section shows the typical performance curves of the MSC750SMA170SA device.



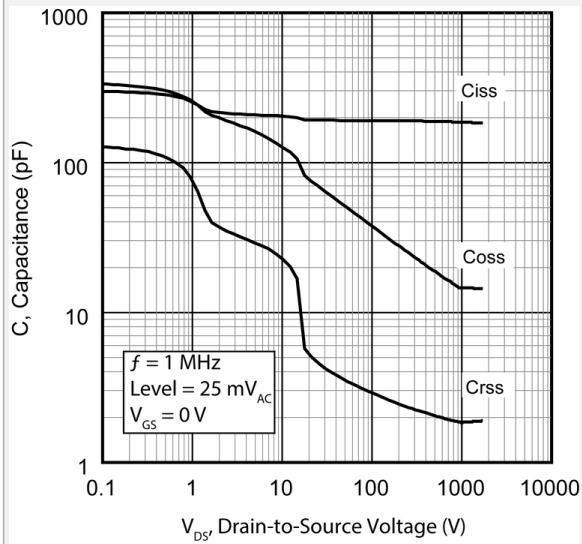
**Figure 1-5.  $R_{DS(on)}$  vs. Junction Temperature**



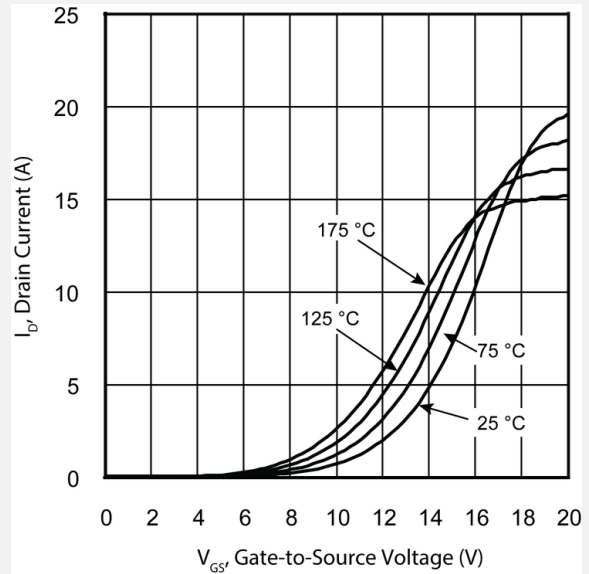
**Figure 1-6. Gate Charge Characteristics**



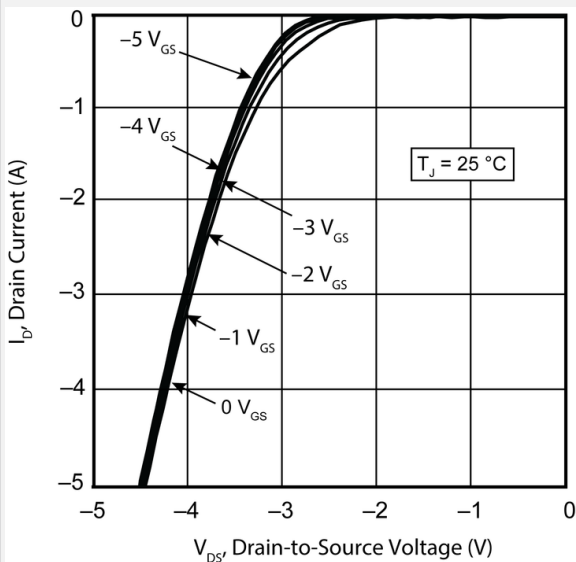
**Figure 1-7. Capacitance vs. Drain-to-Source Voltage**



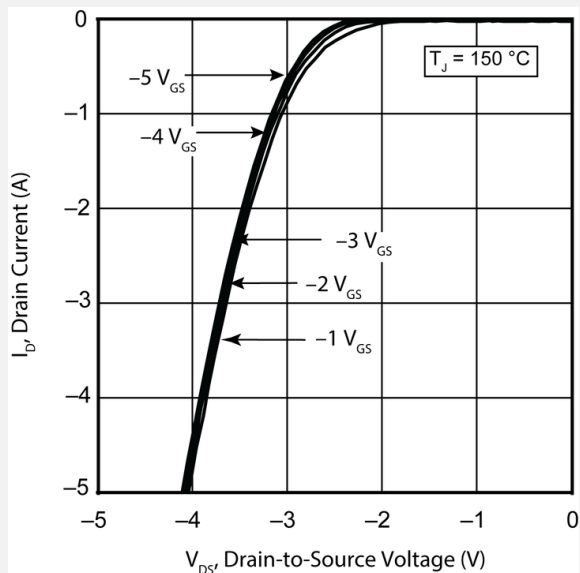
**Figure 1-8.  $I_D$  vs.  $V_{DS}$  3<sup>rd</sup> Quadrant Conduction**



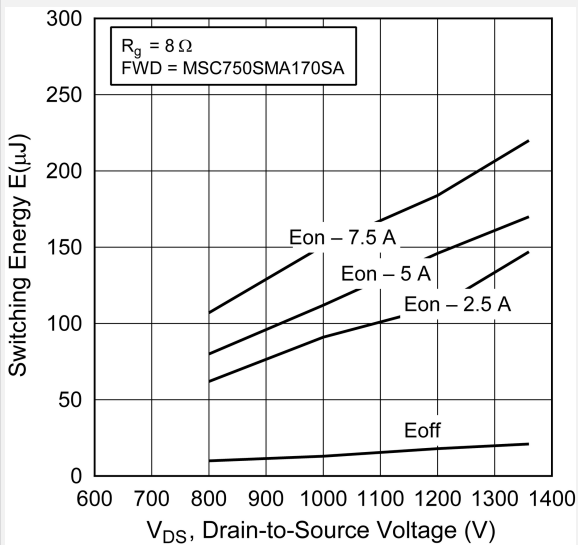
**Figure 1-9.  $I_D$  vs.  $V_{DS}$  3<sup>rd</sup> Quadrant Conduction**



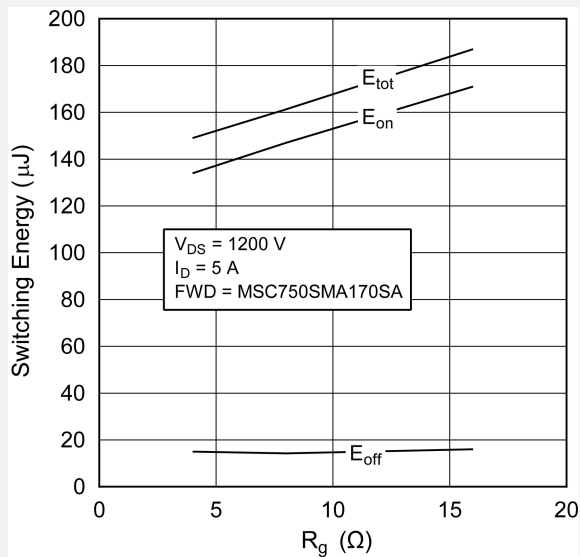
**Figure 1-10.  $I_D$  vs.  $V_{DS}$  3<sup>rd</sup> Quadrant Conduction**



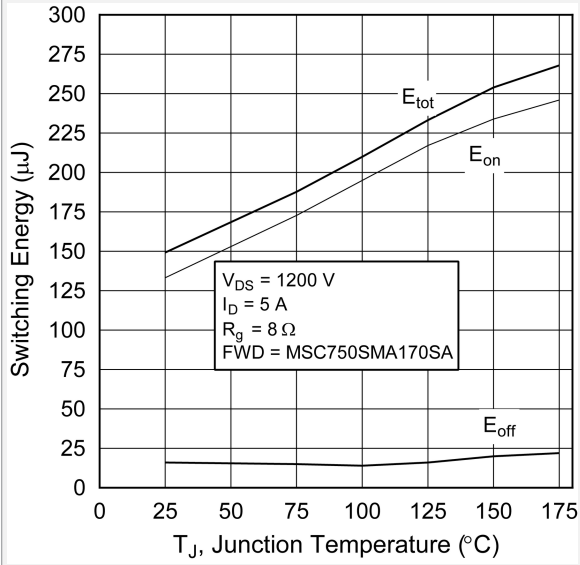
**Figure 1-11. Switching Energy vs.  $V_{DS}$  &  $I_D$**



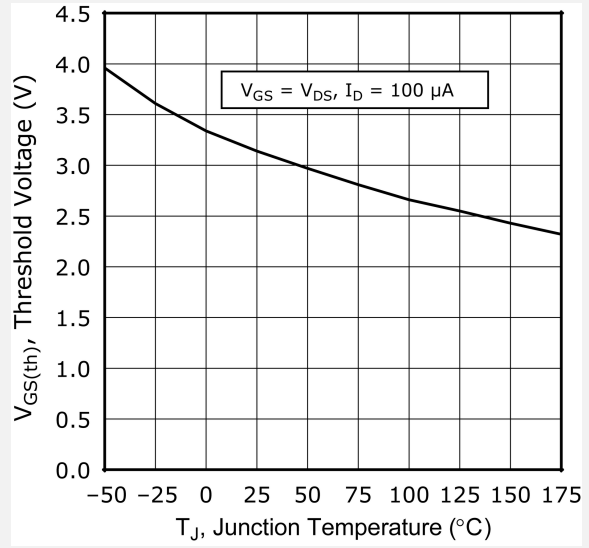
**Figure 1-12. Switching Energy vs.  $R_g$**



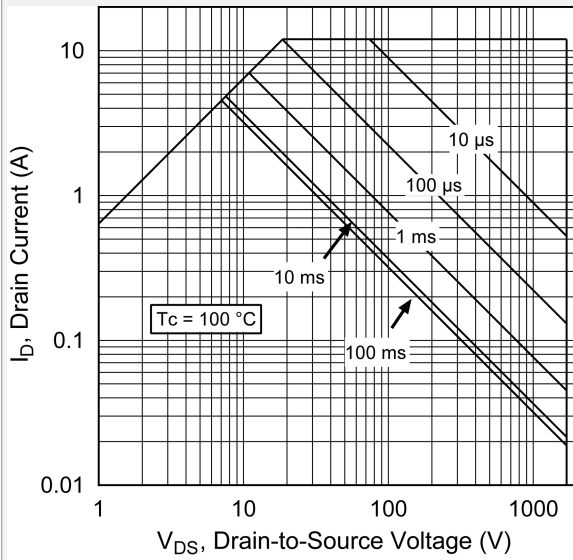
**Figure 1-13. Switching Energy vs. Temperature**



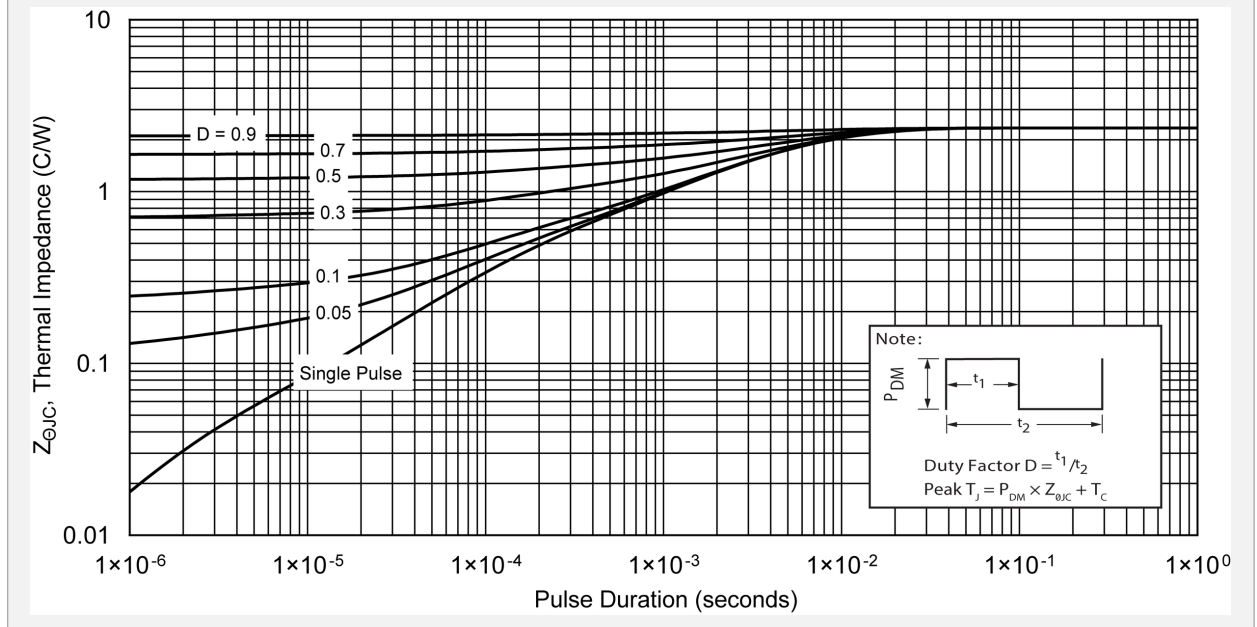
**Figure 1-14. Threshold Voltage vs. Junction Temp.**



**Figure 1-15. Forward Safe Operating Area**

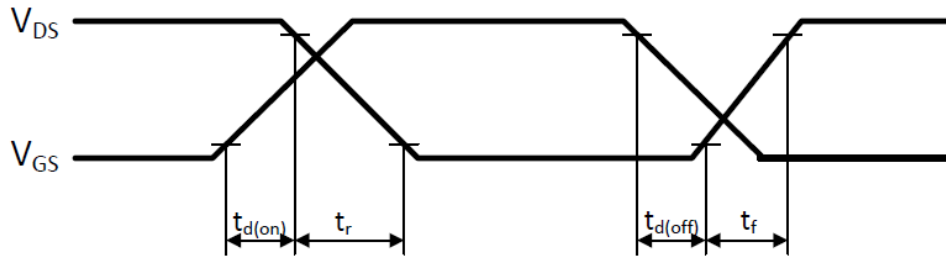


**Figure 1-16. Maximum Transient Thermal Impedance**



The following figure shows the switching waveform diagram of the MSC750SMA170SA device.

**Figure 1-17. Switching Waveform**



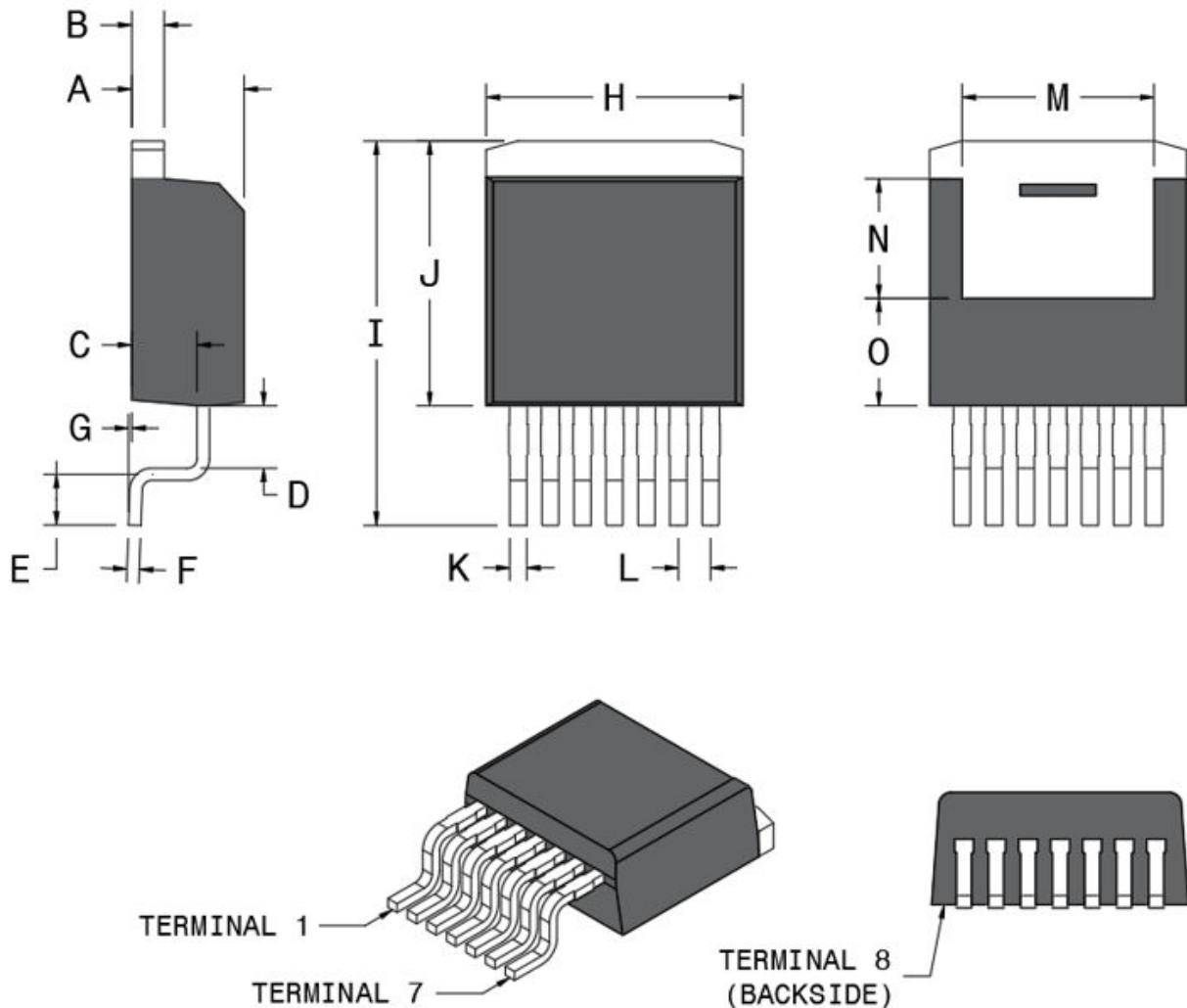
## 2. Package Specification

This section shows the package specification of the MSC750SMA170SA device.

### 2.1 Package Outline Drawing

The following figure illustrates the TO-263 7-lead package outline of the MSC750SMA170SA device.

Figure 2-1. Package Outline Drawing



The following table shows the TO-263 7-lead dimensions and should be used in conjunction with the package outline drawing.

Table 2-1. TO-263 Dimensions

Symbol	Min (mm)	Max (mm)	Min (in.)	Max (in.)
A	4.30	4.57	0.169	0.180
B	1.17	1.40	0.046	0.055
C	2.50	2.70	0.098	0.106

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## Package Specification

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Symbol	Min (mm)	Max (mm)	Min (in.)	Max (in.)
D	2.85	2.95	0.112	0.116
E	2.32	2.70	0.091	0.106
F	0.33	0.65	0.013	0.026
G	0.00	0.25	0.00	0.010
H	10.13	10.23	0.399	0.403
I	15.04	17.12	0.592	0.674
J	10.19	10.80	0.401	0.425
K	0.50	0.70	0.020	0.028
L	1.27 BSC		0.050 BSC	
M	6.78	7.67	0.267	0.302
N	4.66	4.81	0.183	0.189
O	4.20	4.30	0.165	0.169
Terminal 1	Gate			
Terminal 2	Source sense			
Terminal 3	Source			
Terminal 4	Source			
Terminal 5	Source			
Terminal 6	Source			
Terminal 7	Source			
Terminal 8	Drain			

### 3. Revision History

Table 3-1. Revision History

Revision	Date	Description
A	03/2022	Document created.

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