



**THE DATASHEET OF
DMN1001UCA10-7**



Product Summary

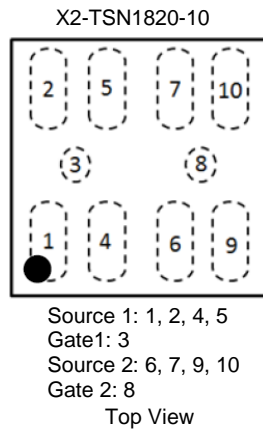
BV _{SSS}	R _{SS(ON)} Typ	I _S Max T _A = +25°C
12V	3.36mΩ @ V _{GS} = 3.8V	20A

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{SS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Battery Management
- Load Switch
- Battery Protection

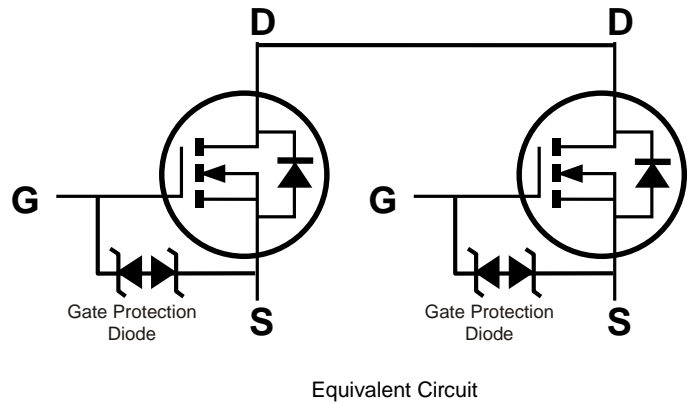


Features

- CSP with Footprint 1.84mm × 1.96mm
- Height = 0.30mm (Typical) for Low Profile
- ESD Protection of Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

Mechanical Data

- Case: X2-TSN1820-10
- Terminal Connections: See Diagram Below
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — NiAu. Solderable per MIL-STD-202, Method 208 (e4)
- Weight: 0.0026 grams (Approximate)

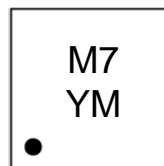


Ordering Information (Note 4)

Part Number	Case	Packaging
DMN1001UCA10-7	X2-TSN1820-10	3000/Tape & Reel

- Notes:
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 - See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



M7 = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: G = 2019)
M = Month (ex: 9 = September)

Date Code Key

Year	2019	2020	2021	2022	2023	2024	2025	2026	2027
Code	G	H	I	J	K	L	M	N	O

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Source-Source Voltage			V _{SSS}	12	V
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Source Current (Note 5) V _{GS} = 4.5V	Steady State	T _A = +25°C	I _S	20	A
		T _A = +70°C		16	
Continuous Source Current (Note 5) V _{GS} = 2.5V	Steady State	T _A = +25°C	I _S	14.5	A
		T _A = +70°C		11.5	
Pulsed Source Current (Note 6)			I _{SM}	90	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 7)	P _D	1.0	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 7)	R _{θJA}	125	°C/W
Power Dissipation (Note 5)	P _D	2.4	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	R _{θJA}	52	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Source -Source Breakdown Voltage	BV _{SSS}	12	—	—	V	V _{GS} = 0V, I _S = 1mA
Zero Gate Voltage Drain Current T _J = +25°C	I _{SSS}	—	—	1	μA	V _{SS} = 10V V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±10	μA	V _{GS} = ±8V, V _{SS} =0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	0.35	—	1.4	V	V _{SS} = 10V, I _S = 0.87mA
Static Source-Source On-Resistance	R _{SS(ON)}	1.9	3.19	3.55	mΩ	V _{GS} = 4.5V, I _S = 5A
		2.0	3.36	3.75		V _{GS} = 3.8V, I _S = 5A
		2.25	3.69	4.8		V _{GS} = 3.1V, I _S = 5A
		2.5	4.38	6.9		V _{GS} = 2.5V, I _S = 5A
Diode Forward Voltage	V _{SS}	—	0.7	1.2	V	V _{GS} = 0V, I _S = 6.8A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{ISS}	—	2865	—	pF	V _{SS} = 6V, V _{GS} = 0V, f = 1.0kHz
Output Capacitance	C _{OSS}	—	500	—		
Reverse Transfer Capacitance	C _{RSS}	—	247	—		
Gate Resistance	R _G	—	375	—	Ω	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz
Total Gate Charge	Q _G	—	29	—	nC	V _{DD} = 6V, V _{GS} = 4V, I _S = 6.8A
Gate-Source Charge	Q _{GS}	—	5.0	—		
Gate-Drain Charge	Q _{GD}	—	9.6	—		
Gate Charge at V _{TH}	Q _{G(TH)}	—	5.2	—		
Turn-On Delay Time	t _{D(ON)}	—	508	—	ns	V _{DD} = 6V, V _{GS} = 4V, I _S = 6.8A
Turn-On Rise Time	t _R	—	1034	—		
Turn-Off Delay Time	t _{D(OFF)}	—	1395	—		
Turn-Off Fall Time	t _F	—	2074	—		

- Notes:
5. Device mounted on FR-4 material with 1inch² (6.45cm²), 2oz. (0.071mm thick) Cu.
 6. Repetitive rating, pulse width limited by junction temperature.
 7. Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
 8. Short duration pulse test used to minimize self-heating effect.
 9. Guaranteed by design. Not subject to production testing.

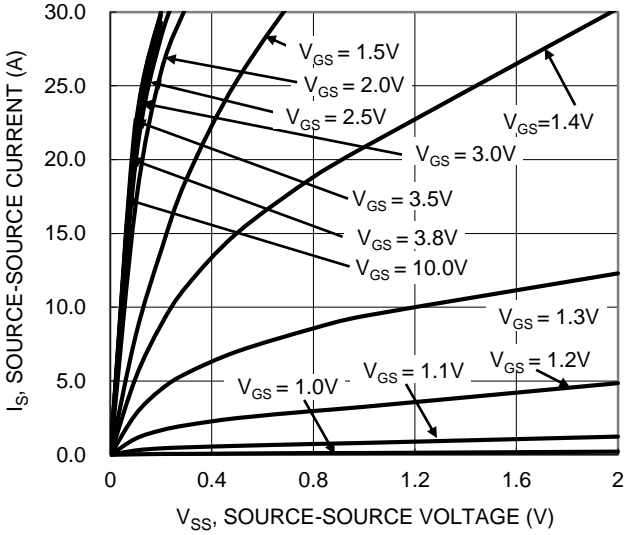


Figure 1. Typical Output Characteristic

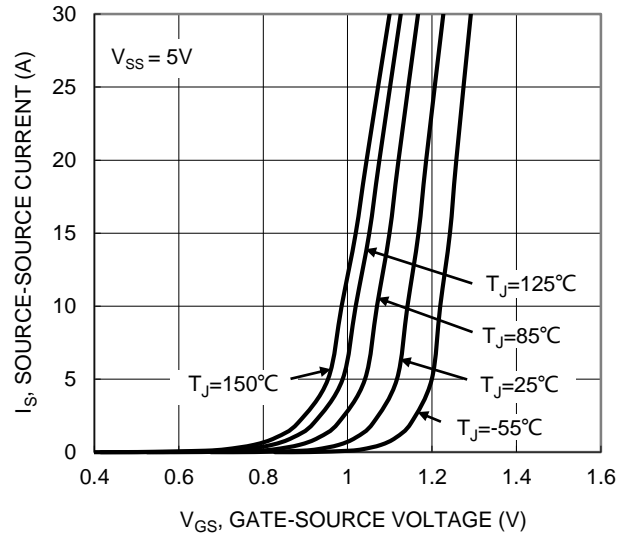


Figure 2. Typical Transfer Characteristic

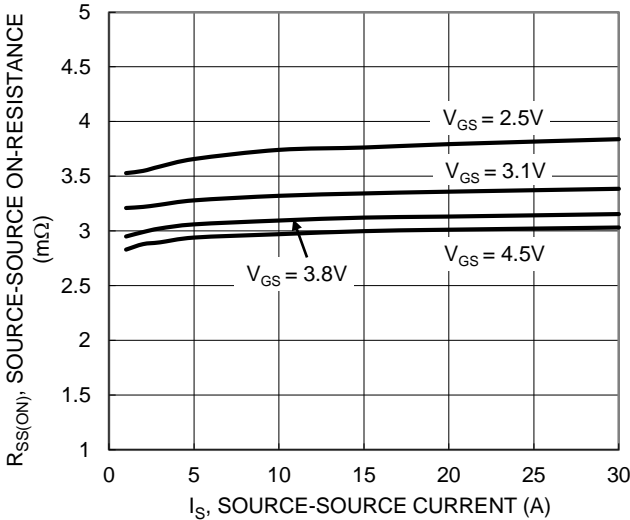


Figure 3. Typical On-Resistance vs. Source Current and Gate Voltage

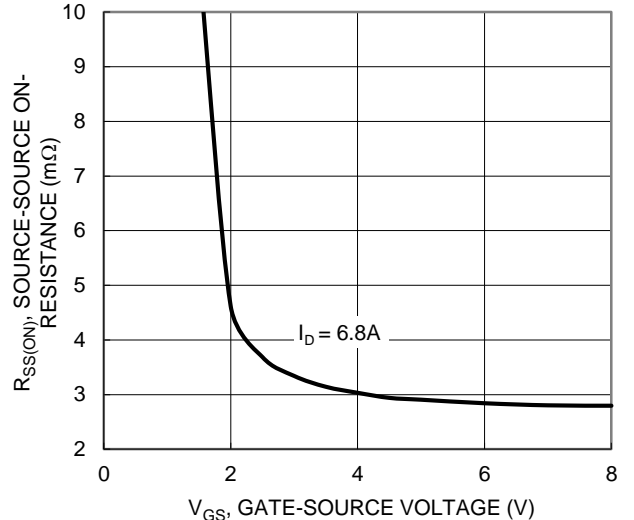


Figure 4. Typical Transfer Characteristic

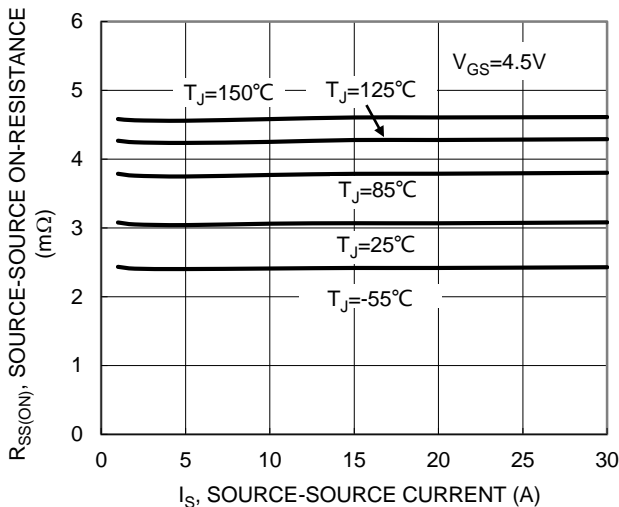


Figure 5. Typical On-Resistance vs. Source Current and Junction Temperature

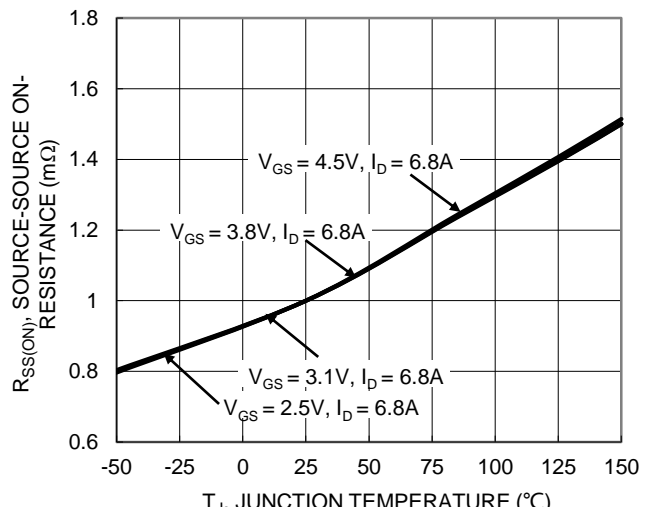


Figure 6. On-Resistance Variation with Junction Temperature

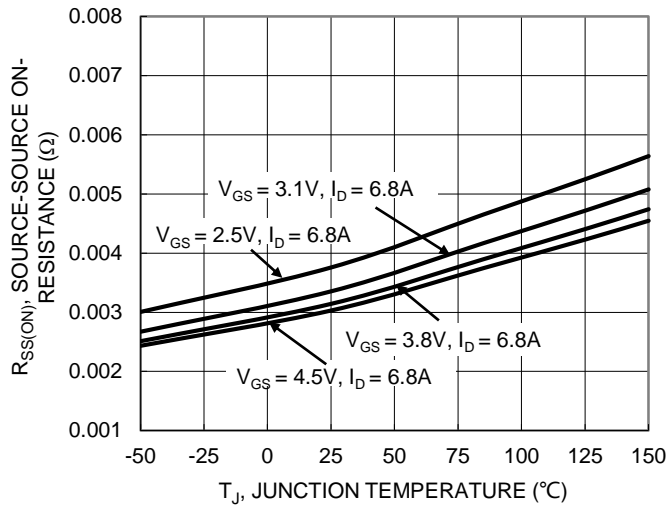


Figure 7. On-Resistance Variation with Junction Temperature

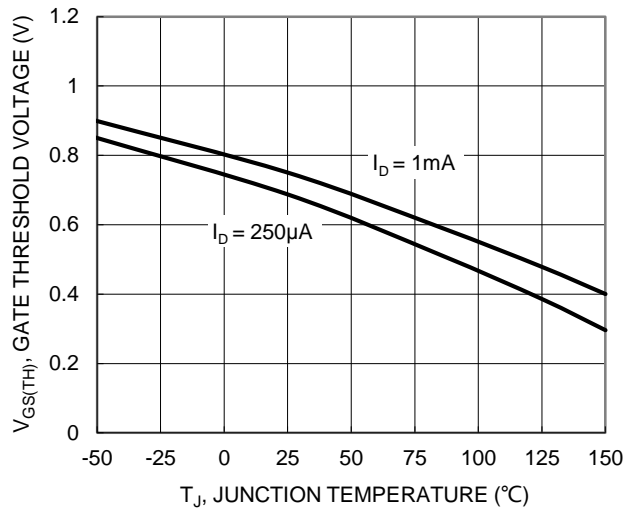


Figure 8. Gate Threshold Variation vs. Junction Temperature

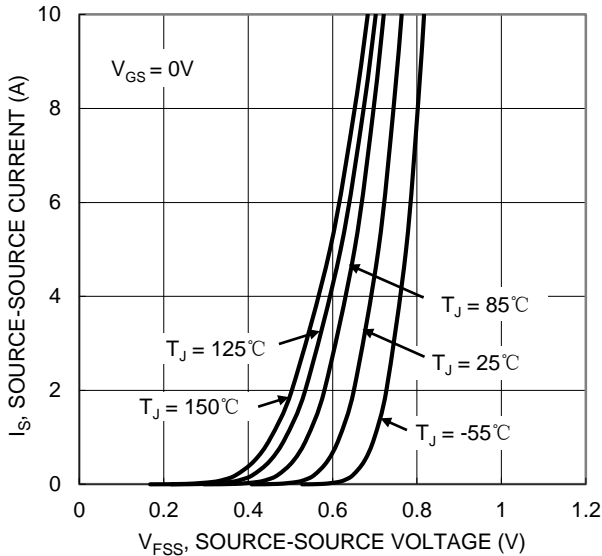


Figure 9. Diode Forward Voltage vs. Current

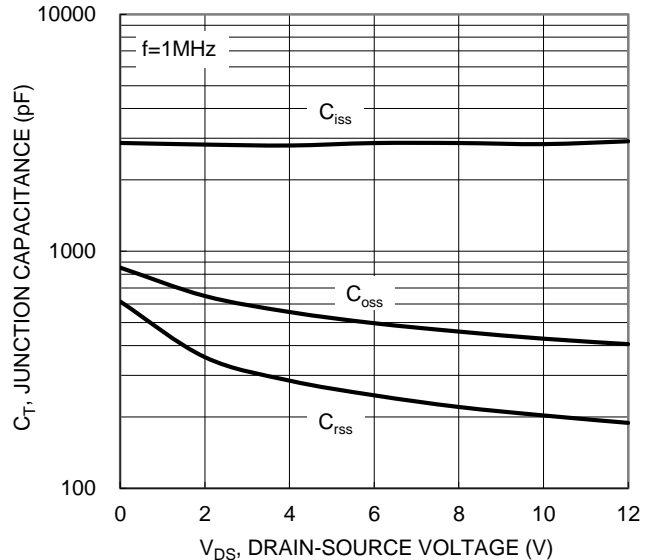


Figure 10. Typical Junction Capacitance

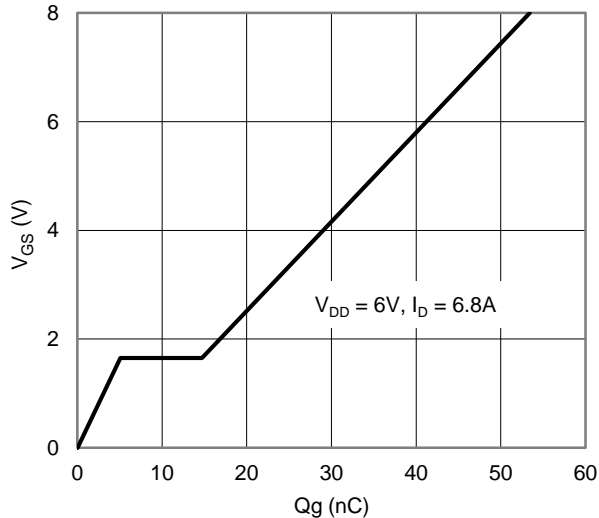


Figure 11. Gate Charge

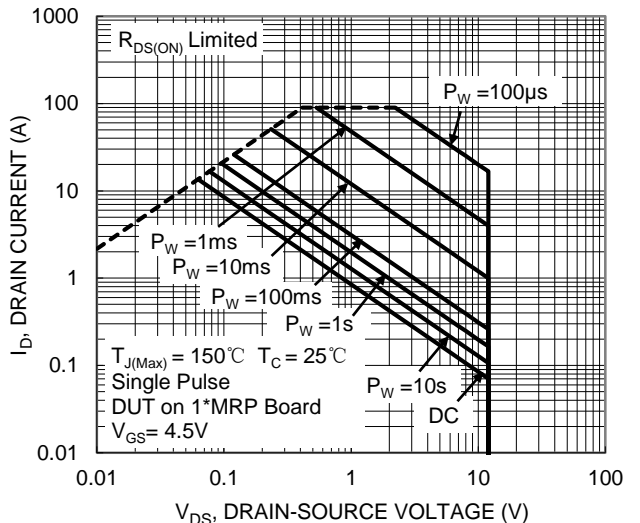


Figure 12. SOA, Safe Operation Area

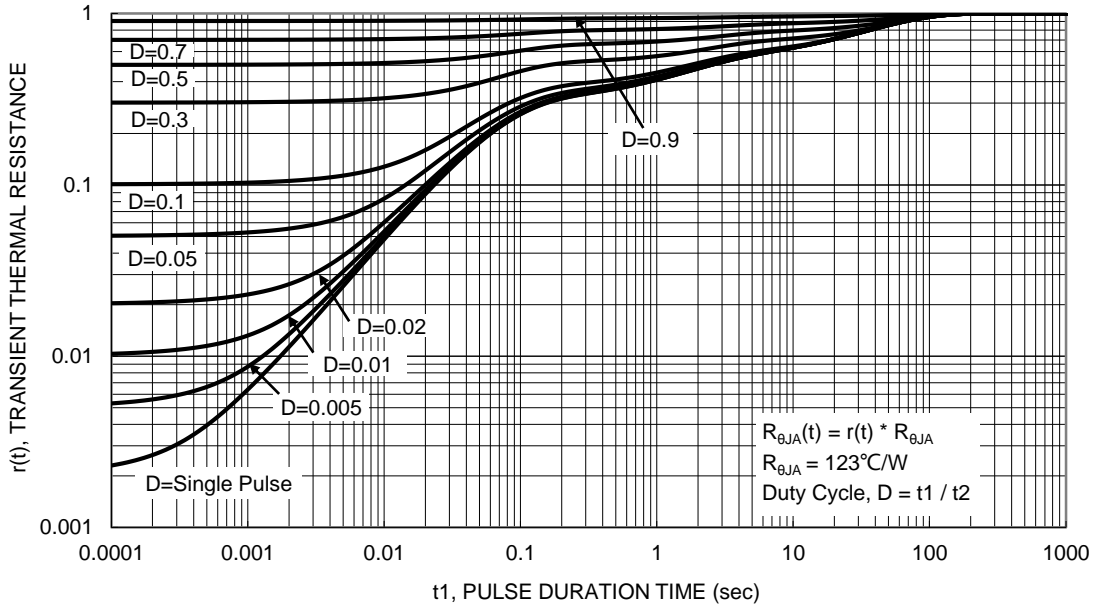
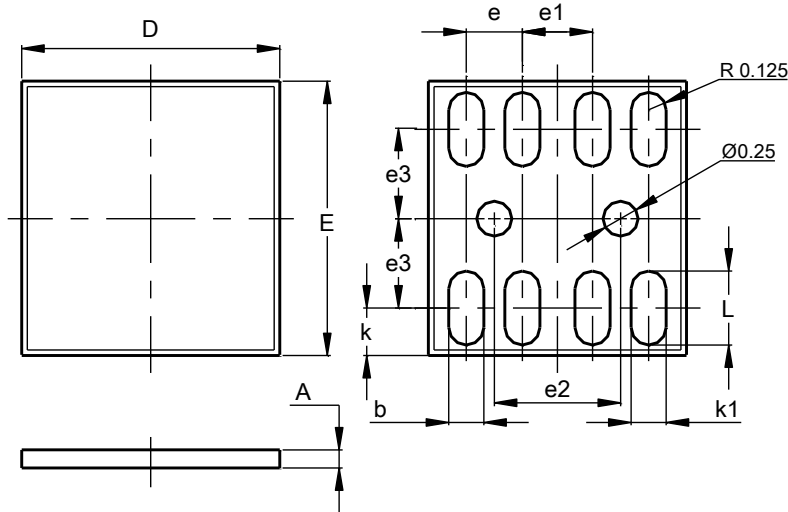


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X2-TSN1820-10



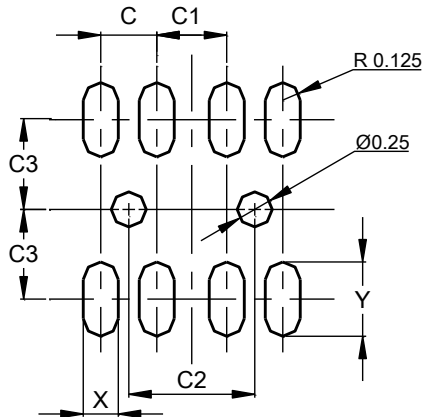
X2-TSN1820-10			
Dim	Min	Max	Typ
A	0.27	0.33	0.30
b	0.22	0.28	0.25
D	1.81	1.88	1.84
E	1.93	2.00	1.96
e	--	--	0.40
e1	--	--	0.50
e2	--	--	0.90
e3	--	--	0.64
k	--	--	0.34
k1	--	--	0.27
L	0.50	0.56	0.53
L1	--	--	1.81

All Dimensions in mm

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X2-TSN1820-10



Dimensions	Value (in mm)
C	0.40
C1	0.50
C2	0.90
C3	0.64
X	0.25
Y	0.53

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