

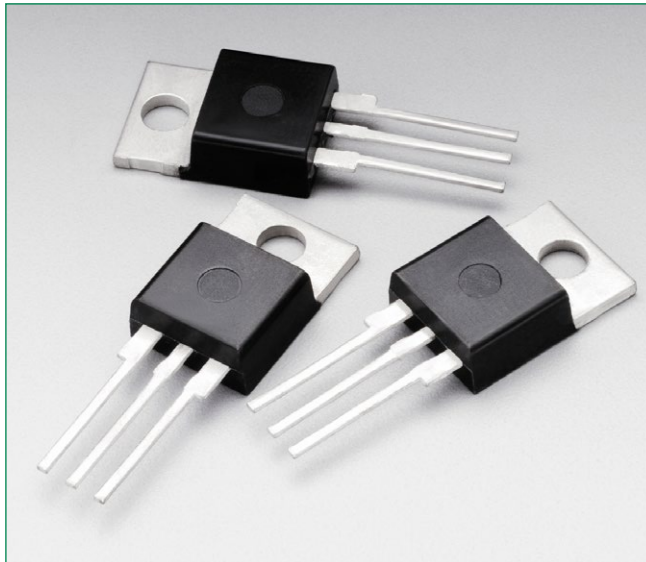


**THE DATASHEET OF  
MAC3030-8G**





# MAC3030-8



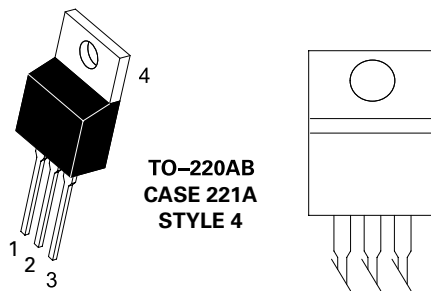
## Description

Designed primarily for full-wave AC control applications, such as light dimmers, motor controls, heating controls and power supplies; or wherever full-wave silicon gate controlled solid-state devices are needed. Triac type thyristors switch from a blocking to a conducting state for either polarity of applied main terminal voltage with positive or negative gate triggering.

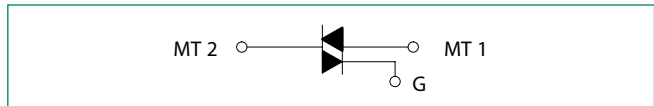
## Features

- Blocking Voltage to 250 Volts
- All Diffused and Glass Passivated Junctions for Greater Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance, High Heat Dissipation and Durability
- Gate Triggering Guaranteed in Four Modes (Quadrants)
- Pb-Free Packages are Available

## Pin Out



## Functional Diagram



## Additional Information



**Datasheet**



**Resources**



**Samples**

### Maximum Ratings ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Peak Repetitive Off-State Voltage (Note 1) (– 40 to 1125°C, Sine Wave, 50 to 60 Hz, Gate Open)	$V_{\text{DRM}}'$ $V_{\text{RRM}}$	250	V
On-State RMS Current ( $T_C = +70^\circ\text{C}$ ) Full Cycle Sine Wave, 50 to 60 Hz	$I_{\text{T (RMS)}}$	8.0	A
Peak Non-Repetitive Surge Current (One Full Cycle Sine Wave, 60 Hz, $T_C = +25^\circ\text{C}$ ) Preceded and followed by rated current	$I_{\text{TSM}}$	80	A
Circuit Fusing Consideration ( $t = 8.3$ ms)	$I^2t$	26	A <sup>2</sup> sec
Peak Gate Current, ( $T_C = +70^\circ\text{C}$ , Pulse Width = 10 $\mu\text{s}$ )	$I_{\text{GM}}$	2.0	A
Peak Gate Power ( $T_C = +70^\circ\text{C}$ , Pulse Width = 10 $\mu\text{s}$ )	$P_{\text{GM}}$	20	W
Average Gate Power ( $T_C = +70^\circ\text{C}$ , $t = 8.3$ ms)	$P_{\text{G (AV)}}$	0.35	W
Operating Junction Temperature Range	$T_J$	-40 to +125	°C
Storage Temperature Range	$T_{\text{stg}}$	-40 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

- $V_{\text{DRM}}$  and  $V_{\text{RRM}}$  for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

### Thermal Characteristics

Rating	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (AC) Junction-to-Ambient	$R_{\theta\text{JC}}$ $R_{\theta\text{JA}}$	2.0 62.5	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	$T_L$	260	°C

### Electrical Characteristics - OFF ( $T_J = 25^\circ\text{C}$ unless otherwise noted ; Electricals apply in both directions)

Characteristic	Symbol	Min	Typ	Max	Unit
Peak Repetitive Blocking Current ( $V_D = V_{\text{DRM}} = V_{\text{RRM}}$ ; Gate Open)	$I_{\text{DRM}}'$ $I_{\text{RRM}}$	-	-	1.0	mA
		-	-	2.0	

### Electrical Characteristics - ON ( $T_J = 25^\circ\text{C}$ unless otherwise noted; Electricals apply in both directions)

Characteristic	Symbol	Min	Typ	Max	Unit	
Peak On-State Voltage ( $I_{\text{TM}} = \pm 11$ A Peak, Pulse Width $\leq 2$ ms, Duty Cycle $\leq 2\%$ )	$V_{\text{TM}}$	–	1.2	1.65	V	
Gate Trigger Current (Continuous dc) ( $V_D = 12$ V, $R_L = 100$ Ohms)	$I_{\text{GT}}$	MT2(+), G(+)	–	12	50	mA
		MT2(+), G(–)	–	12	50	
		MT2(–), G(–)	–	20	50	
		MT2(–), G(+)	–	35	75	
Gate Trigger Voltage (Continuous dc) ( $V_D = 12$ V, $R_L = 100$ $\Omega$ )	$V_{\text{GT}}$	MT2(+), G(+)	–	0.9	2.0	V
		MT2(+), G(–)	–	0.9	2.0	
		MT2(–), G(–)	–	1.1	2.0	
		MT2(–), G(+)	–	1.4	2.5	
Gate Non-Trigger Voltage (Continuous DC), ( $V_D = 12$ V, $T_C = 110^\circ\text{C}$ , $R_L = 100$ $\Omega$ ) All Four Quadrants	$V_{\text{GD}}$	0.2	–	–	V	
Holding Current ( $V_D = 12$ V <sub>dc</sub> , Gate Open, Initiating Current = $\pm 200$ mA)	$I_{\text{H}}$	–	6.0	50	mA	
Turn-On Time (Rated $V_{\text{DRM}}$ , $I_{\text{TM}} = 11$ A) ( $I_{\text{GT}} = 120$ mA, Rise Time = 0.1 s, Pulse Width = 2 s)	$t_{\text{gt}}$	–	1.5	–	$\mu\text{s}$	

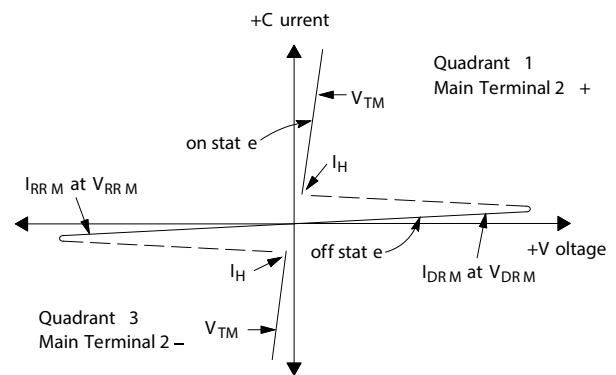


### Dynamic Characteristics

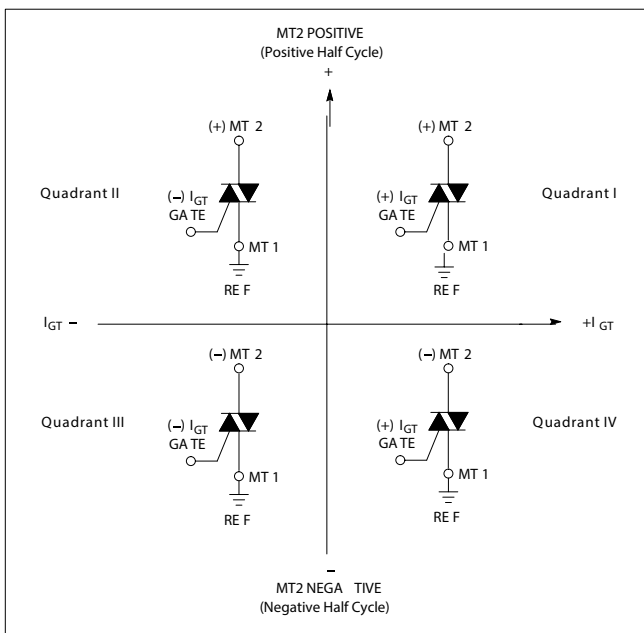
Characteristic	Symbol	Min	Typ	Max	Unit
Critical Rate of Rise of Commutation Voltage ( $V_D = \text{Rated } V_{DRM}$ , $I_{TM} = 14 \text{ A}$ , Commutating $di/dt = 5.0 \text{ A/ms}$ , Gate Unenergized, $T_C = 70^\circ\text{C}$ )	$(di/dt)_c$	–	5.0	–	A/ms
Critical Rate of Rise of Off-State Voltage ( $V_D = \text{Rated } V_{DRM}$ , Exponential Waveform, Gate Open, $T_C = \pm 70^\circ\text{C}$ )	$dv/dt$	–	100	–	V/ $\mu\text{s}$

### Voltage Current Characteristic of SCR

Symbol	Parameter
$V_{DRM}$	Peak Repetitive Forward Off State Voltage
$I_{DRM}$	Peak Forward Blocking Current
$V_{RRM}$	Peak Repetitive Reverse Off State Voltage
$I_{RRM}$	Peak Reverse Blocking Current
$V_{TM}$	Maximum On State Voltage
$I_H$	Holding Current

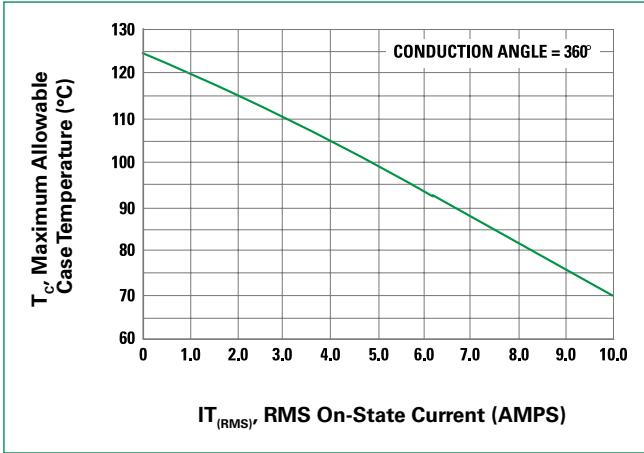


### Quadrant Definitions for a Triac

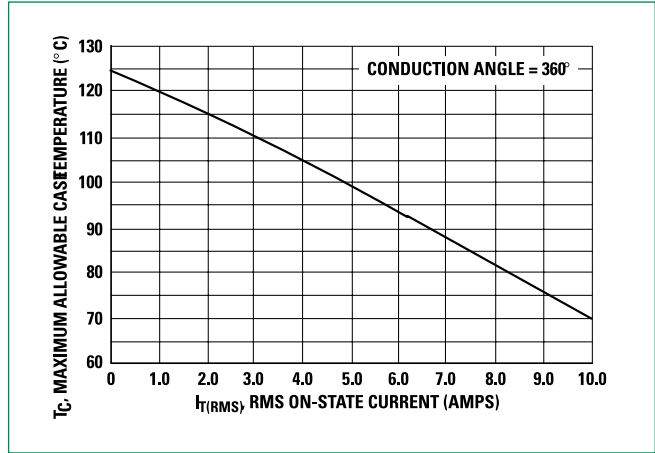


All polarities are referenced to MT1.  
With in-phase signals (using standard AC lines) quadrants I and III are used

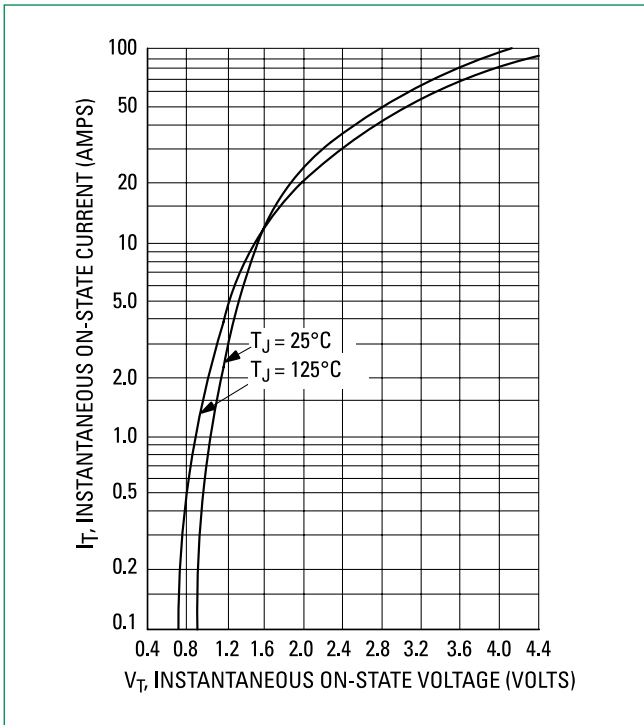
**Figure 1. Current Derating**



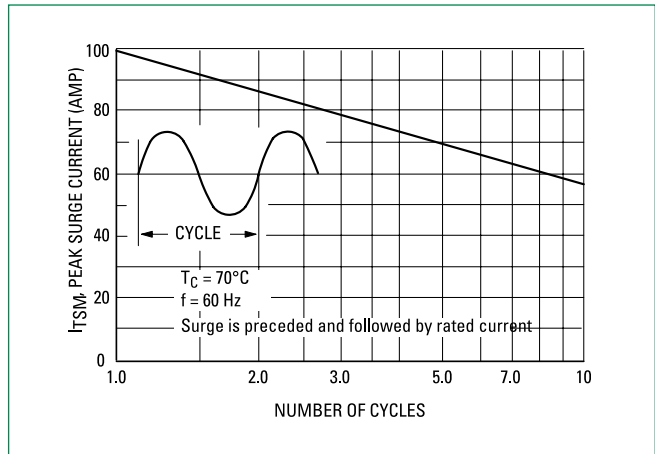
**Figure 2. Power Dissipation**



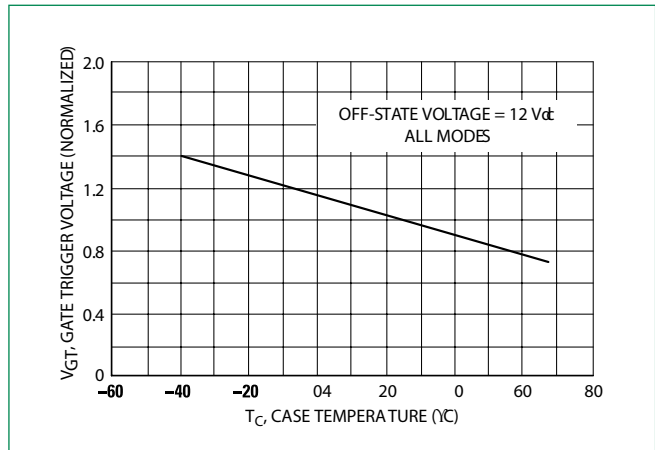
**Figure 3. Maximum On-State Characteristics**



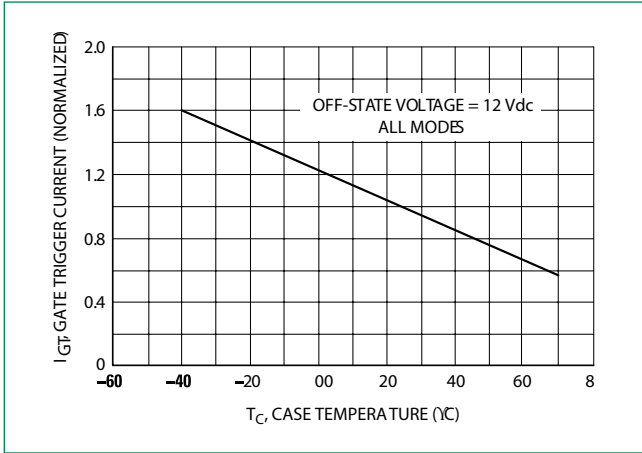
**Figure 4. Maximum Non-Repetitive Surge Current**



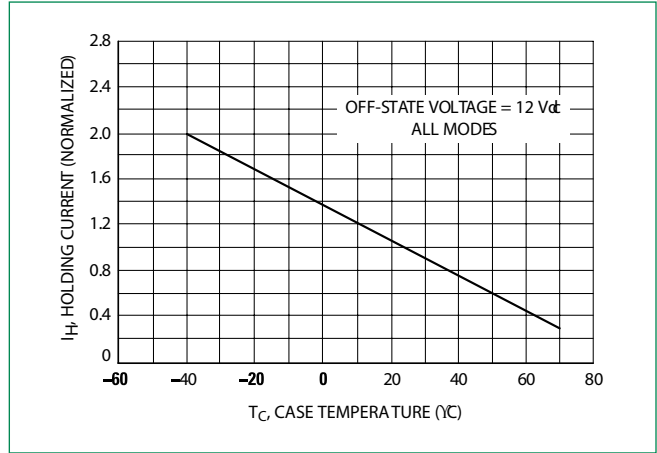
**Figure 5. Typical Gate Trigger Voltage**



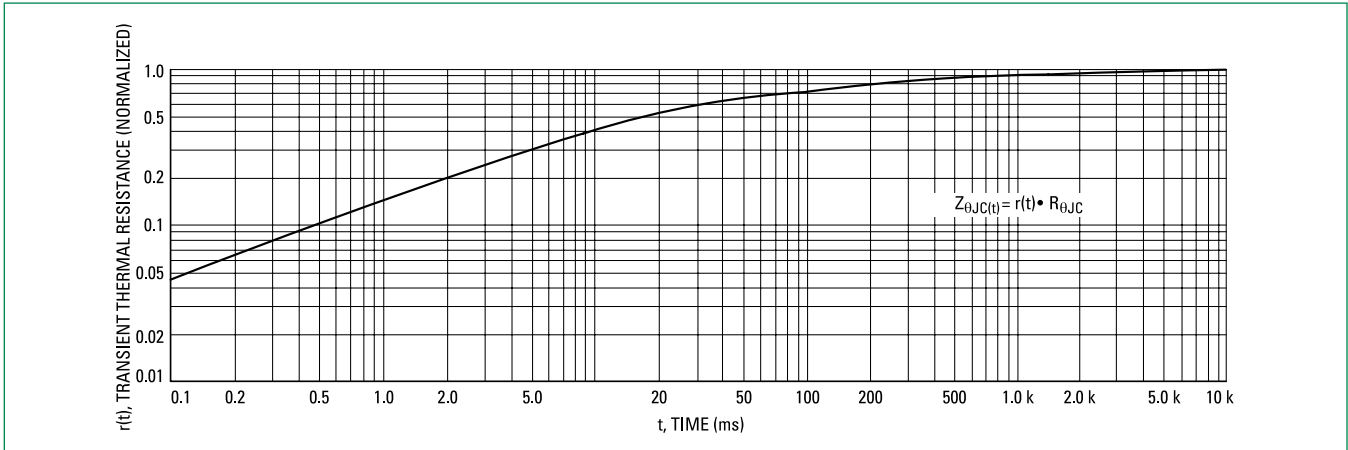
**Figure 6. Typical Gate Trigger Current**



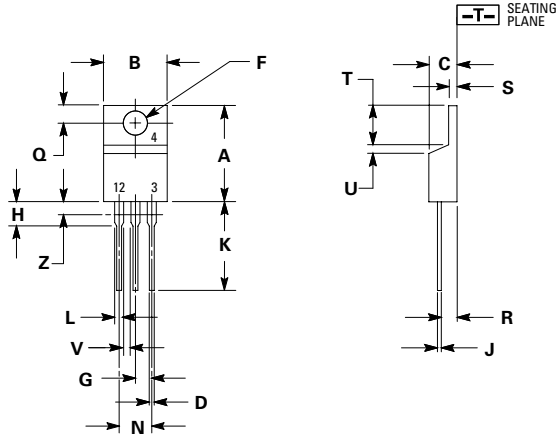
**Figure 7. Typical Holding Current**



**Figure 8. Thermal Response**



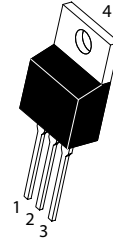
### Dimensions



Dim	Inches		Millimeters	
	Min	Max	Min	Max
A	0.590	0.620	14.99	15.75
B	0.380	0.420	9.65	10.67
C	0.178	0.188	4.52	4.78
D	0.025	0.035	0.64	0.89
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.41	2.67
H	0.110	0.130	2.79	3.30
J	0.018	0.024	0.46	0.61
K	0.540	0.575	13.72	14.61
L	0.060	0.075	1.52	1.91
N	0.195	0.205	4.95	5.21
Q	0.105	0.115	2.67	2.92
R	0.085	0.095	2.16	2.41
S	0.045	0.060	1.14	1.52
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
V	0.045	—	1.15	—
Z	—	0.080	—	2.04

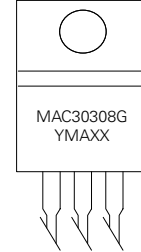
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

### Part Marking System



**TO-220AB  
CASE 221A  
STYLE 12**

x =D, M, or N  
Y =Year  
M =Month  
A =Assembly Site  
XX =Lot Serial Code  
G =Pb-Free Package



Pin Assignment	
1	Main Terminal 1
2	Main Terminal 2
3	Gate
4	No Connection


### Ordering Information

Device	Package	Shipping
MAC3030-8	TO-220AB	500 Units/ Box
MAC3030-8G	TO-220AB (Pb-Free)	500 Units/ Box

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