



**THE DATASHEET OF  
NX138BKSF**





# NX138BKS

60 V, dual N-channel Trench MOSFET

15 June 2016

Product data sheet

## 1. General description

Dual N-channel enhancement mode Field-Effect Transistor (FET) in a very small SOT363 (SC-88) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

## 2. Features and benefits

- Low threshold voltage
- Very fast switching
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection > 2 kV HBM

## 3. Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

## 4. Quick reference data

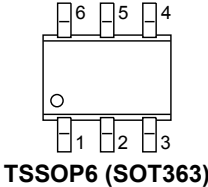
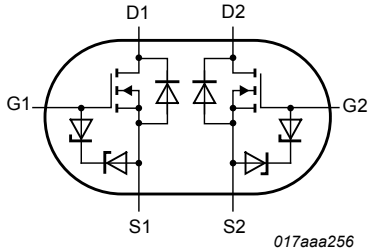
Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Per transistor</b>						
$V_{DS}$	drain-source voltage	$T_j = 25\text{ °C}$	-	-	60	V
$V_{GS}$	gate-source voltage		-20	-	20	V
$I_D$	drain current	$V_{GS} = 10\text{ V}; T_{amb} = 25\text{ °C}$	[1]	-	210	mA
<b>Static characteristics (per transistor)</b>						
$R_{DSon}$	drain-source on-state resistance	$V_{GS} = 10\text{ V}; I_D = 200\text{ mA}; T_j = 25\text{ °C}$	-	2.1	3.5	$\Omega$

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S1	source TR1	 <p>TSSOP6 (SOT363)</p>	 <p>017aaa256</p>
2	G1	gate TR1		
3	D2	drain TR2		
4	S2	source TR2		
5	G2	gate TR2		
6	D1	drain TR1		

## 6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
NX138BKS	TSSOP6	plastic surface-mounted package; 6 leads	SOT363

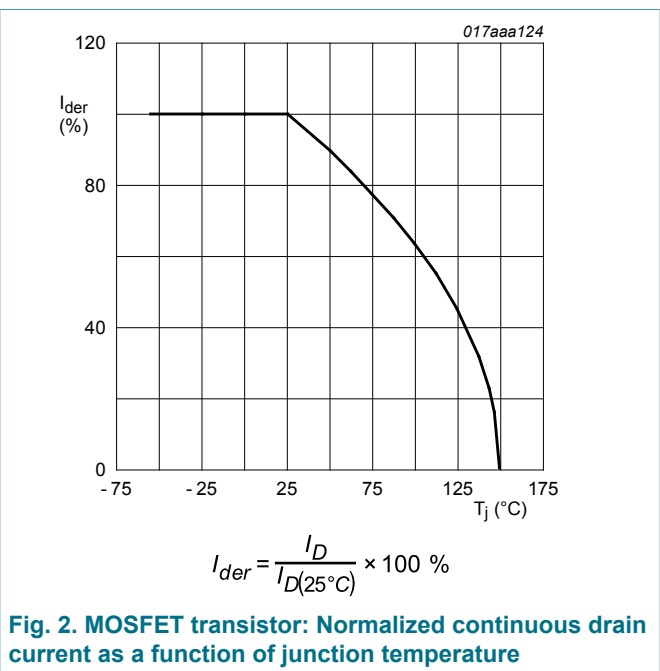
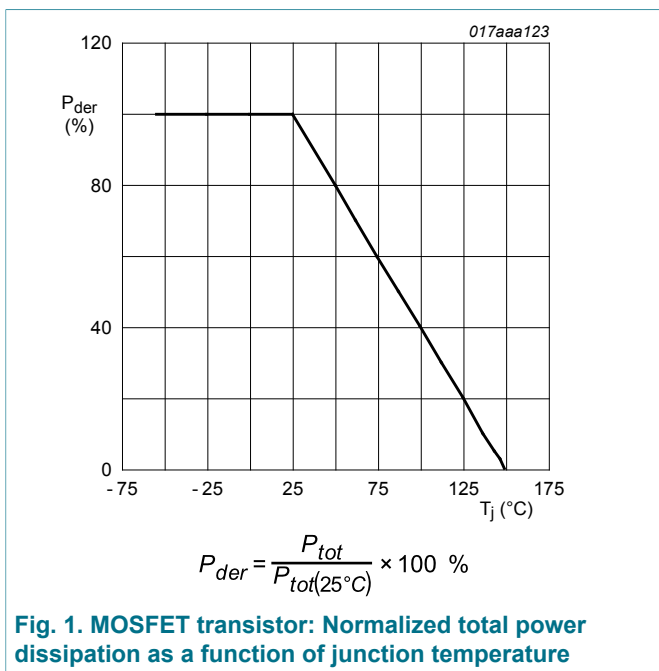
## 7. Limiting values

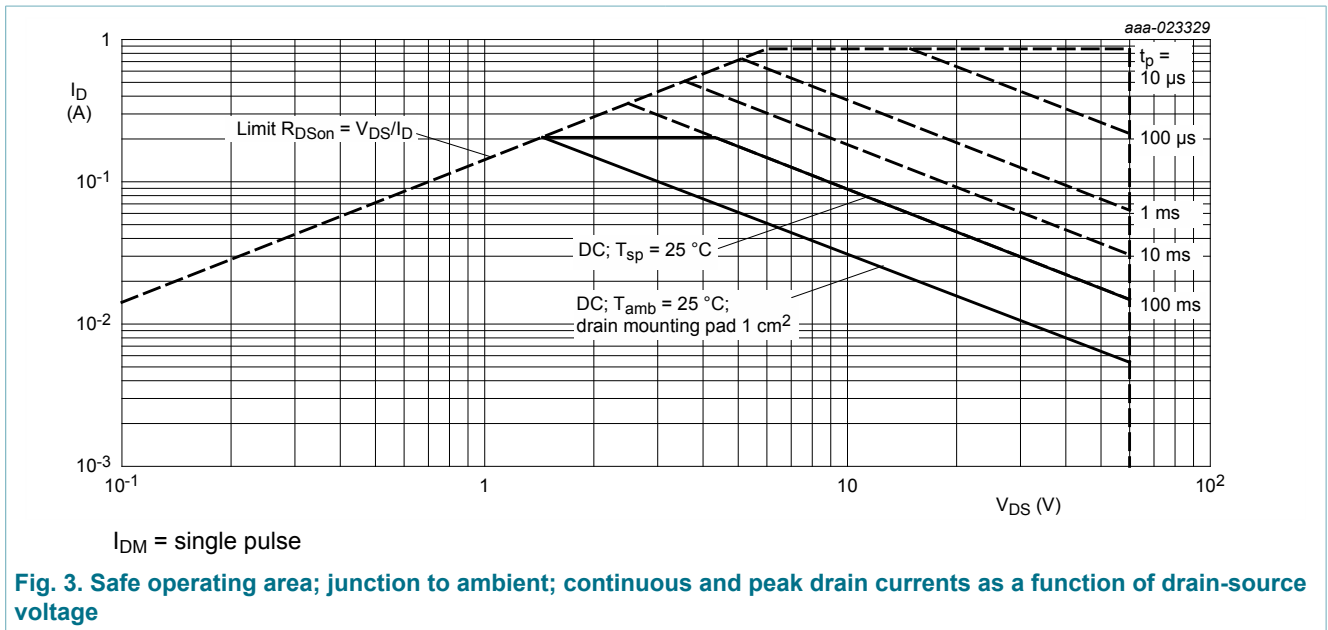
**Table 4. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
<b>Per transistor</b>						
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	60	V
V <sub>GS</sub>	gate-source voltage			-20	20	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 25 °C	[1]	-	210	mA
		V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 100 °C	[1]	-	135	mA
		V <sub>GS</sub> = 10 V; T <sub>sp</sub> = 25 °C		-	330	mA
I <sub>DM</sub>	peak drain current	T <sub>amb</sub> = 25 °C; single pulse; t <sub>p</sub> ≤ 10 μs		-	855	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	285	mW
			[1]	-	320	mW
		T <sub>sp</sub> = 25 °C		-	860	mW
<b>Per device</b>						
T <sub>j</sub>	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
<b>Source-drain diode</b>						
I <sub>S</sub>	source current	T <sub>amb</sub> = 25 °C	[1]	-	170	mA

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.





## 8. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
<b>Per transistor</b>							
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	380	440	K/W
			[2]	-	340	390	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	125	145	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.

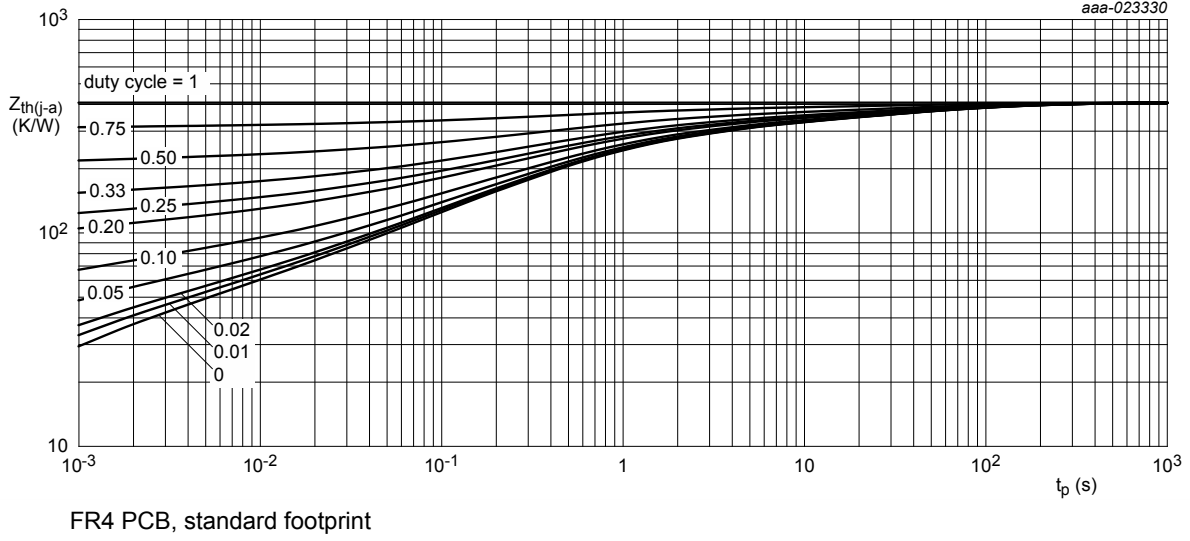


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

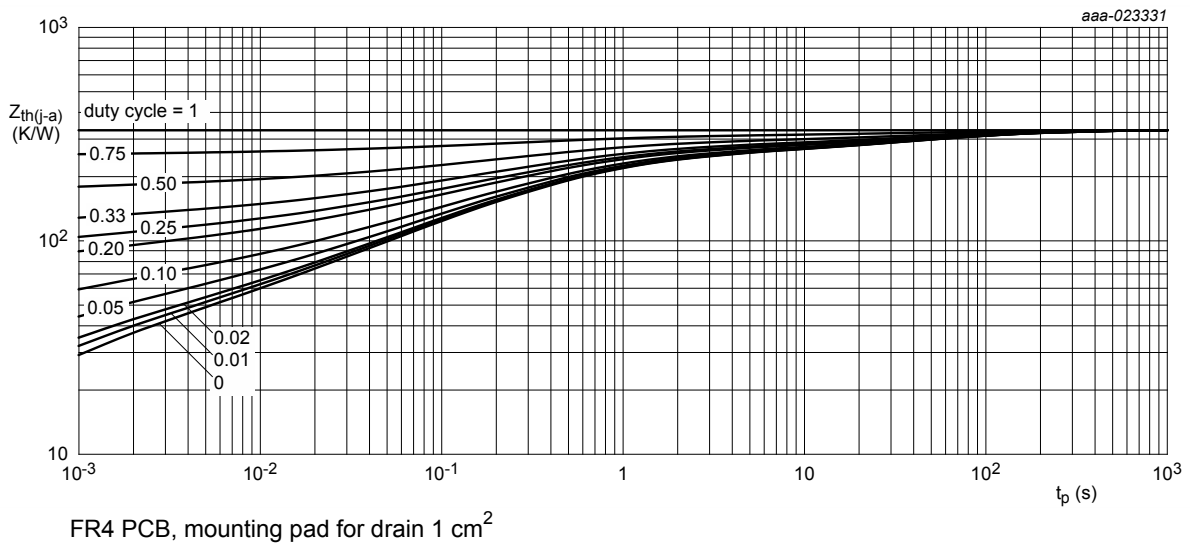


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

## 9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static characteristics (per transistor)</b>						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250 \mu A$ ; $V_{GS} = 0 V$ ; $T_j = 25 \text{ }^\circ C$	60	-	-	V
$V_{GSth}$	gate-source threshold voltage	$I_D = 250 \mu A$ ; $V_{DS} = V_{GS}$ ; $T_j = 25 \text{ }^\circ C$	0.5	1	1.5	V
$I_{DSS}$	drain leakage current	$V_{DS} = 60 V$ ; $V_{GS} = 0 V$ ; $T_j = 25 \text{ }^\circ C$	-	-	1	$\mu A$
$I_{GSS}$	gate leakage current	$V_{GS} = 20 V$ ; $V_{DS} = 0 V$ ; $T_j = 25 \text{ }^\circ C$	-	-	10	$\mu A$
		$V_{GS} = -20 V$ ; $V_{DS} = 0 V$ ; $T_j = 25 \text{ }^\circ C$	-	-	-10	$\mu A$
		$V_{GS} = 10 V$ ; $V_{DS} = 0 V$ ; $T_j = 25 \text{ }^\circ C$	-	-	1	$\mu A$
		$V_{GS} = -10 V$ ; $V_{DS} = 0 V$ ; $T_j = 25 \text{ }^\circ C$	-	-	-1	$\mu A$
		$V_{GS} = 5 V$ ; $V_{DS} = 0 V$ ; $T_j = 25 \text{ }^\circ C$	-	-	0.3	$\mu A$
		$V_{GS} = -5 V$ ; $V_{DS} = 0 V$ ; $T_j = 25 \text{ }^\circ C$	-	-	-0.3	$\mu A$
$R_{DSon}$	drain-source on-state resistance	$V_{GS} = 10 V$ ; $I_D = 200 \text{ mA}$ ; $T_j = 25 \text{ }^\circ C$	-	2.1	3.5	$\Omega$
		$V_{GS} = 10 V$ ; $I_D = 200 \text{ mA}$ ; $T_j = 150 \text{ }^\circ C$	-	4.3	7.2	$\Omega$
		$V_{GS} = 5 V$ ; $I_D = 170 \text{ mA}$ ; $T_j = 25 \text{ }^\circ C$	-	2.2	3.8	$\Omega$
		$V_{GS} = 2.5 V$ ; $I_D = 75 \text{ mA}$ ; $T_j = 25 \text{ }^\circ C$	-	2.6	5	$\Omega$
$g_{fs}$	forward transconductance	$V_{DS} = 10 V$ ; $I_D = 200 \text{ mA}$ ; $T_j = 25 \text{ }^\circ C$	-	0.7	-	S
<b>Dynamic characteristics (per transistor)</b>						
$Q_{G(tot)}$	total gate charge	$V_{DS} = 30 V$ ; $I_D = 200 \text{ mA}$ ; $V_{GS} = 10 V$ ; $T_j = 25 \text{ }^\circ C$	-	0.5	0.7	nC
$Q_{GS}$	gate-source charge		-	0.12	-	nC
$Q_{GD}$	gate-drain charge		-	0.12	-	nC
$C_{iss}$	input capacitance	$V_{DS} = 30 V$ ; $f = 1 \text{ MHz}$ ; $V_{GS} = 0 V$ ; $T_j = 25 \text{ }^\circ C$	-	20	-	pF
$C_{oss}$	output capacitance		-	3.1	-	pF
$C_{rss}$	reverse transfer capacitance		-	2	-	pF
$t_{d(on)}$	turn-on delay time		$V_{DS} = 30 V$ ; $I_D = 200 \text{ mA}$ ; $V_{GS} = 10 V$ ; $R_{G(ext)} = 6 \Omega$ ; $T_j = 25 \text{ }^\circ C$	-	8	-
$t_r$	rise time	-		8	-	ns
$t_{d(off)}$	turn-off delay time	-		13	-	ns
$t_f$	fall time	-		5	-	ns
<b>Source-drain diode (per transistor)</b>						
$V_{SD}$	source-drain voltage	$I_S = 200 \text{ mA}$ ; $V_{GS} = 0 V$ ; $T_j = 25 \text{ }^\circ C$	-	0.9	1.2	V

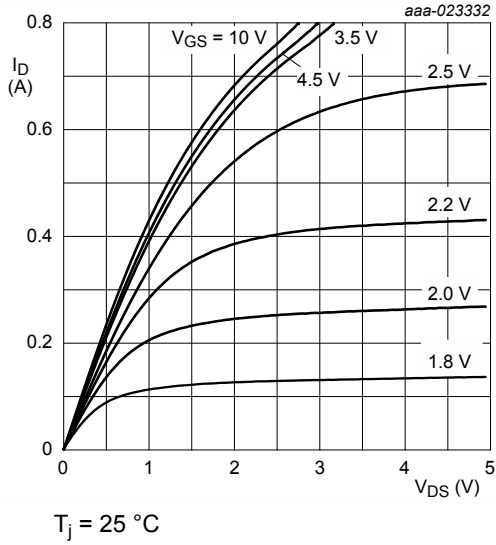


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

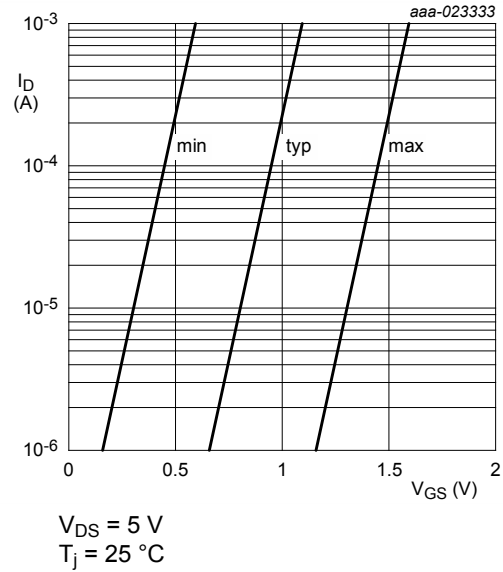


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

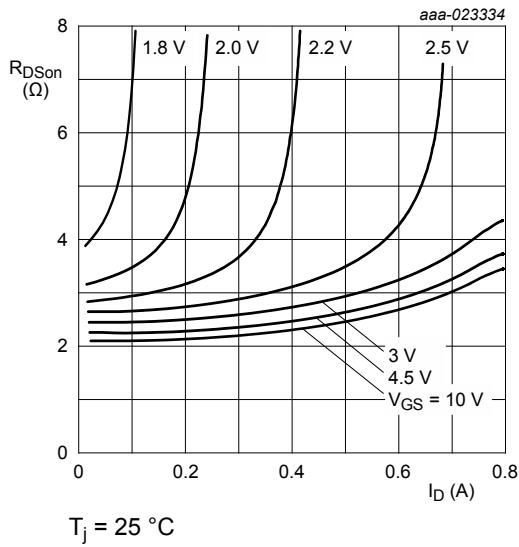


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

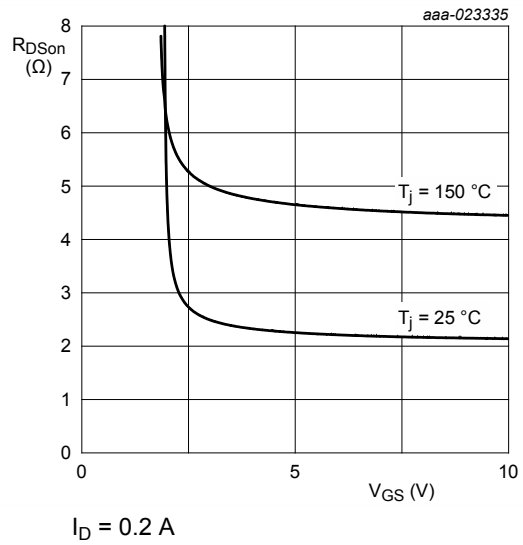


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

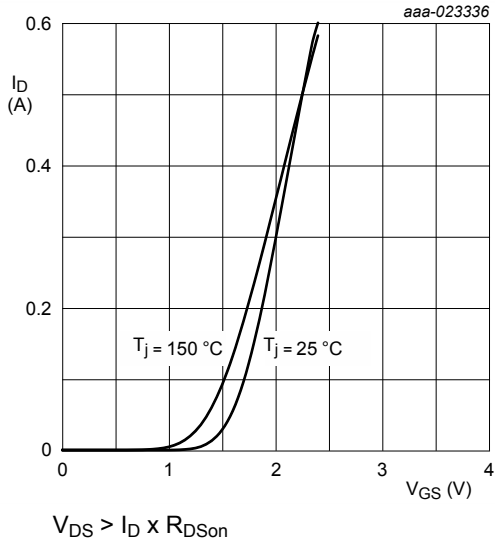


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

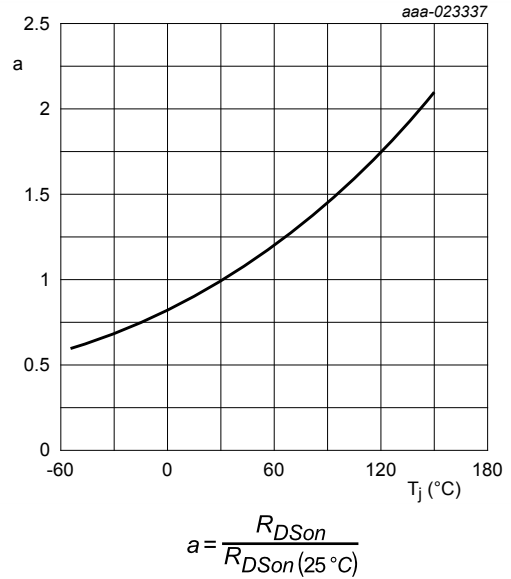


Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

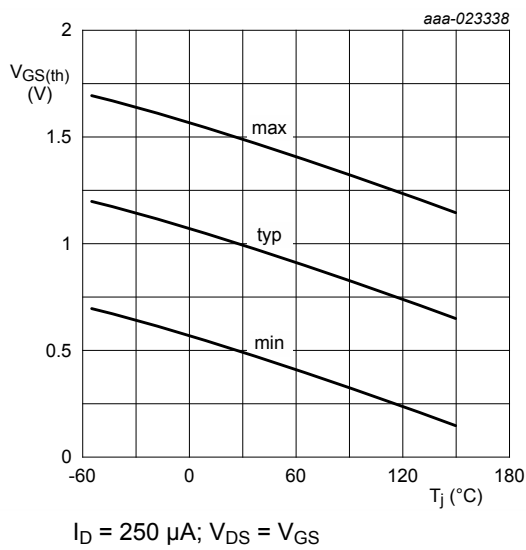


Fig. 12. Gate-source threshold voltage as a function of junction temperature

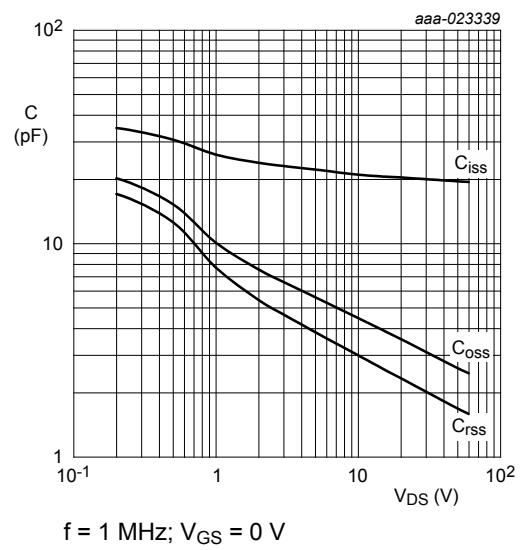


Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

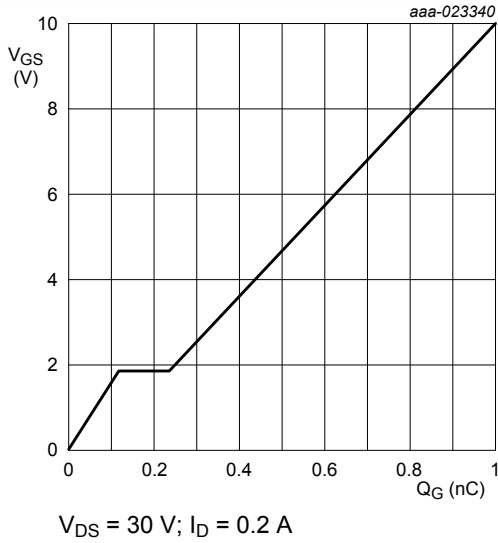


Fig. 14. Gate-source voltage as a function of gate charge; typical values

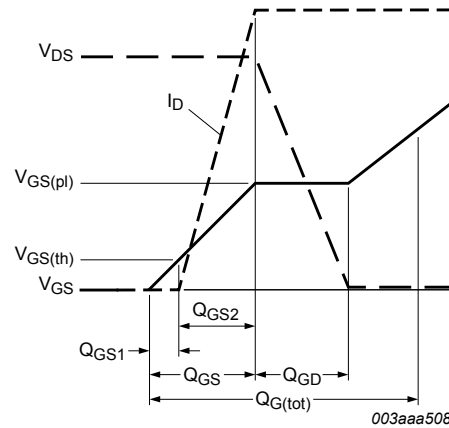


Fig. 15. MOSFET transistor: Gate charge waveform definitions

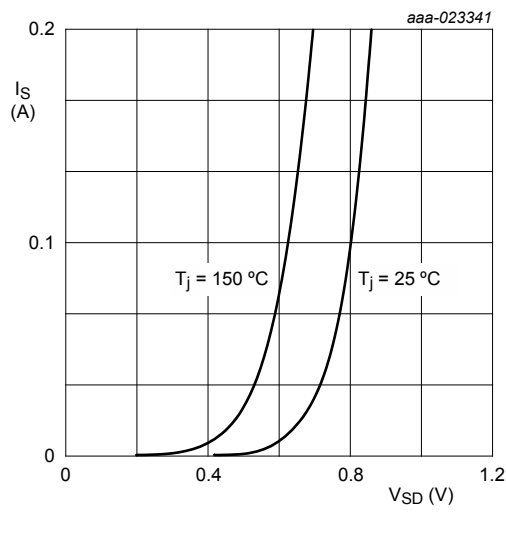


Fig. 16. Source current as a function of source-drain voltage; typical values

## 10. Test information

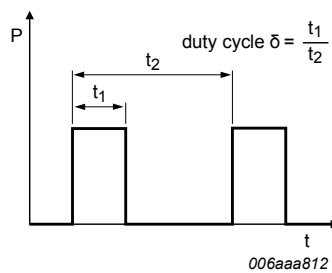


Fig. 17. Duty cycle definition

### 11. Package outline

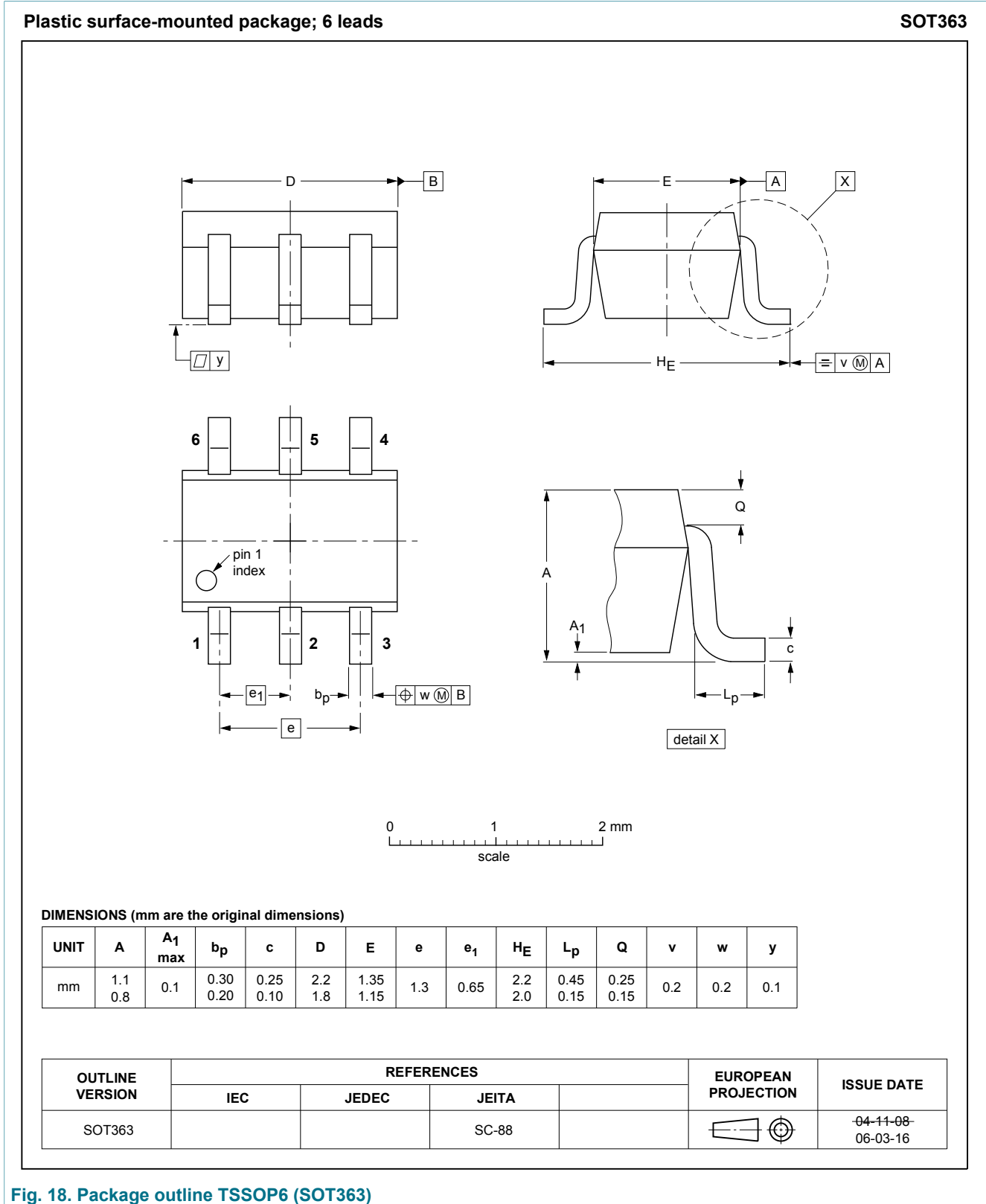


Fig. 18. Package outline TSSOP6 (SOT363)

## 12. Soldering

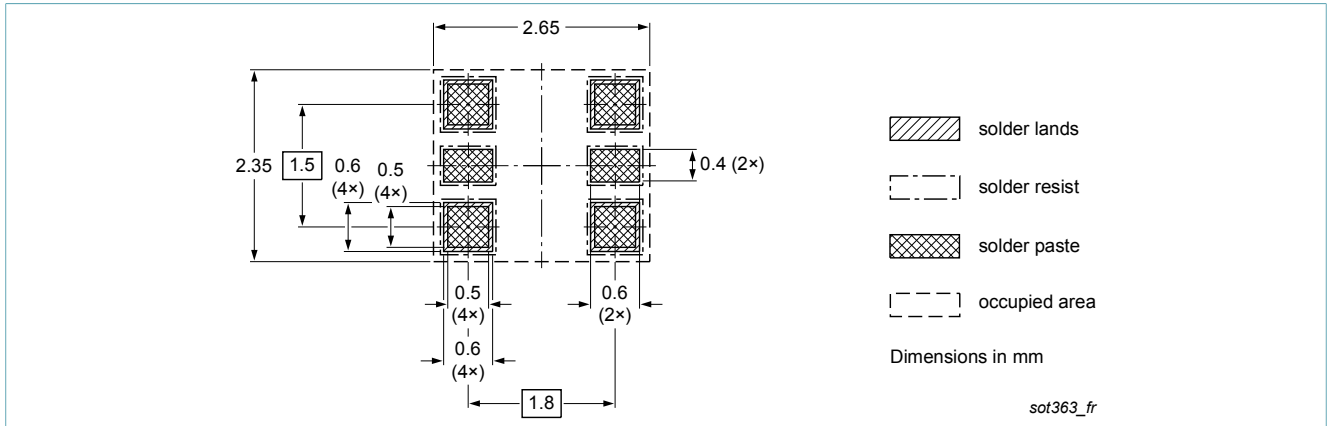


Fig. 19. Reflow soldering footprint for TSSOP6 (SOT363)

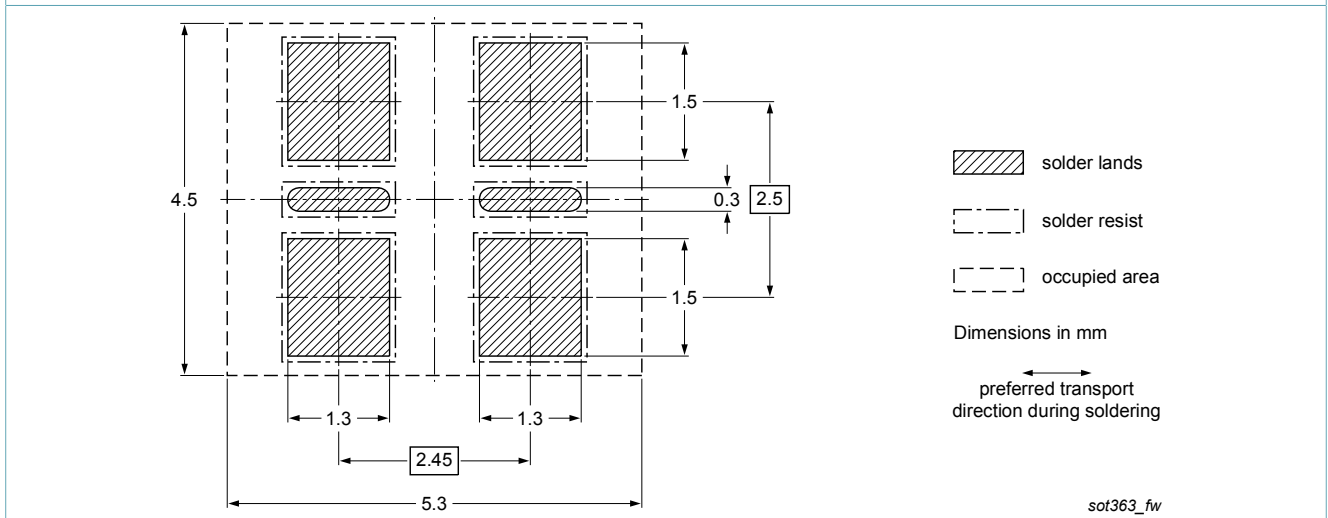


Fig. 20. Wave soldering footprint for TSSOP6 (SOT363)

### 13. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
NX138BKS v.1	20160615	Product data sheet	-	-

## 14. Legal information

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Document status [1][2]	Product status [3]	Definition
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## 15. Contents

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1. General description.....	1
2. Features and benefits.....	1
3. Applications.....	1
4. Quick reference data.....	1
5. Pinning information.....	2
6. Ordering information.....	2
7. Limiting values.....	3
8. Thermal characteristics.....	4
9. Characteristics.....	6
10. Test information.....	9
11. Package outline.....	10
12. Soldering.....	11
13. Revision history.....	12
14. Legal information.....	13

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