




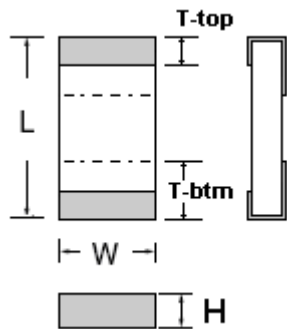
CPA Series (2512)

	<p>Construction:</p> <ul style="list-style-type: none"> • High Purity Alumina ceramic • Nickel alloy thin-film resistive element • Epoxy-resin overcoat • Pre-tinned (Sn100, matte) terminations over Ni barrier is standard (RoHS and Pb Free) • Halogen Free 	<p>Features:</p> <ul style="list-style-type: none"> • TCR's to $\pm 25\text{ppm}/^\circ\text{C}$ • Tolerances less than $\pm 1\%$ available • Standard and custom sizes & terminations available (Sn60Pb40 option) • High volume production, suitable for commercial and special applications • Competitive pricing
-----------------------------------------------------------------------------------	--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------	---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------

Description:

These power resistors are designed to tolerate high current and establish a low thermal resistance interface with the circuit board. A lower thermal resistance more efficiently sinks heat to the board, enabling a larger effective area for heat dissipation. As a result, much lower surface temperatures are achievable in comparison to standard chip resistors for the same chip size and applied power.

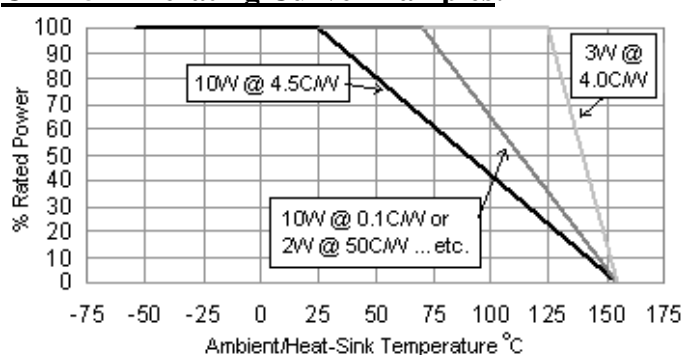
Dimensions:

	Size		Standard Dimensions (mm)				
	Inch	Metric	L	W	H	T-top	T-btm
	2512	6332	6.3 ± 0.2	3.2 ± 0.2	0.7 ± 0.1	0.9 ± 0.2	2.0 ± 0.2
Call for other sizes and/or termination styles							

Electrical Specifications:

Size: Inch (Metric)	2512 (6332)	
Rated Power ^{1,2}	Up to 16W ^{1,2}	
Rated Voltage	$\sqrt{P \times R}$	
Resistance Tolerance	$\pm 1\%$	
Standard Resistance Values (E12)	3.3 to 120 Ω Call for other values	
TCR (ppm/ $^\circ\text{C}$) ³	± 25 (E)	22 thru 120 Ω
	± 50 (Q)	3.3 thru 20 Ω
Operating Temperature Range ⁴	-55 to 155 $^\circ\text{C}$	
Insulation Resistance (100V, 1min) ⁵	> 1G Ω	

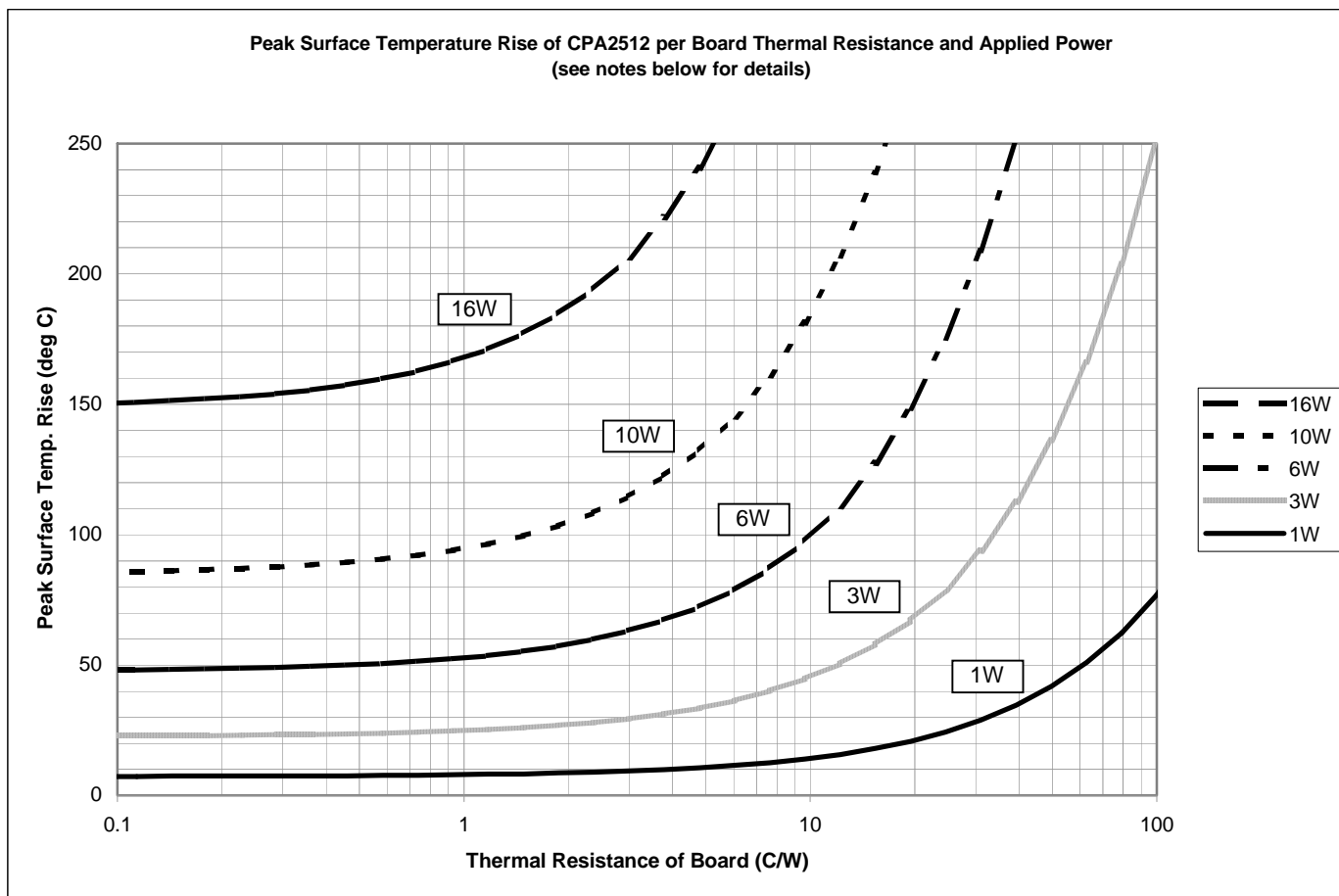
CPA2512 Derating Curve Examples:⁶



Notes:

1. Dependent on effective thermal conductivity/resistance of board construction/land design and size of board - greater power capability for board/land with lower thermal resistance. For relatively high thermal resistance mountings, the power resistors are capable of generating sufficient heat to reflow solder bonds without device damage.
2. Refer to Thermal Performance Plot below.
3. Per MIL-PRF-55342 (-55/25/125 $^\circ\text{C}$).
4. Per MIL-PRF-55342.
5. Per IEC 60115-1.
6. Derating curves are derived from the thermal performance plots.

Thermal Performance:



Notes:

- Plots produced by characterization of thermal coefficients determined from experimental measurements (by thermal imaging camera) at thermal equilibrium with parts mounted to various boards (with homogeneous thermal conductivity to minimize uncertainty) per recommended solder pad dimensions and with boards pressed against a Cu carrier/heat-sink (not ideal) with a thermal compound interface in a static environment (no air flow).
- Heat flow primarily through thickness of board with virtually zero lateral heat transfer in board.
- Thermal resistance of test boards were calculated based on material manufacturer specified thermal conductivity (20°C) via the following: Thermal Resistance (°C/W) = L / (k • A), where Thermal Conductivity, k (W/m•K) = (L / (A • ΔT)) • ΔQ/Δt, L = Thickness of board in meters and A = area of chip resistor in meters (2512 size = 6.3x3.2mm)
- The relationships between peak surface temperature rise, power, and board thermal resistance are linear, but the x-axis is plotted in log-scale to offer greater resolution at lower board thermal resistances.

Recommended Solder Pad Dimensions:

The diagram shows a top-down view of a chip resistor assembly. A central rectangular chip resistor is mounted on a larger rectangular land. The land has solder pads on its top and bottom edges. Solder resist is applied to the top and bottom surfaces of the land. Dimension A is the width of the solder pads, B is the width of the land, and C is the width of the chip resistor.

Feature	Standard Dimensions (mm)
A	1.6
B	7.7
C	3.5

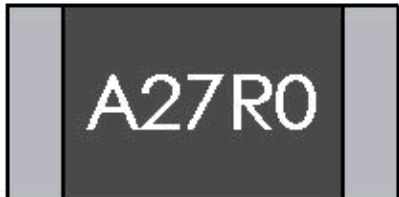
Environmental Performance Specifications:

Test	Reference	Conditions of Test	Requirement
Life ⁴	MIL-PRF-55342, MIL-STD-202 Method 108A	70°C, 2000h, rated power ³ , 1.5h on, 0.5h off	± 0.5% + 0.01Ω
Thermal Shock	MIL-PRF-55342, MIL-STD-202 Method 107G	Condition F-3, -65°C/0.25h to 155°C/0.25h, 100 cycles	± 0.1% + 0.01Ω
High Temperature Exposure	MIL-PRF-55342	155°C, 100h	± 0.1% + 0.01Ω
Short Time Overload	MIL-PRF-55342	2.5 x rated voltage ³ , 5 sec.	± 0.1% + 0.01Ω
Moisture Load Life	JEDEC 22-A101	85°C / 85%RH, 2,000 hours 24h/cycle, with and without bias, bias = 1.5h on, 0.5h off @ 1/10 th rated power ³	± 0.5% + 0.01Ω
Resistance to Soldering Heat ¹	MIL-PRF-55342, MIL-STD-202 Method 210F	260°C for 15 sec., over 220°C for 60 sec., 3 cycles	± 0.1% + 0.01Ω
Solderability ²	MIL-PRF-55342, MIL-STD-202 Method 208H	Precondition E: 150°C dry bake for 16h, Method 1 “Dip and Look Test”, 245°C, 5 sec., Pb-free (SnAgCu) Solder	Min 95% coverage of critical area
Board Flex	IEC 60115-1 / JIS C 5202	Bend amount of 3mm, measurements during and after bend	± 0.1% + 0.01Ω, No mech. damage
Terminal Strength	MIL-PRF-55342	Force of 3kg for 30 sec.	No mech. damage

Notes:

1. Test conditions modified to represent the high temperature Pb-free reflow conditions and an extra cycle is added.
2. JESD22-B102D adds test conditions for Pb-free and is aligned with J-STD-002B referenced in MIL-STD-202 Method 208H. JESD22-B102D procedure comes from EIA-638, “Surface Mount Solderability Test”.
3. Parts mounted to boards in accordance with NEMA grade FR-4 of IPC-4101 (62mils thick) with no Cu carrier/heat-sink at a rated power of 2W (Board Therm. Res. ~ 72C/W).
4. Due to the complexity of managing the heat load of hundreds of pieces during qualification, long-term reliability testing for the 16W power rating had been conducted in terms of the equivalent current density via much thinner/narrower resistor patterns to limit the heat load. Full power testing was conducted on a smaller scale.

Marking:

	<p>Marking shall include:</p> <ul style="list-style-type: none"> ▪ Material Designator (A = Alumina) ▪ The 4-digit Resistance Value (MIL-STD-1285D) <p>Ex. A27R0 = 27.0Ω Resistance with Alumina Material</p>
-------------------------------------------------------------------------------------	----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------

Part Numbering: (Ex. CPA2512E27R0FS-T10)

CP	A	2512	E	27R0	F	S	-T10
Product Designator	Material Designator	Size, Inch	TCR	Resistance Value	Tolerance	Custom Designator	Packaging Tape & Reel
CP	A = Alumina	Refer to table above	E = ± 25 ppm/°C Q = ± 50 ppm/°C	Ex. 27R0 = 27.0 Ω	F = ± 1%	Standard = S Custom = TBD	-T10 = 1000 -T50 = 5000

Note: When requesting quotes or ordering parts, it is not necessary to add the T&R package quantity (-T##) to the end of the part number. This will be added by us based on the quantity ordered.

Looking for pricing, stock, or lifecycle information?

Click below to explore more details on WIN SOURCE:

 [View CPA2512E68R0FS-T10 on WIN SOURCE](#)

 [Susumu Information](#)

Optimize Your Supply Chain with WIN SOURCE Solutions

-  Global Sourcing Solution
-  Obsolete Management
-  Cost Control Management
-  Shortage Management
-  Alternative Solution
-  Excess Inventory Management