



**THE DATASHEET OF
S558-5500-JD**



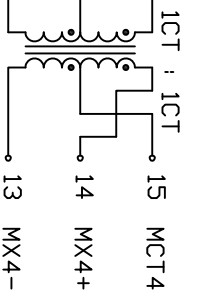
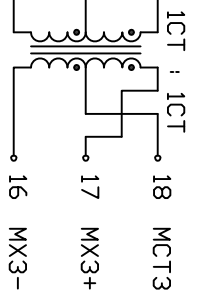
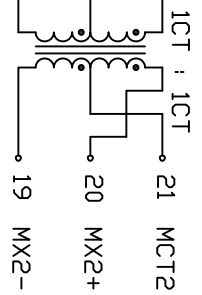
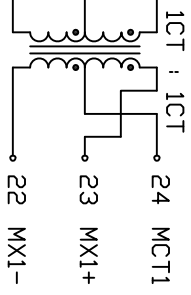
PRELIMINARY

ELECTRICAL CHARACTERISTICS @25°C



MATIC

LINE SIDE



TURNS RATIO
 (2-1-3) : (23-24-22)
 (5-4-6) : (20-21-19)
 (8-7-9) : (17-18-16)
 (11-10-12) : (14-15-13)

POLARITY

DCL

INTERWINDING CAPACITANCE, C_{WW}

@1MHz, 20_nV
 180_uH MIN WITH 8mA DC BIAS
 120_uH MIN WITH 19mA DC BIAS
 30pF MAX

PARAMETER	MAGNITUDE (IN DB)	FREQUENCY (IN MHZ)	
		START	END
INSERTION LOSS	SDD12 , SDD21 -0.004$(fMHz)>-0.4$ MAX	1	150
RETURN LOSS	SDD11 , SDD22 -20 MIN	1	40
ALINE NEAR END CROSSSTALK	SDDXY' -20+15LDGf/40MHz) MIN	40	150
NEAR END CROSSSTALK	SDDXY -46+7LDGf/125MHz) MIN	1	150
IMPEDANCE BALANCE	SDC11 -35 MIN	1	40
	SDC12 -34+15LDGf/40MHz) MIN	1	40
	SDC22 -42 MIN	40	150
MODE CONVERSION	SDC12 -38+19.2LDGf/50MHz) MIN	1	30
	SDC21 -35 MIN	1	150
	SDC21 -35+17LDGf/40MHz) MIN	1	40
COMMON MODE REJECTION	SDC21 -30 MIN	1	40
	SDC21 -30+11LDGf/40MHz) MIN	1	40
	SDC21 -25 MIN	1	150

BALANCED DC LINE CURRENT 720mA MAX @57VDC CONTINUOUS, 1.2A MAX @57VDC FOR 200ms
 HIPOT 2250 VDC
 OPERATING TEMPERATURE: -40 TO 85°C
 MEETS 25G PERFORMANCE REQUIREMENTS AS PER IEEE 802.3bz

PART NO. / DRAWING NO. S558-5500-JD	STANDARD DIM. [] METRIC DIM. AS REF.	TOL. IN INCH	UNIT : INCH [mm]
		.X	
		.XX	
		.XXX	
FILE NAME S5585500JDP.A.DWG	SCALE : N/A	SIZE : A4	

REV. : PA PAGE : 2

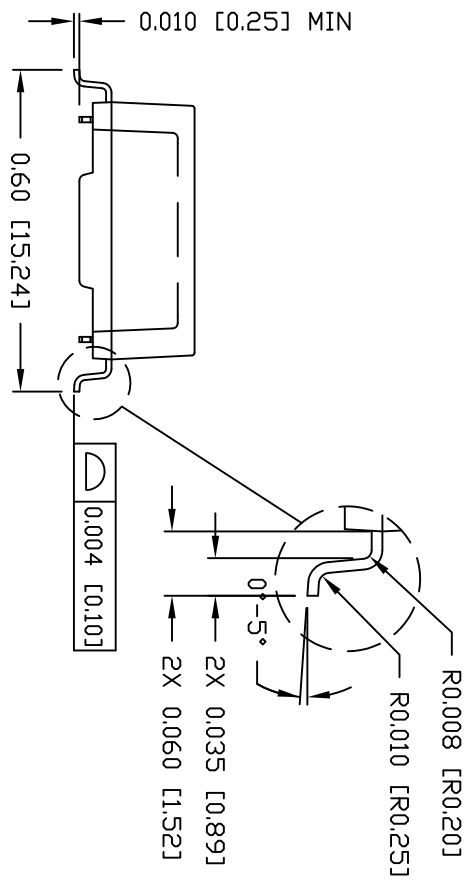
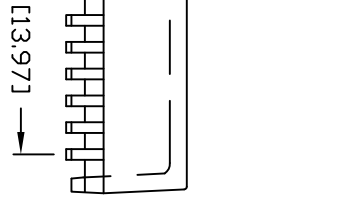
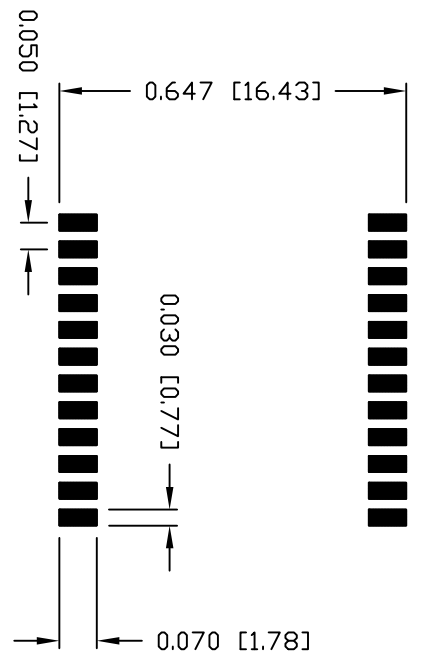
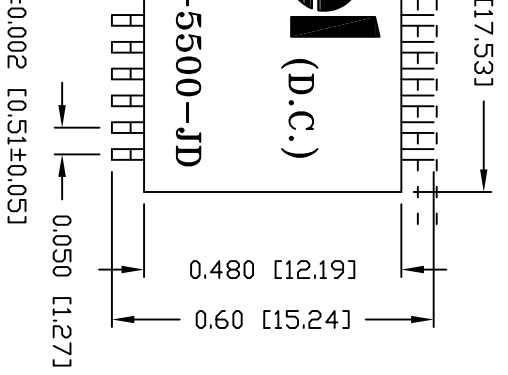
POWER | PROTECT | CONNECT

ELECTRICAL SPECIFICATION

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PRELIMINARY

SUGGESTED PCB PAD LAYOUT



TID DDC. HAND-WORK-04.



MECHANICAL OUTLINE		PART NO. / DRAWING NO. S558-5500-JD		STANDARD DIM. [] METRIC DIM. AS REF.		REV. : PA		PAGE : 3	
FILE NAME S5585500JDP.A.DWG		TOL. IN INCH .X .XX .XXX		UNIT : INCH [mm]		SCALE : N/A		SIZE : A4	

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