



**THE DATASHEET OF  
S1633B-50.0000(T)**



Supply Current, Standby Mode			10	$\mu$ A	Output Hi-Z
Frequency Stability			$\pm 20$ to $\pm 50$	ppm	See Note 1 below
Operating Temperature Range	-20		+70	$^{\circ}$ C	Commercial (standard)
	-40		+85		Industrial (standard)
Output Logic 0, $V_{OL}$			10% $V_{DD}$	V	
Output Logic 1, $V_{OH}$	90% $V_{DD}$			V	
Output Load			15	pF	See Note 2 below
Duty Cycle	45		55	%	Measured 50% $V_{DD}$
Rise and Fall Time	up to 32 MHz		7	ns	Measured 20/80% of waveform
	32 to 70 MHz		5		
	70 to 133 MHz		2.5		
Jitter, Phase	1 to 133 MHz		1	ps RMS (1- $\sigma$ )	10kHz to 20 MHz frequency band
Jitter, Accumulated	up to 70 MHz		5	ps RMS (1- $\sigma$ )	20,000 adjacent periods
	70 to 133 MHz		3		
Jitter, Total	up to 70 MHz		50	ps pk-pk	100,000 random periods
	70 to 133 MHz		30		

Notes:

1. Stability includes all combinations of operating temperature, load changes, rated input (supply) voltage changes, initial calibration tolerance (25 $^{\circ}$ C), aging (1 year at 25 $^{\circ}$ C average effective ambient temperature), shock and vibration.
2. For specifications other than those listed, please contact sales.



RMS Phase jitter or integrated phase jitter

RMS Period jitter

Total Period jitter (pk-pk)

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