



**THE DATASHEET OF
DMT4015LDV-7**



Product Summary

BV_{DSS}	$R_{DS(ON)}$ Max	I_D Max $T_c = +25^\circ\text{C}$
40V	20m Ω @ $V_{GS} = 10\text{V}$	21.2A
	25m Ω @ $V_{GS} = 4.5\text{V}$	19.1A

Description and Applications

This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

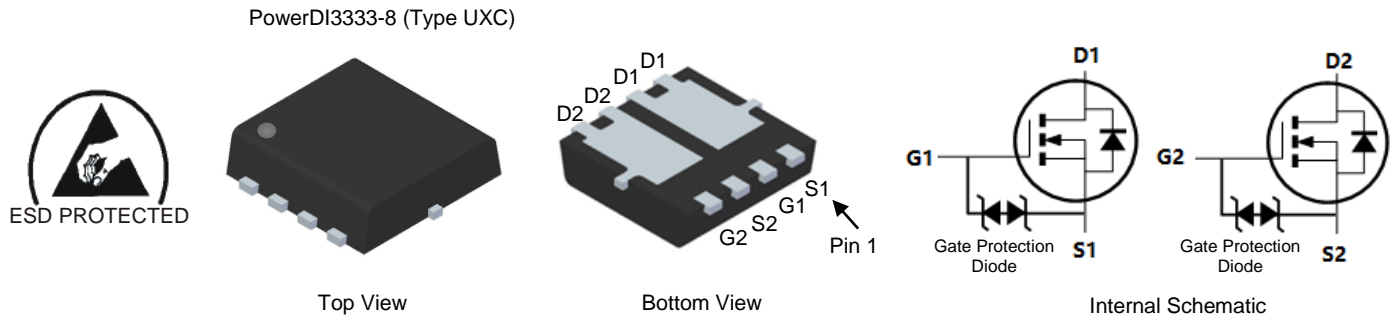
- Wireless Charging
- DC-DC Converters
- Power Management

Features and Benefits

- 100% Unclamped Inductive Switching (UIS) Test in Production — Ensures More Reliable and Robust End Application
- Low $R_{DS(ON)}$ — Ensures On-State Losses Are Minimized
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Gate
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

Mechanical Data

- Case: PowerDI[®]3333-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.072 grams (Approximate)

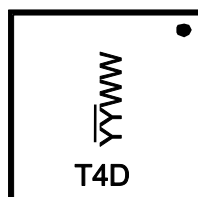


Ordering Information (Note 4)

Part Number	Case	Packaging
DMT4015LDV-7	PowerDI3333-8 (Type UXC)	2,000/Tape & Reel
DMT4015LDV-13	PowerDI3333-8 (Type UXC)	3,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



T4D = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Last Two Digits of Year (ex: 20 = 2020)
 WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	40	V		
Gate-Source Voltage	V _{GSS}	±16	V		
Continuous Drain Current (Note 6) V _{GS} = 10V	I _D	T _C = +25°C	21.2	A	
		T _C = +70°C	17.0		
Continuous Drain Current (Note 6) V _{GS} = 10V	I _D	Steady State	T _A = +25°C	7.8	A
			T _A = +70°C	6.3	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	50	A		
Maximum Continuous Body Diode Forward Current (Note 6)	I _S	2	A		
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)	I _{SM}	50	A		
Avalanche Current, L = 0.1mH	I _{AS}	20.7	A		
Avalanche Energy, L = 0.1mH	E _{AS}	21.4	mJ		

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	1.1	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	110.6	°C/W
Total Power Dissipation (Note 6)	P _D	2.0	W
Thermal Resistance, Junction to Ambient (Note 6)	R _{θJA}	61.2	°C/W
Thermal Resistance, Junction to Case (Note 6)	R _{θJC}	8.3	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	40	—	—	V	V _{GS} = 0V, I _D = 1mA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	µA	V _{DS} = 40V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±10	µA	V _{GS} = ±16V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.75	—	2.5	V	V _{DS} = V _{GS} , I _D = 250µA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	15.3	20	mΩ	V _{GS} = 10V, I _D = 8A
		—	20.1	25		V _{GS} = 4.5V, I _D = 4A
Diode Forward Voltage	V _{SD}	—	0.7	1.2	V	V _{GS} = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	808	—	pF	V _{DS} = 30V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{oss}	—	279	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	30	—	pF	
Gate Resistance	R _g	—	1.4	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	8.6	—	nC	V _{DS} = 30V, I _D = 10A
Total Gate Charge (V _{GS} = 10V)	Q _g	—	15.7	—	nC	
Gate-Source Charge	Q _{gs}	—	2.9	—	nC	
Gate-Drain Charge	Q _{gd}	—	3.4	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	7.5	—	ns	V _{GS} = 10V, V _{DS} = 30V, R _g = 6Ω, I _D = 10A
Turn-On Rise Time	t _r	—	2.7	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	17.4	—	ns	
Turn-Off Fall Time	t _f	—	8.9	—	ns	
Body Diode Reverse Recovery Time	t _{RR}	—	26.7	—	ns	I _F = 10A, di/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q _{RR}	—	16.2	—	nC	

- Notes:
- Device mounted on FR-4 PCB, with minimum recommended pad layout, single sided.
 - Device mounted on FR-4 substrate PCB, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

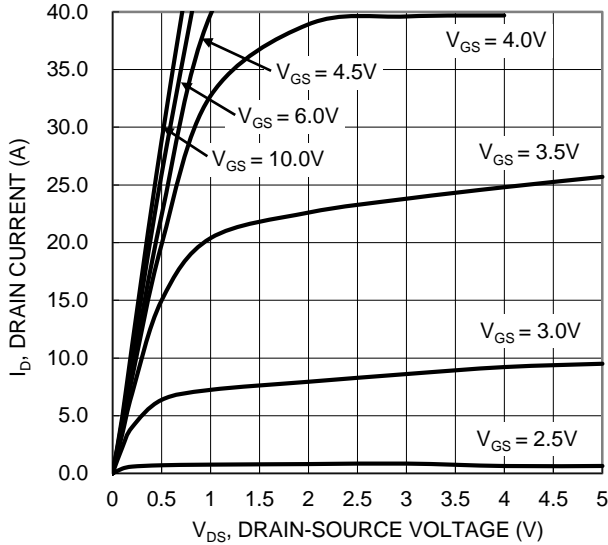


Figure 1. Typical Output Characteristic

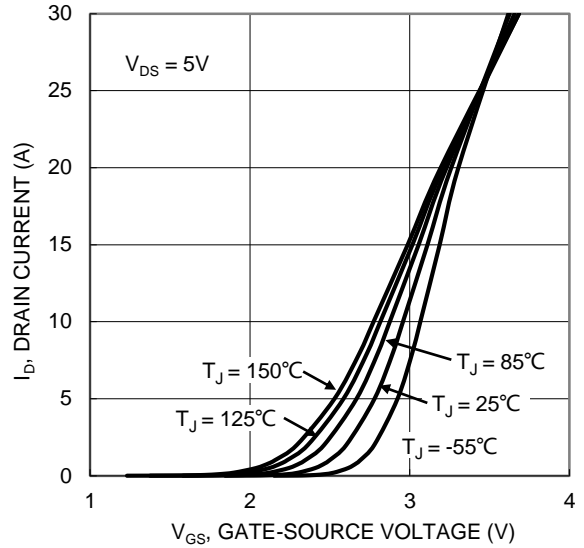


Figure 2. Typical Transfer Characteristic

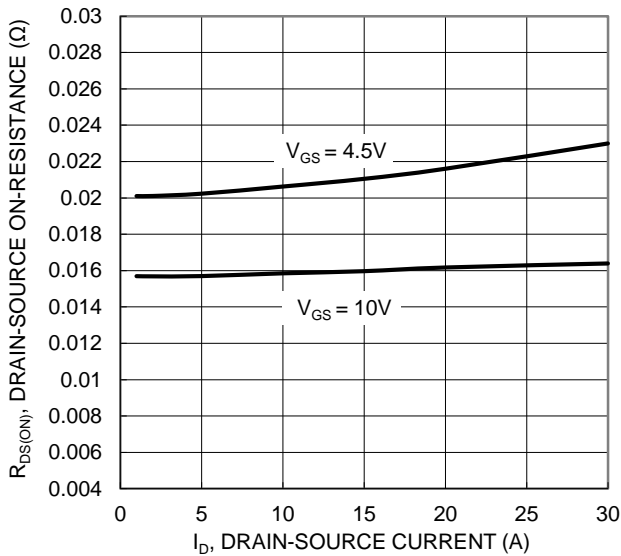


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

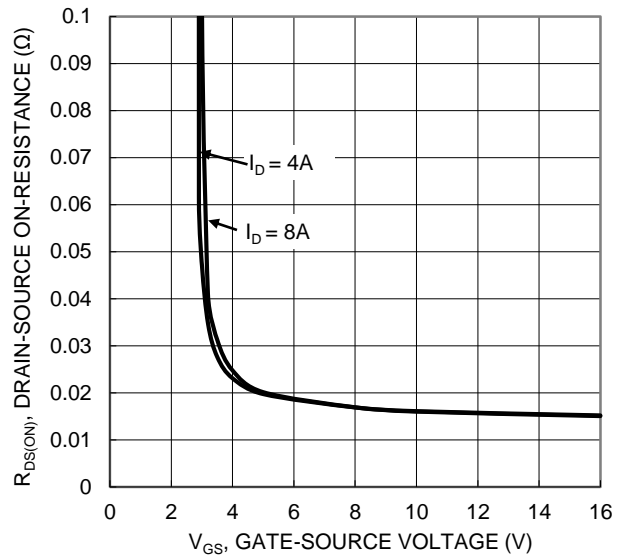


Figure 4. Typical Transfer Characteristic

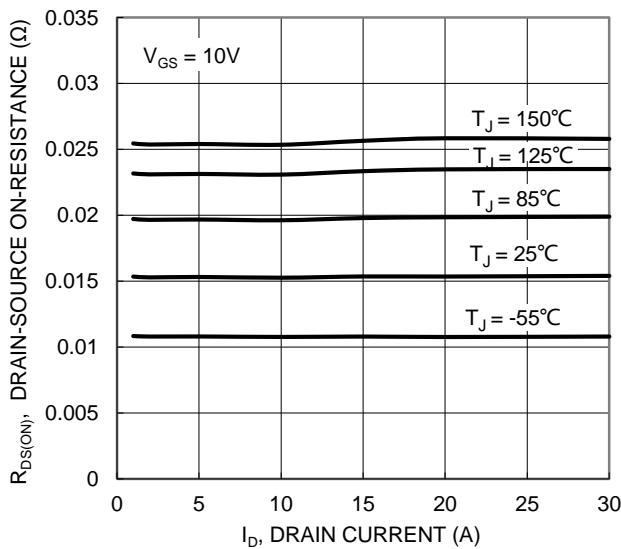


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

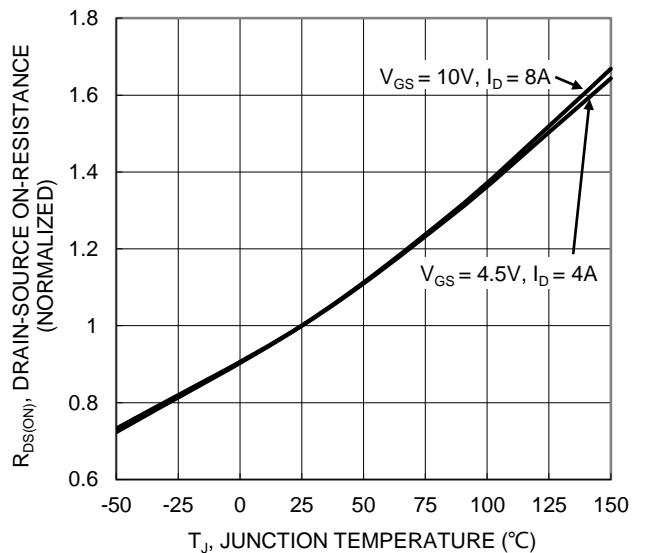


Figure 6. On-Resistance Variation with Temperature

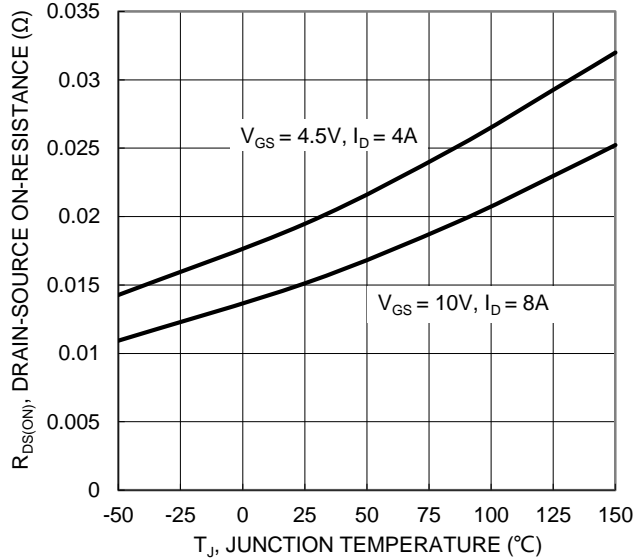


Figure 7. On-Resistance Variation with Temperature

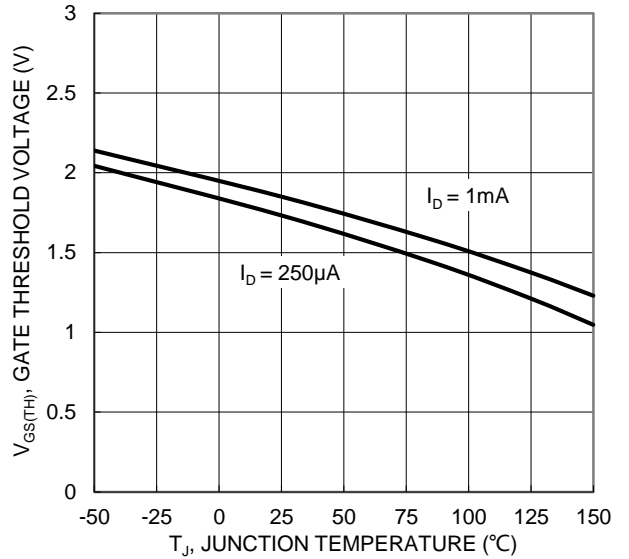


Figure 8. Gate Threshold Variation vs. Junction Temperature

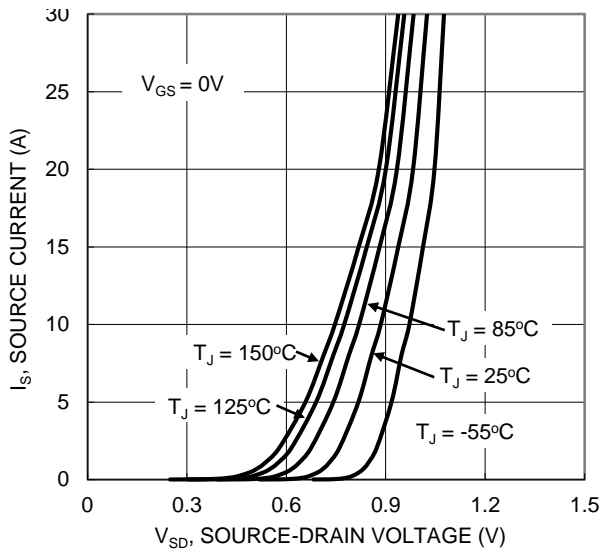


Figure 9. Diode Forward Voltage vs. Current

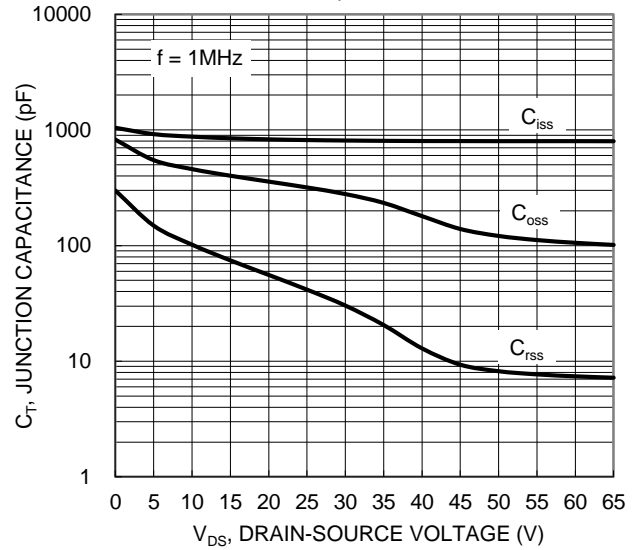


Figure 10. Typical Junction Capacitance

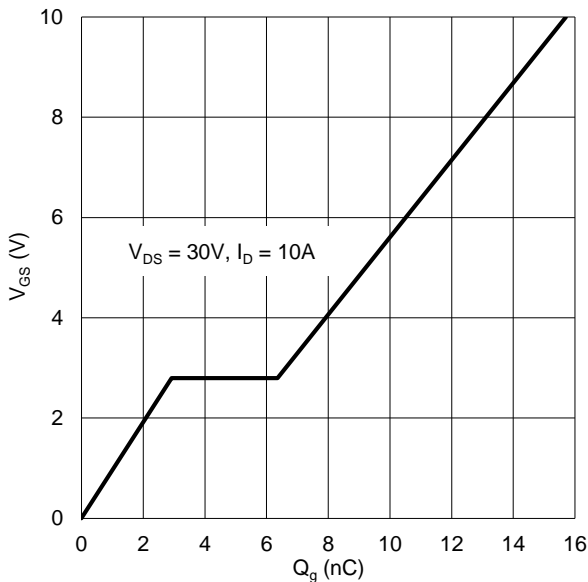


Figure 11. Gate Charge

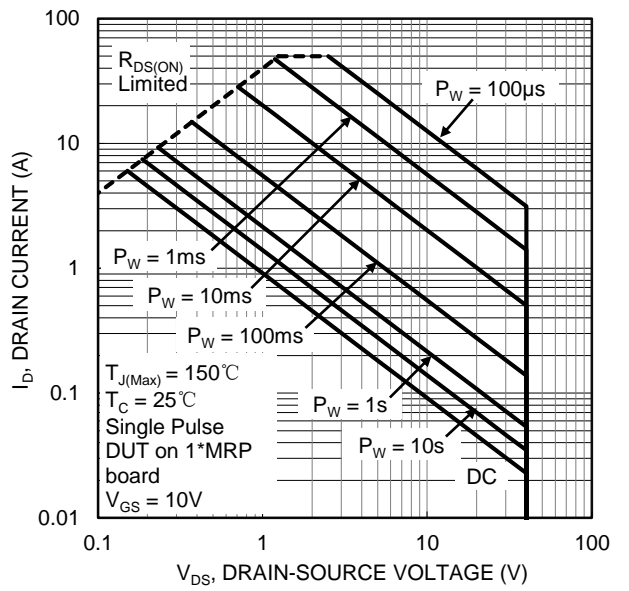


Figure 12. SOA, Safe Operation Area

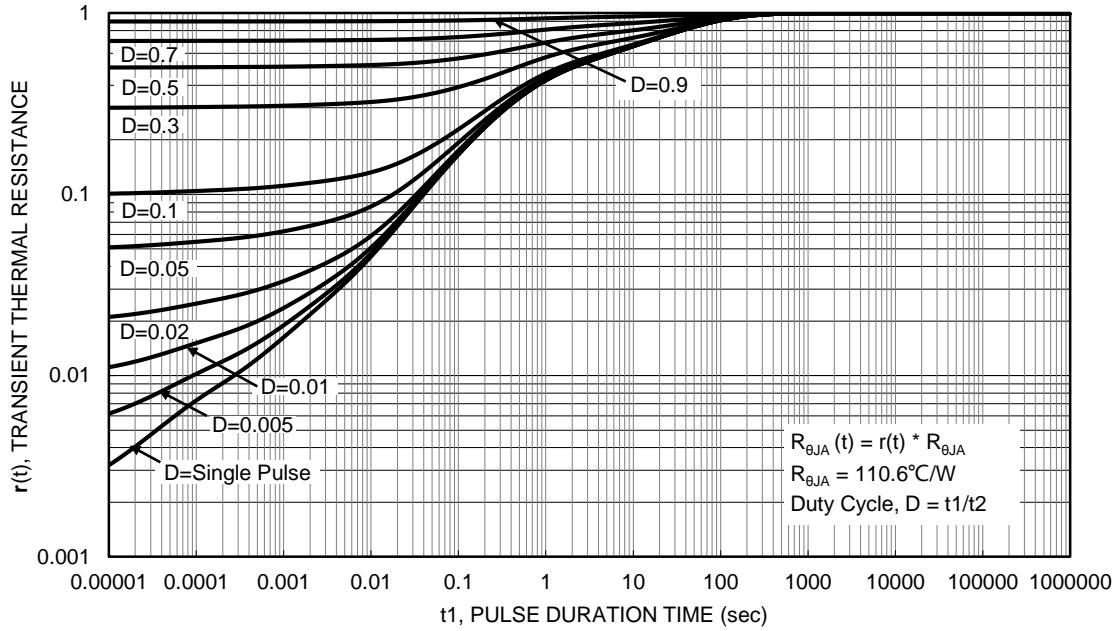
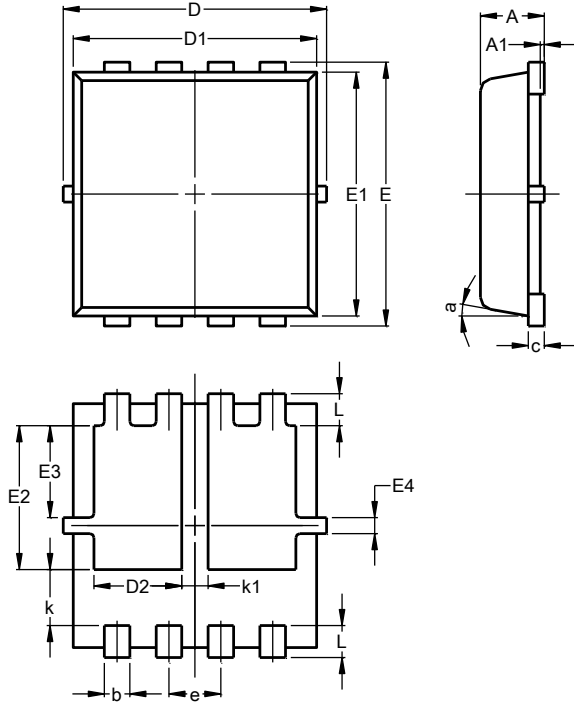


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI3333-8 (Type UXC)

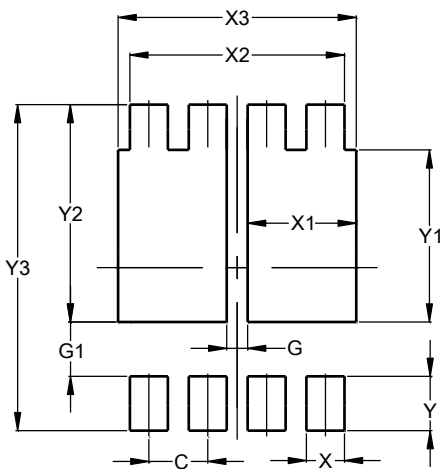


PowerDI3333-8 (Type UXC)			
Dim	Min	Max	Typ
A	0.75	0.85	0.80
A1	0.00	0.05	--
b	0.25	0.40	0.32
c	0.10	0.25	0.15
D	3.20	3.40	3.30
D1	2.95	3.15	3.05
D2	0.90	1.30	1.10
E	3.20	3.40	3.30
E1	2.95	3.15	3.05
E2	1.60	2.00	1.80
E3	0.95	1.35	1.15
E4	0.10	0.30	0.20
e	--	--	0.65
L	0.30	0.50	0.40
k	0.50	0.90	0.70
k1	0.13	0.53	0.33
a	0°	12°	10°
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI3333-8 (Type UXC)



Dimensions	Value (in mm)
C	0.650
G	0.230
G1	0.600
X	0.420
X1	1.200
X2	2.370
X3	2.630
Y	0.600
Y1	1.900
Y2	2.400
Y3	3.600

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